

Pioneer

Service Manual

SERVICE GUIDE

ORDER NO.

RRV2055

COMPACT DISC RECORDER

PDR-555RW

PDR-V500

PDR-19RW

PDR-509

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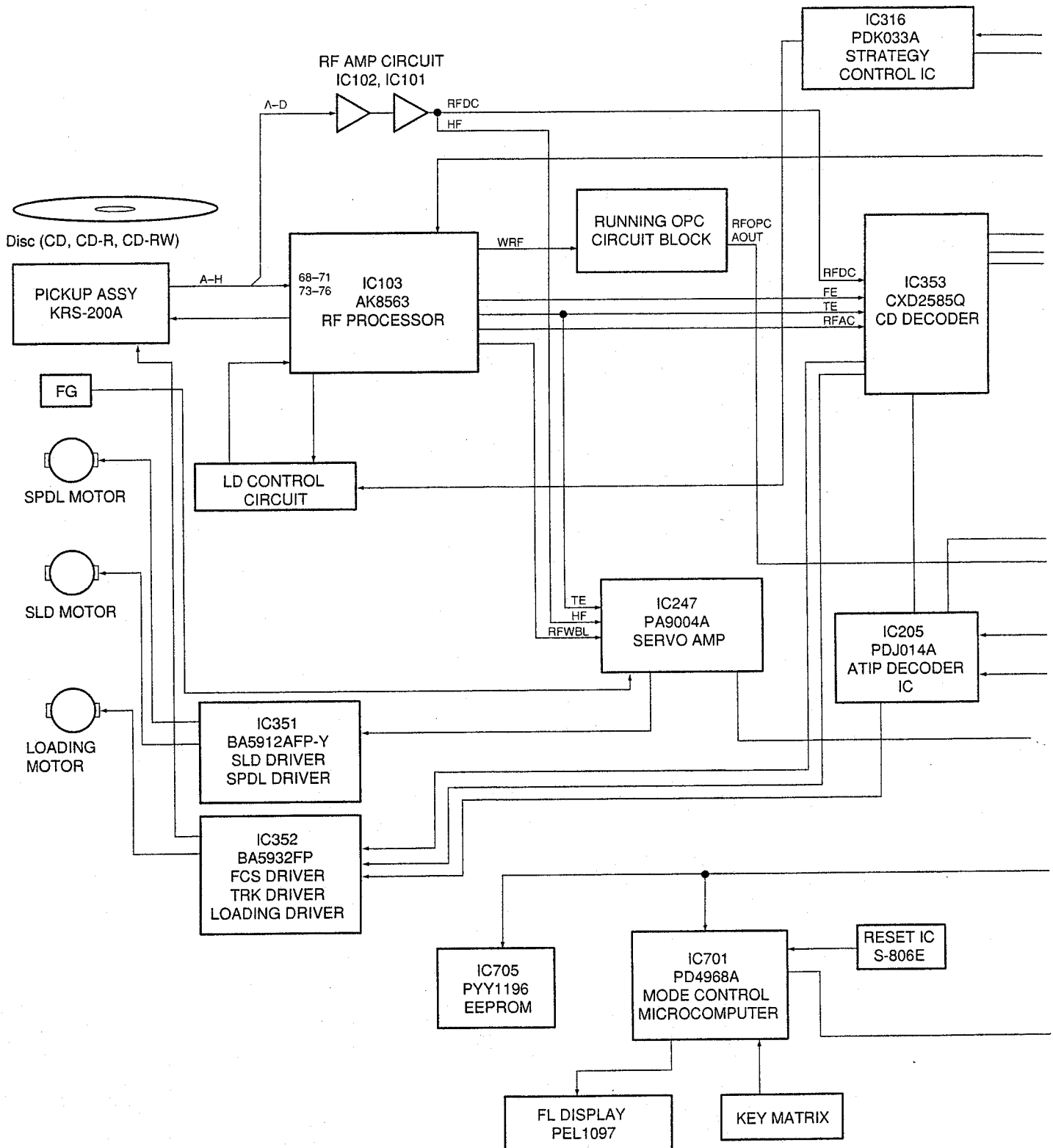
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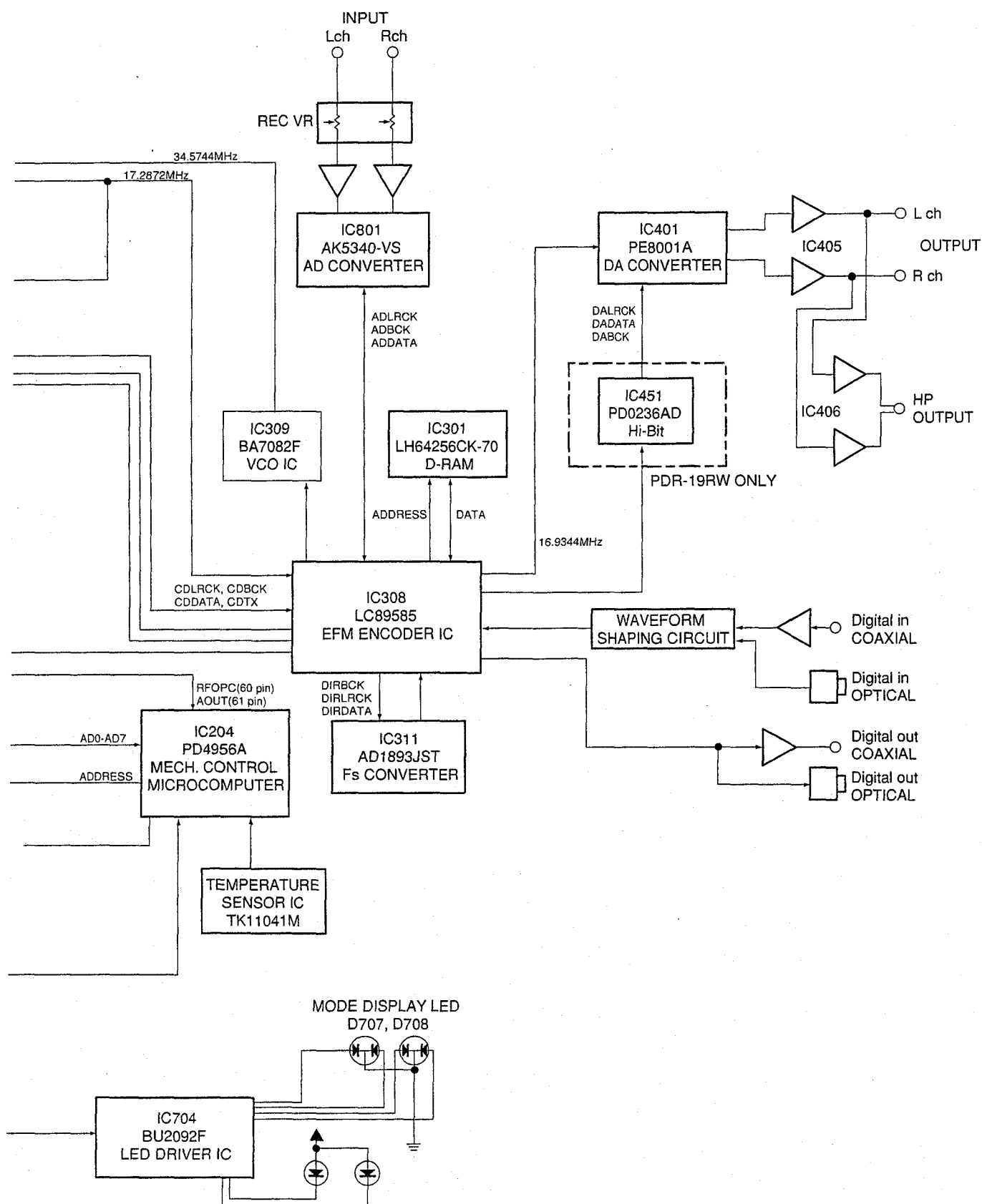
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1. BLOCK DIAGRAM

1.1 PDR-555RW, PDR-V500 AND PDR-19RW

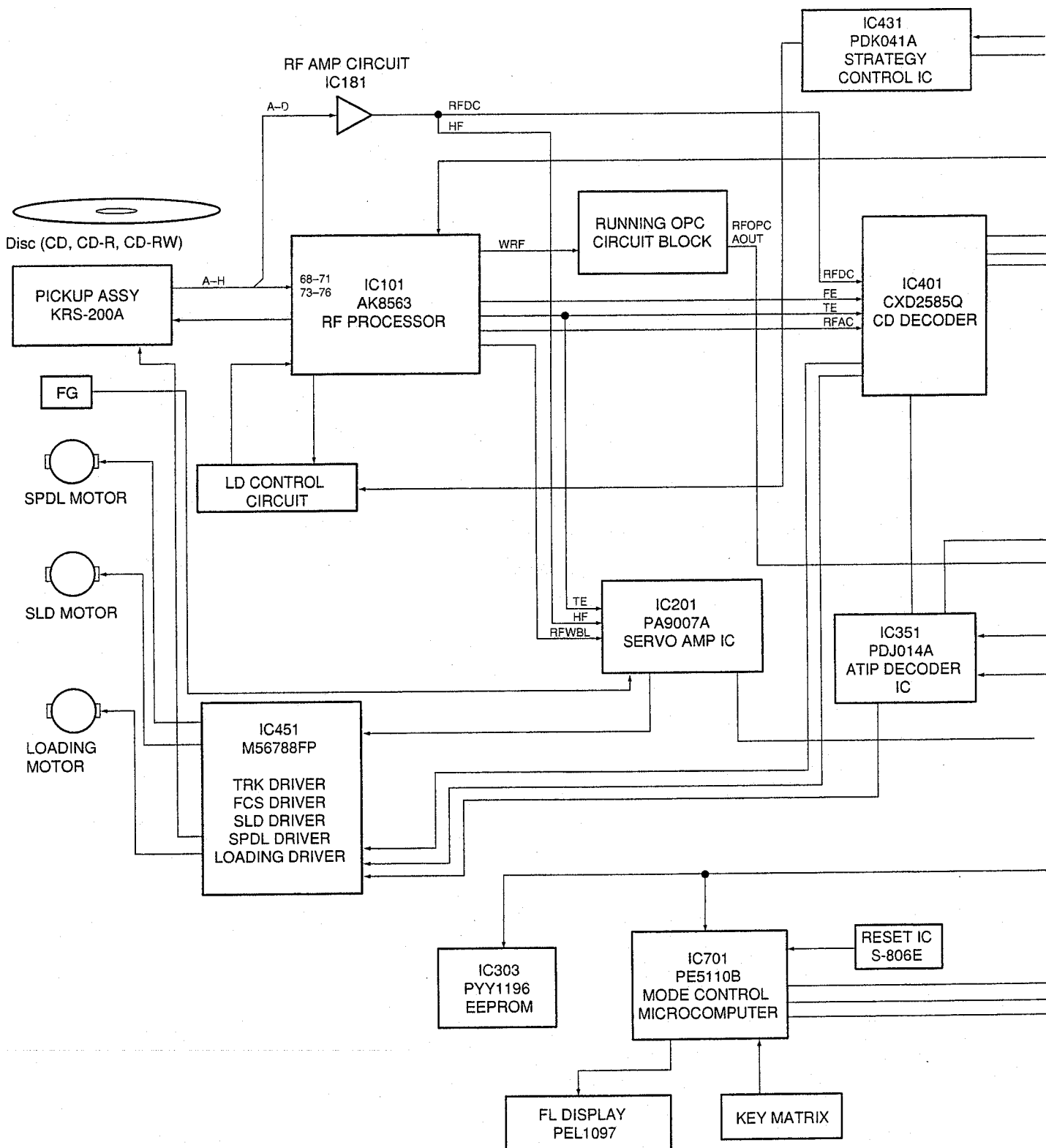


PDR-555RW, PDR-V500, PDR-19RW, PDR-509

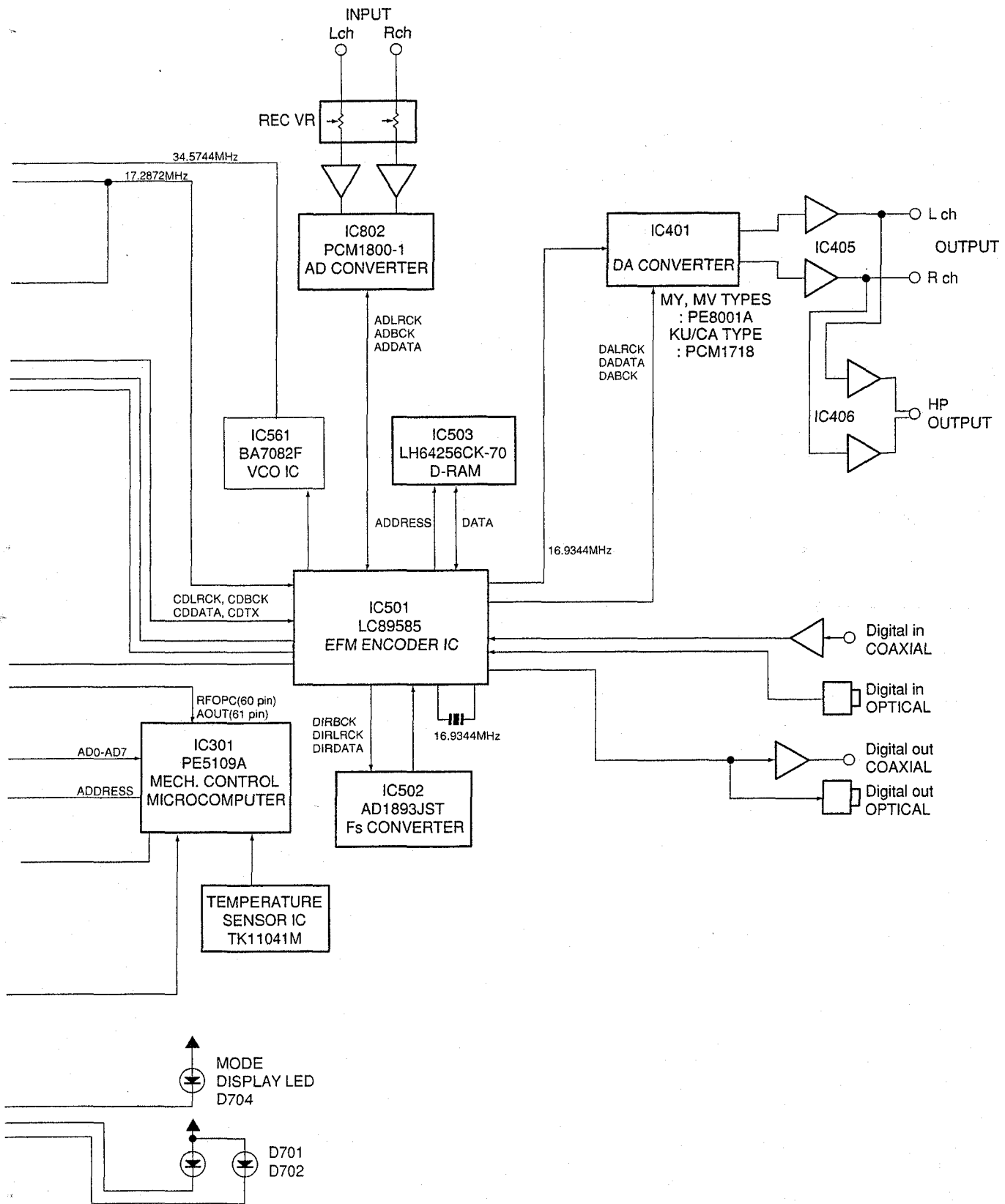


PDR-555RW, PDR-V500, PDR-19RW, PDR-509

1.2 PDR-509



PDR-555RW, PDR-V500, PDR-19RW, PDR-509



2. PRODUCT DESCRIPTIONS

The PDR-555RW series (PDR-555RW, PDR-V500 and PDR-19RW) is the first series of CD recorders from PIONEER that supports recording and erasing of CD-RW discs. Basic operations with CDs and CD-Rs with this series are based on those of the CD recorders of the PDR-05 series.

The main differences from the PDR-05 series concerning the circuits are:

- The pickup is changed.
- The circuit in RF amplifier is changed.
- The LD drive circuit (including the strategy control circuit) is changed.
- A running OPC circuit is added.
- The focus servo, tracking servo and sled servo are digitized.
- The driver IC is changed.
- The CD decoder IC is changed.
- The sampling rate converter IC is changed.
- The DA converter is changed.

Also, the circuits of the CD recorders of the PDR-509 series are based on those of the PDR-555RW series. So the main circuits used in the PDR-509 series are equivalent to those of the PDR-555RW series. But as an exception, AD converter is changed to the AK5340-VS from the PCM1800-1.

3. PORT TABLE OF MICROCOMPUTER

• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

3.1 MODE CONTROL OF PDR-555RW, PDR-V500 AND PDR-19RW

■ PD4968A (FUNCTION ASSY : IC701)

• Mode Control IC

No.	Mark	Pin Name	I/O	Pin Function
1	FIP6	GRID 6	O	FL grid output 5
2	FIP5	GRID 5	O	FL grid output 6
3	FIP4	GRID 4	O	FL grid output 7
4	FIP3	GRID 3	O	FL grid output 8
5	FIP2	GRID 2	O	FL grid output 9
6	FIP1	GRID 1	O	FL grid output 10
7	FIP0	GRID 0	O	FL grid output 11
8	VDD	-	-	Connect to VDD
9	SCOK	RSCK	O	Serial clock for JIG communication
10	SO0	RSO	O	Serial output for JIG communication
11	SI0	RSI	I	Serial input for JIG communication
12	P24	XTAL	O	XTAL ON/OFF (At digital selection without FS converter : L)
13	P23	XEVCO	O	Encoder VCO ON/OFF (At CD : H)
14	SCK1	FSCK	I/O	Serial clock of the mechanism controller LSI
15	SO1	FSO	O	Serial output of the mechanism controller LSI
16	SI1	FSI	I	Serial input of the mechanism controller LSI
17	RESET	XRESET	O	Reset input of the mode controller
18	P74	LDATA	O	Communication data output for LED driver
19	P73	LCLOCK	O	Communication data input for LED driver
20	AVSS	GND	I	Connect to VDD
21	P17	XFUSE	O	During use the serial communication between the mode controller and LC89585 (During use : L)
22	P16	LCK	O	Communication latch output for LED driver
23	P15	XVCO	O	PLL ON/OFF (For SRC ON/OFF SRC OFF: L)
24	P14	FS_THR	O	SRC through output
25	P13	DACLAT	O	Communication latch output for D/A converter
26	P12	XRST	O	Reset output for mechanism controller and ATIP decoder (H: release the reset)
27	P11	XOPT	O	Optical input selection (At optical input selection : L)
28	P10	-	O	Not used (A/D input)
29	AVDD	VDD	-	Connect to VDD
30	AVREF	VDD	-	Connect to VDD
31	P04	ROT_DI	I	For judgement of the rotary encoder SW direction
32	XT2	-	O	Not used
33	VSS	GND	-	Connect to GND
34	X1	-	I	System oscillation 4.19MHz
35	X2	-	O	
36	P37	SW1	I	Demo mode ON/OFF L: Demo display exist
37	P36	MODEL_0	I	
38	P35	MODEL_1	I	Model switching pin
39	P34	MODEL_2	I	
40	P33	RREQ	O	CE output for JIG communication

3.2 MECHANISM CONTROL OF PDR-555RW, PDR-V500 AND PDR-19RW

■ PD4956B (SERVO DIGITAL ASSY : IC204)

• Mechanism Control IC

No.	Mark	Pin Name	I/O	Pin Function
41	P32	MACK	O	Communication response for mechanism controller
42	P31	LREQ	O	CE signal for LC89585
43	P30	UNLOCK	I	Digital unlock detection
44	INTP3	POT_INT	I	Rotary encoder SW operation detection (↓ interrupt)
45	INTP2	XPFAIL	I	Power down detection
46	INTP1	MREQ	I	Mechanism controller communication request (interrupt)
47	INTP0	REMIN	I	Remote control input (interrupt)
48	IC	VPP	I	Connect to GND
49	P72	ISEL3	I	Input selector rotary SW input 3 (H: Analog selection)
50	P71	ISEL2	I	Input selector rotary SW input 2 (H: Optical selection)
51	P70	ISEL1	I	Input selector rotary SW input 1 (H: Coaxial selection)
52	VDD	VDD	-	Connect to VDD
53	P127	SCAN4	O	Key matrix output 4
54	P126	SCAN3	O	Key matrix output 3
55	P125	SCAN2	O	Key matrix output 2
56	P124	SCAN1	O	Key matrix output 1
57	P123	SCAN0	O	Key matrix output 0
58	P122	KEYIN3	I	Key matrix input 3
59	P121	KEYIN2	I	Key matrix input 2
60	P120	KEYIN1	I	Key matrix input 1
61	P117	KEYIN0	I	Key matrix input 0
62	P116	ATT_0V	I	
63	P115	AATLAT	O	
64	P114	FINL_SEG	O	"FINALIZE" segment output (At lights up: H)
65	P113	SEG 10	O	FL segment output 10
66	P112	SEG 9	O	FL segment output 9
67	P111	SEG 8	O	FL segment output 8
68	P110	SEG 7	O	FL segment output 7
69	P107	SEG 6	O	FL segment output 6
70	P106	SEG 5	O	FL segment output 5
71	VLOAD	VLOAD	-	VLOAD
72	P105	SEG 4	O	FL segment output 4
73	P104	SEG 3	O	FL segment output 3
74	P103	SEG 2	O	FL segment output 2
75	P102	SEG 1	O	FL segment output 1
76	P101	SEG 0	O	FL segment output 0
77	P100	GRID10	O	FL grid output 10
78	FIP9	GRID 9	O	FL grid output 9
79	FIP8	GRID 8	O	FL grid output 8
80	FIP7	GRID 7	O	FL grid output 7

No.	Mark	Pin Name	I/O	Pin Function
1	P32/XCLK0/SCL	MSCK	O(I)	Serial transfer clock output of clock synchronous system
2	P33/SO0/SDA	MSO	O(I)	Serial transfer data output of clock synchronous system
3	P34/TO0	-	O	Not used
4	P35/TO1	STCN0	O	Outputs for strategy adjustment (3T delay + 30 nsec)
5	P36/TO2	FOK	I	FOCUS OK input (H: FOCUS OK)
6	P37/TO3	LRST	O	RESET output for the servo and digital system ICs (L: Reset)
7	XRESET	XRESET	I	RESET input (L: Reset)
8	VDD1	+V5	-	Positive power supply excepting port section
9	X2	CLOCK	I	Crystal input for system clock (32MHz)
10	X1	CLOCK	-	Crystal output for system clock (32MHz)
11	VSS1	GND	-	GND excepting port section
12	P00	XECE	O	Enable output for reading the jig for test
13	P01	RECE	O	Laser diode recording power ON/OFF ON: H
14	P02	NC	O	Not used
15	P03	NC	O	Not used
16	P04	IT5SEL	O	Input switch of INTP5 pin (H: SENS, L: TOCP)
17	P05	XENCE	O	External sync enable output of LC89585
18	P06	XASYN	O	ATIP frame sync
19	P07	XENCE	O(I)	Serial enable output of LC89585
20	P67/XREFRQ/HLDK	CLV	O	Spindle servo CLV/CAV mode
21	P66/XWAIT/HLDRQ	ECLV	O	Spindle servo EFM/Wobble mode
22	P65/XWR	XWR	O	Strobe signal output for READ operation of the external memory
23	P64/XRD	XRD	O	Strobe signal output for WRITE operation of the external memory
24	P63/A19	XLT	O	Latch output of CXD2585Q command
25	P62/A18	SSCK	O	Serial clock output for CXD2585Q command
26	P61/A17	SSO	O	Serial data output for CXD2585Q command
27	P60/A16	ALAT	O	Latch output for AK8563 command
28	P57/A15	SCLK	O	Serial clock output for serial readout of CXD2585Q
29	P56/A14	TP_2P	O	Test pin
30	P55/A13	TP_1P	O	
31	P54/A12	LDPW4	O	Recording laser power output setting
32	P53/A11	LDPW3	O	
33	P52/A10	LDPW2	O	
34	P51/A9	LDPW1	O	
35	P50/A8	LDPW0	O	Data address line
36	P47/AD7	AD7	O	
37	P46/AD6	AD6	O	
38	P45/AD5	AD5	O	
39	P44/AD4	AD4	O	
40	P43/AD3	AD3	O	

■ PDJ014A (SERVO DIGITAL ASSY: IC205)

External port (External RAM domain (2C000H to 2C0FFH))

No.	Mark	Pin Name	I/O	Pin Function
41	P42/AD2	AD2	O	Data address line
42	P41/AD1	AD1		
43	P40/AD0	AD0		
44	ASTB/CLKOUT	ASTB	O	External latch signal of lower address signal for external memory access
45	Vss0	GND	–	GND of port section
46	TEST	GND	–	Connect to Vss0
47	P10/PWM0	SPSP	O	Spindle drive PWM output in the Spindle CAV
48	P11/PWM1	DGAI	O	In the PLAY or REC mode, it becomes "L" for outer periphery from 18 minutes of the CD and 12cm CD-R, and "H" for outer periphery from 9 minutes of the 8cm CD-R.
49	P12/ASCK2/XSCK2	SQCK	O	Serial clock output for sub-Q of CXD2585Q
50	P13/RXD2/SI2	SQSI	I	Serial data input for sub-Q of CXD2585Q
51	P14/TXD2/SO2	SO2	O	Serial data output
52	P15	MREQ	O	Serial hand shake output to the mode controller
53	P16	D8CM	O	8cm CD-R disc 8cm: H
54	P17	NC	O	Not used
55	VDD0	+5V	–	Positive power supply of port section
56	P70/ANI0	TEPP	I(A)	Tracking error peak to peak (for tracking gain adjustment)
57	P71/ANI1	RFT	I(A)	A/D input of upper side envelope of Playback RF
58	P72/ANI2	RFB	I(A)	A/D input of lower side envelope of Playback RF
59	P73/ANI3	TEMP	I(A)	A/D input of temperature sensor
60	P74/ANI4	RFOPC	I(A)	A/D input of RFOPC/MPXOUT
61	P75/ANI5	VWDC2	I(A)	A/D input for strategy adjustment
62	P76/ANI6	TRAY	I(A)	A/D input of loading position
63	P77/ANI7	AD7	I(A)	Not used
64	AVDD	+5V	–	Positive power supply for A/D converter
65	AVREF1	+5V	–	Reference voltage input for A/D converter
66	AVSS	GND	–	GND for A/D converter
67	ANO0	WREF	O(A)	D/A output for recording APC reference
68	ANO1	VWDC2R	O(A)	D/A output for strategy adjustment
69	AVREF2	+5V	–	Reference voltage for D/A converter
70	AVREF3	GND	–	Reference voltage for D/A converter
71	P20/NMI	XPFAIL	I	Power failure detection AT power failure: falling edge
72	P21/INTP0	FG	I	Spindle FG input
73	P22/INTP1	ATIP	I	ATIP SYNC input
74	P23/INTP2/C1	SCOR	I	Frame sync of CXD2585Q
75	P24/INTP3	SUBSYNC	I	Frame sync of LC89585
76	P25/INTP4/ASCK/XSCK1	XRFDT	I	EFM playback RF detection
77	P26/INTP5	ITSIN	I	TOC position sensor (TOC position: L), SENS signal input of CXD2585Q
78	P27/SI0	MSI	I	Serial transfer data input of the clock sync. system
79	P30/RXD/SI1	MACK	I	Serial hand shake input to the mode controller
80	P31/TXD/SO1	XFUSE	I	Signal which is during communication between LC89585 and the mode controller

Note: (A) in item I/O shows "ANALOG".

No.	Mark	Pin Name	I/O	Pin Function
45	POA0	GAINUP1	O	Gain switch for CD-RW (CD-RW: H)
46	POA1	GAINUP2	O	APC circuit control signal for CD-R running OPC
47	POA2	GAINUP3	O	
48	GND	–	–	GND
49	POA3	ROPCL	O	ANI4 input switch (H: RFOPC, L: MPXOUT)
50	POA4	PHYERS	O	Physical Erase
51	POA5	SSEL	O	Tracking envelope detecting reset signal
52	POA6	AGCON	O	AGC circuit ON/OFF for Wobble extraction
53	POA7	LJUNP	O	N track jump
54	POB0	LOUT	O	Loading open
55	POB1	LIN	O	Loading close
56	POB2	KOK	O	Optical axis switching circuit ON/OFF
57	POB3	EECS	O	Enable output for writing and reading the EEPROM data
58	Vcc	–	–	+5V
59	POB4	STCN4	O	Strategy control output
60	POB5	STCN3	O	
61	POB6	STCN2	O	
62	POB7	STCN1	O	
63	POC0	TEG2	O	Tracking error amplifier gain adjustment
64	POC1	TEG1	O	
65	POC2	TEG0	O	
66	POC3	RW/XR	O	Switch the CD-RW/Other
67	POC4	–	–	Not used
68	GND	–	–	GND
69	POC5	XCD	O	Switch the CD/Other
70	POC6	ENBL	O	LD ON/OFF output
71	POC7	XAMUTE	O	Audio last stage mute

3.3 MODE CONTROL OF PDR-509

■ PE5110B (FUNCTION ASSY : IC701)

• Mode Control IC

No.	Mark	Pin Name	I/O	Pin Function
1	FIP6	GRID 6	O	FL grid output 5
2	FIP5	GRID 5	O	FL grid output 6
3	FIP4	GRID 4	O	FL grid output 7
4	FIP3	GRID 3	O	FL grid output 8
5	FIP2	GRID 2	O	FL grid output 9
6	FIP1	GRID 1	O	FL grid output 10
7	FIP0	GRID 0	O	FL grid output 11
8	VDD	-	-	Connect to VDD
9	SCOK	-	O	Not used "L" outputs
10	SO0	-	O	Not used "L" outputs
11	SIO	-	O	Not used "L" outputs
12	P24	XTAL	O	XTAL ON/OFF (At digital selection without FS converter : L)
13	P23	XEVCO	O	Encoder VCO ON/OFF (At CD : H)
14	SCK1	FSCK	I/O	Serial clock of the mechanism controller LSI
15	SO1	FSO	O	Serial output of the mechanism controller LSI
16	SI	FSI	I	Serial input of the mechanism controller LSI
17	RESET	XRESET	I	Reset input of the mode controller (L : Reset)
18	P74	DISP_L	O	"DISP OFF" LED lights up output (L: lights up)
19	P73	LCLOCK	O	"AUTO/MANUAL" LED lights up output (L: lights up)
20	AVSS	GND	I	Connect to GND
21	P17	XFUSE	O	During use the serial communication between the mode controller and LC89585 (During use : L)
22	P16	CENT_L	O	"CENTER" LED lights up output (L : lights up)
23	P15	XVCO	O	PLL ON/OFF (At digital selection without FS converter : L (PLL oscillation))
24	P14	FS_THR	O	FS through output (Digital input at FS through ON and 44.1kHz : L)
25	P13	DACLAT	O	Communication latch output for D/A converter
26	P12	XRST	O	Reset output for mechanism controller and ATIP decoder (L: reset)
27	P11	XOPT	O	Optical input selection (At optical input selection : L)
28	P10	-	O	Not used "L" outputs (prepare the parallel remote control key input)
29	AVDD	VDD	-	Connect to VDD
30	AVREF	VDD	-	Connect to VDD
31	P04	-	-	
32	XT2	-	O	Not used
33	VSS	GND	-	Connect to VDD
34	X1	-	I	
35	X2	-	O	System oscillation 4.19MHz
36	P37	SW1	I	Demo mode ON/OFF (H fixed: No demo mode)
37	P36	FS_SW	I	FS through ON/OFF switching input (H: FS through)
38	P35	HIB_SW	I	Hi-bit mode ON/OFF switching input (H: Hi-bit)
39	P34	LGT_SW	I	LEGATO ON/OFF switching input (H: LEGATO ON)
40	P33	RREQ	O	CE output for jig communication

No.	Mark	Pin Name	I/O	Pin Function
41	P32	MACK	O	Communication response for mechanism controller (H to L: communication permission) (L to H: Communication end)
42	P31	LREQ	O	CE signal for LC89585 (L: Enable)
43	P30	UNLOCK	I	Digital unlock detection
44	INTP3	POT_INT	I	Rotary encoder SW operation detection (↓ interrupt)
45	INTP2	XPFAIL	I	Power down detection (L: power down)
46	INTP1	MREQ	I	Mechanism controller communication request (interrupt)
47	INTP0	REMIN	I	Remote control input (interrupt)
48	IC	VPP	I	Connect to GND
49	P72	ROT3	I	Not used "L" outputs
50	P71	ROT2	I	"H" outputs when playing the CD/CD-R/CD-RW discs in the Hi-bit mode
51	P70	ROT1	I	Rotary encoder SW direction judgment input
52	VDD	VDD	-	Connect to VDD
53	P127	SCAN4	O	Key matrix output 4
54	P126	SCAN3	O	Key matrix output 3
55	P125	SCAN2	O	Key matrix output 2
56	P124	SCAN1	O	Key matrix output 1
57	P123	SCAN0	O	Key matrix output 0
58	P122	KEYIN3	I	Key matrix input 3
59	P121	KEYIN2	I	Key matrix input 2
60	P120	KEYIN1	I	Key matrix input 1
61	P117	KEYIN0	I	Key matrix input 0
62	P116	-	O	Not used "L" outputs
63	P115	SCMS	O	Prepare the mode switch ("L" outputs)
64	P114	FINL_SEG	O	FINALIZE-segment output (At lights up: H)
65	P113	SEG 10	O	FL segment output 10
66	P112	SEG 9	O	FL segment output 9
67	P111	SEG 8	O	FL segment output 8
68	P110	SEG 7	O	FL segment output 7
69	P107	SEG 6	O	FL segment output 6
70	P106	SEG 5	O	FL segment output 5
71	VLOAD	-	-	VLOAD
72	P105	SEG 4	O	FL segment output 4
73	P104	SEG 3	O	FL segment output 3
74	P103	SEG 2	O	FL segment output 2
75	P102	SEG 1	O	FL segment output 1
76	P101	SEG 0	O	FL segment output 0
77	P100	GRID10	O	FL grid output 10
78	FIP9	GRID 9	O	FL grid output 9
79	FIP8	GRID 8	O	FL grid output 8
80	FIP7	GRID 7	O	FL grid output 7

3.4 MECHANISM CONTROL OF PDR-509

■ PE5109A (CD-R CORE ASSY : IC301)

• Mechanism Control IC

No.	Mark	Pin Name	I/O	Pin Function
1	P32/XCLK0/SCL	MSCK	I/O	Serial transfer clock output of clock synchronous system (Set to Input port at not used.)
2	P33/SO0/SDA	MSO	I/O	Serial transfer data output of clock synchronous system (Set to Input port at not used.)
3	P34/TO0	EECS	O	Enable output for writing and reading of the EEPROM data
4	P35/TO1	MREQ	O	Serial hand shake to the mode controller "L"
5	P36/TO2	FOK	I	FOCUS OK input (L: FOCUS OK)
6	P37/TO3	LRST	O	Reset output for the servo and digital system ICs (L: Reset)
7	XRESET	XRESET	I	Reset input (L: Reset)
8	VDD1	+5V	-	+5V
9	X2	CLOCK	-	Crystal input for system clock (32MHz)
10	X1	CLOCK	-	Crystal output for system clock (32MHz)
11	VSS1	GND	-	GND
12	P00	XECE	O	Enable output for reading the jig for test "L"
13	P01	RECE	O	Laser diode recording power ON/OFF ON: H
14	P02	XAMUTE	O	AUDIO last stage mute "L" (according to the mode controller) MUTE ON: during REC/PAUSE, at input selector switch and during STOP
15	P03	TP302	O	"L" outputs
16	P04	TP303	O	"L" outputs
17	P05	XEXSC	O	External sync enable output of LC89585 "L"
18	P06	XASYNC	O	ATIP frame sync "L"
19	P07	XENCE	O(I)	Serial enable output of LC89585 "H" (Set to Input port at not used.)
20	P67/XREFRQ/HLDK	TP305	O	"L" outputs
21	P66/XWAIT/HLDQ	TP306	O	"L" outputs
22	P65/XWR	XWR	O	Strobe signal output for read operation of the external memory
23	P64/XRD	XRD	O	Strobe signal output for write operation of the external memory
24	P63/A19	XLT	O	Latch output of CXD2585Q command
25	P62/A18	SSCK	O	Serial clock output for CXD2585Q command
26	P61/A17	SSO	O	Serial data output for CXD2585Q command
27	P60/A16	ALAT	O	Latch output for AK8563 command
28	P57/A15	SCLK	O	Serial clock output for serial readout of CXD2585Q
29	P56/A14	ENBL	O	Laser diode ON/OFF H: ON
30	P55/A13	TP307	O	"L" outputs
31	P54/A12	LDPW4	O	Recording laser power monitor output
32	P53/A11	LDPW3		
33	P52/A10	LDPW2		
34	P51/A9	LDPW1		
35	P50/A8	LDPW0		
36	P47/AD7	AD7	O	Data address line
37	P46/AD6	AD6		
38	P45/AD5	AD5		
39	P44/AD4	AD4		
40	P43/AD3	AD3		

No.	Mark	Pin Name	I/O	Pin Function
41	P42/AD2	AD2	O	Data address line
42	P41/AD1	AD1		
43	P40/AD0	AD0		
44	ASTB/CLKOUT	ASTB	O	External latch signal of lower address signal for external memory access
45	Vss0	GND	-	GND
46	TEST	GND	-	GND
47	P10/PWM0	SPSP	O(A)	Spindle drive PWM output in the Spindle CAV
48	P11/PWM1	LPWM	O(A)	Loading motor output (PWM) AT PWM is not used: "H" (fixed to "H")
49	P12/ASCK2/XSCK2	SQCK	O	Serial clock output for sub-Q of CXD2585Q
50	P13/RXD2/SI2	SQSI	I	Serial data input for sub-Q of CXD2585Q
51	P14/TXD2/SO2	SO2	O	Serial data output
52	P15	TP314	O	"L" outputs
53	P16	TP315	O	"L" outputs
54	P17	TP316	O	"L" outputs
55	VDD0	+5V	-	+5V
56	P70/ANI0	TEPP	I(A)	Tracking error peak to peak (for tracking gain adjustment)
57	P71/ANI1	RFT	I(A)	A/D input of upper side envelope of Playback RF
58	P72/ANI2	RFB	I(A)	A/D input of lower side envelope of Playback RF
59	P73/ANI3	TEMP	I(A)	A/D input of temperature sensor
60	P74/ANI4	RFOPC	I(A)	Running OPC return light 1
61	P75/ANI5	VWDC2	I(A)	Running OPC return light 2
62	P76/ANI6	TRAY	I(A)	A/D input of loading position (OPEN/CLAMP)
63	P77/ANI7	AD7	I(A)	Not used
64	AVDD	Avdd	-	+5V
65	AVREF1	Avref1	-	+5V
66	AVSS	AVss	-	GND
67	ANO0	WREF	O(A)	Recording power 1
68	ANO1	VWDC2R	O(A)	Outputs for strategy setting
69	AVREF2	Avref2	-	+5V
70	AVREF3	Avref3	-	GND
71	P20/NMI	XPFAIL	I	Power failure detection
72	P21/INTP0	FG	I	Spindle FG detection
73	P22/INTP1	ATIP	I	ATIP SYNC detection
74	P23/INTP2/C1	SCOR	I	EFM decoder frame sync detection
75	P24/INTP3	SUBSYNC	I	EFM decoder frame sync detection
76	P25/INTP4/ASCK/-XSCK1	XRFDT	I	EFM playback RF detection
77	P26/INTP5	ITSIN	I	SENS input
78	P27/SI0	MSI	I	Serial transfer DATA input of the clock sync. system
79	P30/RXD/SI1	MACK	I	Serial hand shake CLOCK input to the mode controller
80	P31/TXD/SO1	XFUSE	I	"L" during communicate with the mode controller

Note: (A) in item I/O shows "ANALOG".

■ PDJ014A (CD-R CORE ASSY: IC351)

External port (External RAM domain (2C000H to 2C0FFH))

No.	Mark	Pin Name	I/O	Pin Function
45	POA0	LOUT1	O	Loading open "H"
46	POA1	IN1	O	Loading close "H"
47	POA2	TP366	O	"L" outputs
48	GND	GND	O	GND
49	POA3	TP367	O	"L" outputs
50	POA4	CDROP	O	Running OPC control output for CD-R
51	POA5	AGCON	O	AGC circuit ON for WOBBLE extraction at CD-R recording section trace
52	POA6	GAINUP1	O	Gain setting for CD-RW
53	POA7	GAINUP2	O	Bias power correction output for CD-RW
54	POB0	ECLV	O	EFM / Wobble CLV mode of the spindle servo
55	POB1	CLV	O	CLV/CAV mode of the spindle servo
56	POB2	DGA1	O	In the PLAY or REC mode, it becomes "L" for outer periphery from 18 minutes of the CD and 12cm CD-R, and "H" for outer periphery from 9 minutes of the 8cm CD-R.
57	POB3	D8CM	O	"H" for 8cm CD-R disc
58	Vcc	VCC	O	+5V
59	POB4	XCD	O	Select SW of the mirror detection circuit CDR/CD (at CD: L)
60	POB5	SSEL	O	Detection reset signal of the tracking error envelope "L"
61	POB6	TP371	O	"L" outputs
62	POB7	TP372	O	"L" outputs
63	POC0	ADD30	O	Strategy assist setting
64	POC1	DOUBLE	O	For double-speed equivalent (at double-speed: H) (Fixed to "L")
65	POC2	RW_XR	O	Switch the CD-RW/Other (at CD-RW: H)
66	POC3	ERAS	O	At Physical erase: "H"
67	POC4	STCN4	O	Strategy control output
68	GND	GND	O	GND
69	POC5	STCN3	O	Strategy control output
70	POC6	STCN2		
71	POC7	STCN1		

4. PIN FUNCTION OF PRINCIPAL IC

• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

4.1 AD1893JST

PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC311)

PDR-509 (CD-R CORE ASSY : IC502)

• Sample Rate Converter IC

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	N/C	-	Not used	23	N/C	-	Not used
2	BCLK_I	I	Bit clock for input data	24	MODE0_O	I	Serial mode 0 control for output port
3	WCLK_I	I	Word clock for input data	25	BKPOL_O	I	Bit clock polarity L: Normal mode
4	LR_I	I	L/R clock for input data	26	N/C	-	Not used
5	N/C	-	Not used	27	GND	-	Ground
6	VDD	-	Power supply	28	VDD	-	Power supply
7	GND	-	Ground	29	N/C	-	Not used
8	N/C	-	Not used	30	DATA_O	O	Serial output, MSB fast
9	BKPOL_I	I	Bit clock polarity L: Normal mode	31	LR_O	O	L/R clock for output data
10	MODE0_I	I	Serial mode 0 control for input port	32	WCLK_O	O	Word clock for output data
11	N/C	-	Not used	33	N/C	-	Not used
12	N/C	-	Not used	34	N/C	-	Not used
13	MODE1_I	I	Serial mode 1 control for input port	35	BCLK_O	O	Bit clock for output data
14	XRESET	I	Reset signal L: Reset	36	PWRDWN	I	Power down input H: Low consumption electric power state
15	N/C	-	Not used	37	N/C	-	Not used
16	GND	-	Ground	38	SETSLW	I	Settling against the change in the sampling rate H: Slow, L: Fast
17	N/C	-	Not used	39	N/C	-	Not used
18	MUTE_I	I	Mute input	40	XTAL_O	O	Crystal output
19	N/C	-	Not used	41	N/C	-	Not used
20	MUTE_O	O	Mute output	42	XTAL_I	I	Crystal input
21	MODE1_O	I	Serial mode 1 control for output port	43	DATA_I	I	Serial input, MSB fast
22	N/C	-	Not used	44	N/C	-	Not used

4.2 PYY1196

PDR-555RW, PDR-V500 and PDR-19RW (FUNCTION ASSY : IC705)

PDR-509 (CD-R CORE ASSY : IC303)

• EEPROM

No.	Pin Name	I/O	Pin Function
1	NC	-	Non connection
2	VCC	-	Power supply
3	CS	I	Chip select input
4	SK	I	Serial clock input
5	DI	I	Start bit, operation code, address and serial data input
6	DO	O	Serial data output and indication output of READY/XBUSY internal state
7	GND	-	Ground
8	NC	-	Non connection

4.3 LC89585

PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC308)

PDR-509 (CD-R CORE ASSY : IC501)

• EFM Encoder IC

No.	Pin Name	I/O	Pin Connection
1	DIN1	I	Digital input 1
2	DIN2	I	Digital input 2
3	DIN3	I	Digital input 3
4	DIN4	I	Digital input 4
5	DIRRC1	I	RC oscillation input of DIR section
6	DIRRC2	O	RC oscillation output of DIR section
7	AVDD	—	Analog power supply
8	DIRRS	I	VCO oscillation band-pass adjustment input of DIR section
9	AGND	—	Analog ground
10	DIRVCO	I	VCO oscillation setting input of DIR section
11	DIRLPF	O	Low-pass filter of DIR section
12	VSS	—	Ground
13	VDD	—	Power supply
14	DIRCK	O	DIR system clock output
15	DIRBCK	O	DIR bit clock output
16	DIRLRCK	O	DIR LR clock output
17	DIRDATA	O	DIR demodulation data output
18	DIRWDCK	O	DIR word clock output
19	DIRU	O	User bit output
20	DIRERR	O	Data error or monitor output of lock state H: Unlock, L: Lock
21	DRAMSW	I	External DRAM capacity setting input H: 14MHz, L: 1MHz
22	CJSDATA	I	Data input of the clock jitter absorption circuit section
23	CJSBCK	I	Bit clock input of the clock jitter absorption circuit section
24	CJSLRCK	I	LR clock input of the clock jitter absorption circuit section
25	JITVCOIN	I	VCO input of the clock jitter absorption circuit section
26	JITLPFO	O	LPF output of the clock jitter absorption circuit section
27	JITLPFI	I	LPF input of the clock jitter absorption circuit section
28	JITPCO	O	Phase comparison output of the clock jitter absorption circuit section
29	JITERR	O	Lock state monitor output of the clock jitter absorption circuit section H: Unlock
30	DACDATA	O	DAC data output
31	DACBCK	O	DAC bit clock output
32	DACLCK	O	DAC LR clock output
33	ADCDATA	I	ADC data input
34	ADCCLK	O	ADC clock output
35	ADCBCK	O	ADC bit clock output
36	ADCLRCK	O	ADC LR clock output
37	ADCSTBY	O	ADC standby signal output H: Operate, L: Standby
38	XTALIN	I	System clock input
39	XTALOUT	O	System clock output
40	VSS	—	Ground
41	VDD	—	Power supply
42	DACCKOUT	O	DAC system clock output
43	ENCCKOUT	O	System clock output of CD decoder
44	CDDAT	I	Data input of CD decoder
45	CDBCK	I	Bit clock input of CD decoder
46	CDLRCK	I	LR clock input of CD decoder
47	CDTX	I	Digital out signal input of CD decoder
48	DITOUT	O	Digital out signal output
49	TP6	I	Test pin
50	XRESET	I	Reset pin L: Reset

No.	Pin Name	I/O	Pin Connection
51	TP7	I	Test pin
52	XCAS	O	DRAM row-address strobe signal
53	XOE	O	DRAM output enable signal
54	A8	O	DRAM address
55	A7	O	
56	A6	O	
57	A5	O	
58	A4	O	DRAM address
59	A3	O	
60	A2	O	
61	VDD	—	Power supply
62	VSS	—	Ground
63	A1	O	DRAM address
64	A0	O	
65	A9	O	DRAM address
66	XRAS	O	
67	XWR	O	DRAM writing/reading signal
68	DQ2	I/O	DRAM data input/output
69	DQ1	I/O	
70	DQ4	I/O	
71	DQ3	I/O	
72	TP0	I	Test pin
73	TP1	I	
74	TP2	I	
75	TP3	I	
76	ENCVCOIN	I	Clock input of the encode circuit
77	ENCLPFO	O	LPF output of the encode circuit
78	ENCLPFI	I	LPF input of the encode circuit
79	ENCPCO	O	Phase comparison output of the encode circuit
80	ENCERR	O	Lock state monitor output of the encode circuit H: Unlock
81	TP4	O	Test pin
82	TP5	I	
83	XRFDET	I	RF detection signal input L: RF exist, H: no RF
84	RECEN	I	Recording enable signal input L: Recording impossible, H: Recording possible
85	XSAMPLE	O	Sample hold pulse output
86	DET4T	O	4T detecting signal output
87	DET3T	O	3T detecting signal output
88	EFM	O	EFM signal output
89	VDD	—	Power supply
90	VSS	—	Ground
91	ENCCK	O	Encoder clock input
92	XEXTACK	O	ATIP synchronous signal output
93	XEXTSYNC	I	ATIP synchronous enable signal input
94	ATIPSYNC	I	ATIP synchronous signal
95	SUBSYNC	O	Subcode synchronous signal output
96	CCB	I	Select signal of the CPU interface L: General purpose serial, H: Sanyo CCB format
97	CE	I	Chip enable input of the CPU interface
98	CL	I	Data transfer clock input of the CPU interface
99	DI	I	Data input of the CPU interface
100	DO	O	Data output of the CPU interface

4.4 LH64256CK-70

PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC301)

PDR-509 (CD-R CORE ASSY : IC503)

• DRAM

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	I/O3	I/O	Data 3	14	A4	I	Address 4
2	I/O4	I/O	Data 4	15	A5	I	Address 5
3	XWE	I	Write enable	16	A6	I	Address 6
4	XRAS	I	Row address strobe	17	A7	I	Address 7
5	NC	—	Not used	18	A8	I	Address 8
—	—	—	—	—	—	—	—
9	A0	I	Address 0	22	XOE	I	Output enable
10	A1	I	Address 1	23	XCAS	I	Column address strobe
11	A2	I	Address 2	24	I/O1	I/O	Data 1
12	A3	I	Address 3	25	I/O2	I/O	Data 2
13	VCC	—	Power supply	26	VSS	—	Ground

4.5 PA9004A or PA9007A

PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC247)

PDR-509 (CD-R CORE ASSY : IC201)

• CDR Servo Amp.

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	VDD	—	Power supply	33	VCC1	—	Power supply
2	PWM1	I	CAV PWM input	34	TEG3	I	
3	PWM2	I	Wobble CLV PWM input	35	CTR	I	
4	PWM2O	O	Wobble CLV PWM output	36	TEDET	O	
5	PWM2+	I	Connect a capacitor for Wobble CLV LPF	37	BIAS	I	
6	PWM3	I	EFM CLV input	38	WBL1-	I	
7	PWM3+	O	EFM CLV output	39	WBL1O	O	
8	SPDL-	I		40	WBL2-	I	
9	SPDLO	O		41	WBL2O	O	
10	REFV	O		42	WBL3-	I	
11	GND1	—	Ground	43	WBL3O	O	
12	REFIN	I		44	WBLC-	I	
13	CLV	I	Spindle switching signal	45	WBLCO	O	
14	ECLV	I	Spindle switching signal	46	GND2	—	Ground
15	SPDL	O		47	RFB	O	
16	FWREV	O		48	RFB+	I	
17	FGOUT	O	FG output	49	RFT	O	
18	VEE1	—		50	RFT+	I	
19	FGIN	I	FG input	51	RFOPC	I	RF OPC signal input
20	Q0			52	HF	I	HF signal input
21	Q1			53	VEE2	—	
22	Q2			54	CBL	I	
23	Q3			55	CPL	I	
24	Q4			56	CDRMR1	O	CDR mirror
25	LDPWO	O		57	CDRMR2	I	
26	TEG0			58	CDRMRC	I	
27	TEG1	I	Tracking servo gain setting pin	59	RFDT-	I	
28	TEG2			60	RFREF	I	
29	TE	I	Tracking error input	61	RFDET	O	RF detecting signal output
30	TEO	O	Tracking error signal output after the gain set	62	XCD	I	
31	TRKG-	I		63	MIRR	O	Mirror signal output
32	TRKER	O		64	VCC2	—	

4.6 PDJ014A

PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC205)

PDR-509 (CD-R CORE ASSY : IC351)

• ATIP Decoder

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	WBL	I	Wobble input	41	XCE0	O	Chip enable output 0
2	FSK	O	FSK demodulation signal output	42	XCE1	O	Chip enable output 1
3	SBSY	I	Subcode sync. input Normally: 75Hz	43	XCE2	O	Chip enable output 2
4	MDP	O	MDP output for CLV servo	44	XCE3	O	Chip enable output 3
5	SPSEL	I	CPU interface select H: Serial, L: Parallel	45	POA0	I/O	General purpose input/output Gain switch for CD-RW (CD-RW: H)
6	ASYNC	O	ATIP sync. output	46	POA1	I/O	General purpose input/output AC circuit control signal for CD-R running OPC
7	ACK	I	Serial interface clock input	47	POA2	I/O	General purpose input/output
8	GND	—	Ground	48	GND	—	Ground
9	AOUTPE	I	Serial data read enable	49	POA3	I/O	General purpose input/output AN4 input switch (H: RFOPC, L: MPXOUT)
10	AOUT	O	Serial data output 32 bits	50	POA4	I/O	General purpose input/output Physical Erase
11	AINPE	I	Serial data write enable input	51	POA5	I/O	General purpose input/output Reset signal of tracking error envelope detection
12	AIN	I	Serial data input 16 bits	52	POA6	I/O	General purpose input/output AGC circuit ON/OFF for Wobble extraction
13	XCK	I	Master clock input Normal speed: 4.3218MHz	53	POA7	I/O	General purpose input/output N track jump
14	XSRST	I	System reset L: reset	54	POB0	O	General purpose output Loading open
15	SIOK	O	Special information standby flag output H: Readout possible	55	POB1	O	General purpose output Loading close
16	CRCOK	O	CRC calculation result output H: CRC OK, L: CRC NG	56	POB2	O	General purpose output Optical axis switching circuit ON/OFF
17	PROTECT	O	ATIP sync. protection state output H: Protection, L: Non-protection	57	POB3	O	General purpose output Enable output for writing and reading the EEPROM data
18	VCC	—	Power supply	58	VCC	—	Power supply
19	NC	—	Not used	59	POB4	O	
20	XADSEL	I	Start address setting strobe input of address decoder	60	POB5	O	General purpose output Strategy control output
21	XWE	I	Write enable input of the microcomputer	61	POB6	O	
22	XRE	I	Read enable input of the microcomputer	62	POB7	O	
23	SYA0	I	Address bus of the microcomputer	63	POC0	O	General purpose output Tracking error amp gain adjustment
24	SYA1	I		64	POC1	O	
25	SYA2	I		65	POC2	O	
26	SYA3	I		66	POC3	O	General purpose output Switch the CD-RW/other
27	SYA12	I	Ground	67	POC4	O	General purpose output Not used
28	GND	—		68	GND	—	Ground
29	SYA13	I		69	POC5	O	General purpose output Switch the CD/other
30	SYA14	I		70	POC6	O	General purpose output LD ON/OFF output
31	SYA15	I	Data bus of the microcomputer	71	POC7	O	General purpose output Audio last stage mute
32	SYD0	I/O		72	TESTB	I	Test pin
33	SYD1	I/O		73	TEST	I	Test pin
34	SYD2	I/O		74	TEST0	I	Test pin
35	SYD3	I/O		75	TEST1	I	Test pin
36	SYD4	I/O		76	TEST2	I	Test pin
37	SYD5	I/O		77	TEST3	I	Test pin
38	VCC	—	Power supply	78	VCC	—	Power supply
39	SYD6	I/O	Data bus of the microcomputer	79	TEST4	I	Test pin
40	SYD7	I/O		80	PREL_PSTH	I	

4.7 PDK033A [PDR-555RW, PDR-V500 and PDR-19RW (SERVO DIGITAL ASSY : IC316)]

PDK041A [PDR-509 (CD-R CORE ASSY : IC431)]

• Strategy Control IC

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	NC	–	Not used	25	ODON	O	Over-drive control H: over-drive ON
2	XRESET	I	Reset L: Reset	26	NC	–	Not used
3	NC	–	Not used	27	W_XR	O	Writing/reading signal output H:writing
4	CK34M	I	Clock input	28	NC	–	Not used
5	NC	–	Not used	29	WLDON	O	Write LD control
6	CK17M	O	2 dividing output of CK17M	30	VDD	–	Power supply
7	NC	–	Not used	31	GND	–	Ground
8	WFPDSH	O	Sample pulse output for Write APC	32	RWLDON	O	CD-RW LD ON/OFF
9	NC	–	Not used	33	NC	–	Not used
10	SAMPLE	O	Sample hold pulse output	34	REWLDON	O	Switch the CD, CD-R/CD-RW
11	NC	–	Not used	35	NC	–	Not used
12	OPCSH	O	Sample hold pulse output for OPC	36	NC	–	Not used
13	NC	–	Not used	37	STCN1	I	Starategy select 1
14	NC	–	Not used	38	STCN2	I	Starategy select 2
15	CK4M	I	4.3218MHz input	39	STCN3	I	Starategy select 3
16	NC	–	Not used	40	STCN4	I	Starategy select 4
17	EFMIN	I	EFM input	41	NC	–	Not used
18	NC	–	Not used	42	ERASE	I	ERASE control
19	XSAMPLE	I	Sample hold pulse input	43	NC	–	Not used
20	NC	–	Not used	44	RW_XR	I	Switch the CD-R/CD-RW
21	RECE	I	Recording enable signal input	45	NC	–	Not used
22	NC	–	Not used	46	DOUBLE	I	Switch the normal speed/double speed
23	TST1	I	Test pin Connect to ground	47	NC	–	Not used
24	NC	–	Not used	48	ADD30	I	Outputs for strategy adjustment (3T delay + 30)

4.8 AK5340-VS

PDR-555RW, PDR-V500 and PDR-19RW only (AUDIO ASSY : IC801)

• A/D Converter IC

No.	Pin Name	I/O	Pin Function
1	AINL+	I	L ch analog non-inverting input
2	AINL-	I	L ch analog positive-phase input
3	VREFIN	I	Reference voltage input
4	VA+	–	Analog power supply
5	AGND	–	Analog ground
6	NC	–	Not used
7	NC	–	Not used
8	TST1	–	Test pin
9	SEL18	I	Output data length select L: 16 bits, H: 18 bits
10	PD	I	Power down H: Power down
11	TST2	–	Test pin
12	CMODE	I	Master clock select L: 256 fs, H: 384 fs
13	SMODE	I	Interface clock select L: Slave mode, H: Master mode
14	L/XR	I	LR clock input
15	SCLK	I	Serial data clock input
16	SDATA	O	Serial data output
17	FSYNC	I	Output enable of SDATA H: Enable
18	VDP+	–	Digital power supply
19	DGND	–	Digital ground
20	CLK	I	Master clock input
21	TST3	–	Test pin
22	TST4	–	Test pin
23	NC	–	Not used
24	VDB+	–	Digital power supply
25	NC	–	Not used
26	VREF	O	Reference voltage output (VA+) – 2.6V
27	AINR-	I	R ch analog non-inverting input
28	AINR+	I	R ch analog positive-phase input

4.9 PD0236AD

PDR-19RW only (AUDIO ASSY : IC451)

• Hi-Bit IC

No.	Pin Name	I/O	Pin Function
1	BCSEL	I	Is select of the bit clock
2	DASEL	I	Output length select in the bit expansion function ON
3	LRSEL	I	Polarity select of LRCKO
4	LRCKO	O	LR clock output
5	BCKO	O	Bit clock output
6	DATAO	O	Data output
7	GND	-	Ground
8	NC	-	Non connection
9	NC	-	Non connection
10	VDD	-	Power supply
11	LRCKI	I	LR clock input
12	DATAI	I	Data input
13	BCKI	I	Bit clock input
14	NC	-	Non connection
15	SEL	I	Bit length expansion process/Input data output select
16	XRST	I	Reset pin H: Normal, H: Reset

4.10 PCM1800-1

PDR-509 only (AUDIO ASSY : IC802)

• A/D Converter

No.	Pin Name	I/O	Pin Function
1	VINL	I	Analog input L ch
2	VREF1	-	Decoupling capacitor of reference 1
3	REFCOM	-	Reference decoupling common
4	VREF2	-	Decoupling capacitor of reference 2
5	VINR	I	Analog input R ch
6	RSTB	I	Reset input Active "L"
7	BYPAS	I	LCF bypass control
8	FMT0	I	Audio data format 0
9	FMT1	I	Audio data format 1
10	MODE0	I	Master/Slave mode selection 0
11	MODE1	I	Master/Slave mode selection 1
12	FSYNC	I/O	Frame sync input/output
13	LRCK	I/O	Sampling clock input/output
14	BCK	I/O	Bit clock input/output
15	DOUT	O	Audio data output
16	SYSCLK	I	System clock input 256fs, 384fs or 512fs
17	DGND	-	Digital GND
18	VDD	-	Digital power supply
19	CINNPR	-	Anti-aliasing filter capacitor (-), R ch
20	CINPR	-	Anti-aliasing filter capacitor (+), R ch
21	CINNPL	-	Anti-aliasing filter capacitor (-), L ch
22	CINPL	-	Anti-aliasing filter capacitor (+), L ch
23	VCC	-	Analog power supply
24	AGND	-	Analog GND

5. RECORDING MECHANISM FOR CD-Rs AND CD-RWs

5.1 DISC

The PDR-555RW is capable of recording on CD-R discs and of recording and overwriting on CD-RW discs.

A CD-R has a triple-layered structure (from the bottom, the pigment recording layer, reflective layer, and protective layer) on a polycarbonate substrate, as shown in Fig. 5-1.

There are three kinds of pigments: cyanic pigments, phthalocyanin pigments, and azo pigments. And there are two kinds of reflective layer: gold and silver.

To record on the disc, a laser beam is shot at the pigment recording layer to form pits by transforming the pigments by heat.

When the disc is played, the difference in reflectivity between areas with converted pigments and unconverted pigments is read as a signal.

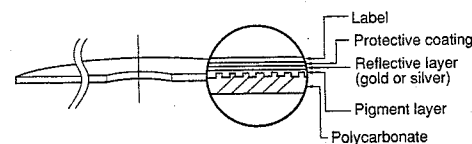


Fig. 5-1 CD-R disc

Transformation of CD-RW

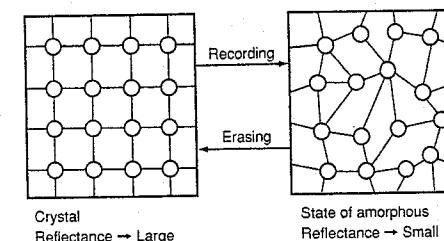


Fig. 5-3 Transformation of CD-RW

5.2 OVERWRITE RECORDING OF CD-RW

CD-RWs adopt overwriting. New recording can be made in an area with previous recording as shown in Fig. 5-4.

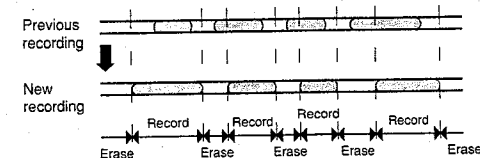


Fig. 5-4 Overwrite recording of CD-RW

The more complex CD-RW has a five-layered structure (from the bottom, a dielectric layer, phase-change recording layer, another dielectric layer, reflective layer, and protective layer) on a polycarbonate board, as shown in Fig. 5-2.

The phase-change recording layer is a colloid of chalcogen substances, such as AG-In-Sb-Te4 and Ge-Sb-Te. It becomes a liquid layer when heated to a high temperature (about 500°C to 700°C), with its atomic structure chaotic. And after being cooled rapidly, it becomes solid, with its atomic structure still chaotic. It is non-crystal (in an amorphous state).

It reaches a crystalline state after being heated to a lower temperature (about 200°C) and being cooled gradually. Lands and pits are made on the disc by repeating this procedure.

The reflectance is small when the layer is noncrystalline (in an amorphous state), and it is large when the layer is crystalline. This difference in reflectivity is read as a signal when the disc is played.

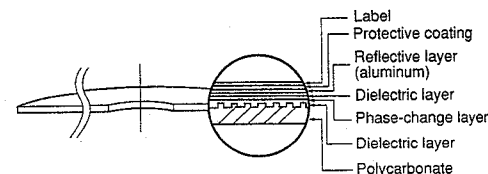


Fig. 5-2 CD-RW disc

6. PICKUP (KRS-200A)

The pickup of the PDR-555RW employs the 3-beam differential push-pull method.

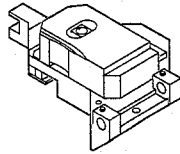


Fig. 6-1 KRS-200A

The pickup is incorporated with a drive IC for the laser diode. The surrounding circuits are designed as shown in Fig. 6-2 :

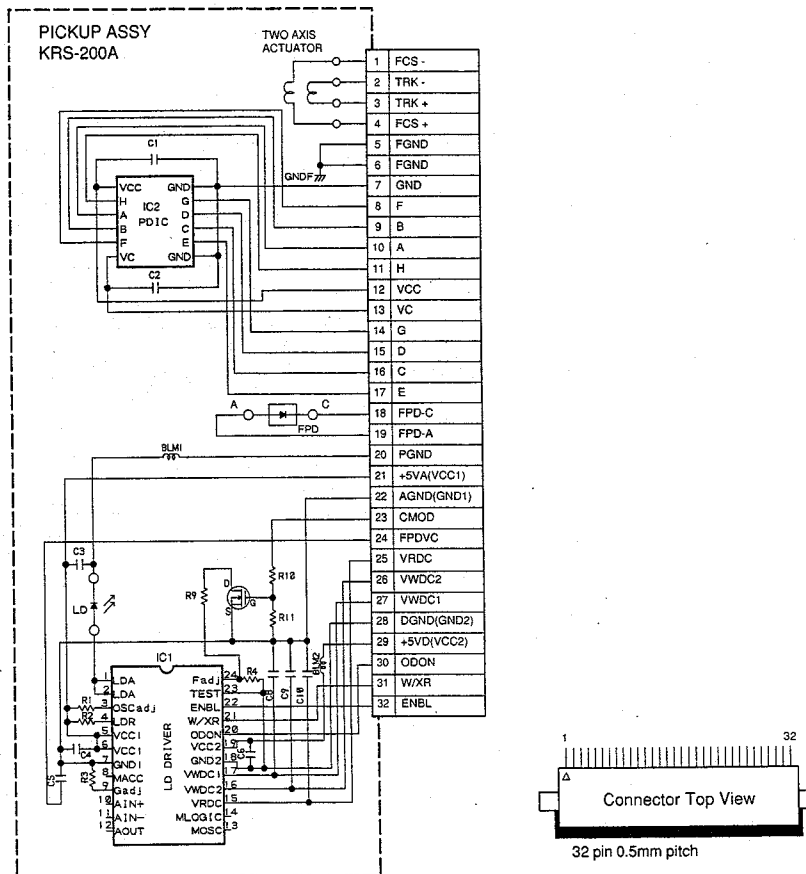


Fig. 6-2 Surrounding circuit of the pickup

7. CIRCUIT DESCRIPTIONS

7.1 SERVO CIRCUITS

7.1.1 Control Circuit for the Laser Diode

This circuit controls the optical output of the laser diode. It has two systems of APC circuit that keep the optical output of the laser diode constant.

One is the APC circuit for playing power of CD/CD-R/CD-RW and for erasing power while recording on CD-RW, and the other is for the recording power while recording on a CD-R or CD-RW.

Each adjustment VR is used for the adjustments shown below.

- VR101 (VR101) PB.PW : Playing power adjustment
- VR102 (VR163) R REC.PW1 : CD-R recording power adjustment
- VR103 (VR162) R REC.PW2 : CD-R overdrive adjustment
- VR104 (VR141) RW REC.PW0 : CD-RW bias power adjustment
- VR106 (VR164) RW REC.PW1 : CD-RW erasing power adjustment
- VR105 (VR161) RW REC.PW2 : CD-RW recording power adjustment
- () In the inside, for PDR-509.

The semi-fixed VRs shown above adjusts the points shown in Fig. 7-1 and 7-2.

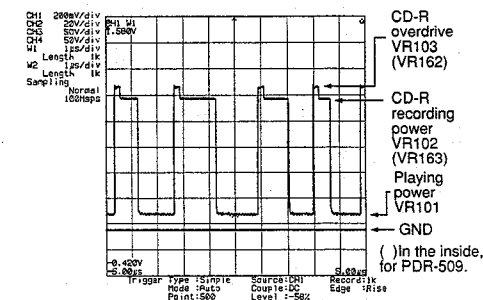


Fig. 7-1 CD-R recording waveform

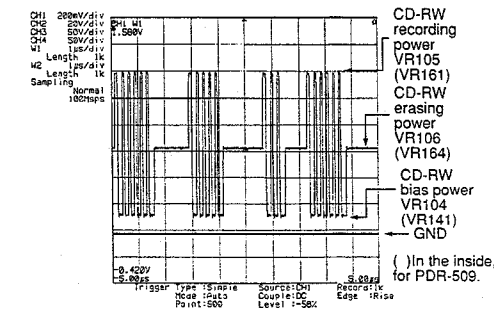


Fig. 7-2 CD-RW recording waveform

7.1.2 Error Signal Generation Circuit

Employing the 3-beam differential push-pull method, the pickup has a photodetector that divides the main beam in four and two detectors that divide the subbeam in two. HF, RF, Wobble, focus error, and tracking error signals are generated from the voltage signal output.

7.1.3 Focus Servo

The focus servo employs the same astigmatism method as that of conventional CD players.

The focus signal generated in the RF processor IC (AK8563, IC103-pin 11) is input to the decoder IC (CXD2585Q, IC353-pin 39).

The signal processed in the IC is output from pins 29 and 30. This output signal is input to the driver IC (BA5932FP, IC352), and is used to drive the focus actuator of the pickup.

7.1.4 Tracking Thread Servo

The tracking servo is also the same as that of conventional CD players.

The tracking error signal generated in the RF processor IC (AK8563, IC103-pin 10) is input to the decoder IC (CXD2585Q, IC353-pin 41) and CDR servo amplifier IC (PA9004A, IC247-pin 29).

The signal processed in the decoder IC is output from the pins 31 and 32. This output signal is input to the driver IC (BA5932FP, IC352), and is used to drive the tracking actuator of the pickup.

The signal input to the CDR servo amplifier IC is input to the mechanism control (IC204-pin 56) as the P-P value for tracking and used for detecting the tracking error level.

The tracking error signal also functions as the control signal of the sled.

7.1.5 Spindle Servo

The spindle servo has four modes: Stop, CAV, EFM-CLV and Wobble-CLV.

The EFM-CLV used for playing a CD is also used for playing a recorded CD-R or CD-RW. The A, B, C, and D signals that correspond to the four divisions of the main beam output from the pickup are generated in IC102 and IC101 as RF signals.

These RF signals are input to the decoder IC (CXD2585Q, IC353-pin 43). MDP (pin 25) of a triple-value PWM signal from the sync signal extracted from RF signal and internal standard signal.

When unrecorded parts of a CD-R or CD-RW are played, the rotation control signal called Wobble is read out from the guide groove of the disc. This Wobble signal is output from the RF processor IC (AK8563, IC103-pin 46), runs through the bandpass filter of the CDR servo amplifier IC (PA9004A, IC247), and becomes the Wobble-CLV signal in the ATIP decoder IC (PDJ014A, IC205).

In addition to the Wobble servo, the ATIP decoder IC decodes information, such as ATIP sync, absolute time, recommended recording power, lead-in area start time, lead-out start time and disc application code, from the Wobble signal, and sends the information to the mechanism-control microcomputer.

If a sudden change in the rotation of the spindle motor is required, such as upon start, stop, and search, FG is read to detect the rotation of the spindle motor in the servo mechanism assembly for CAV

control. The spindle motor is controlled by switching the above three spindle servos (CAV, EFM-CLV and Wobble-CLV) and Stop mode by controlling the switch of the servo amplifier IC (PA9004A, IC247) according to the control signal output from the mechanism-control microcomputer.

7.2 DEFECT CIRCUIT

The defect signal is output if there is a defect, such as a flaw, on the disc. If the defect signal is "Hi," the tracking error is muted and the low-frequency component of the error signal output just before the defect occurs is applied to the focus error and the spindle error so that the liability rises.

7.3 EFM-DIGITAL PLL

Channel clocks are required to demodulate the EFM signal reproduced from the optical system, because it is modulated to 3T to 11T (where T is a cycle of the channel clock), which is integer multiple of T. Practically, the PLL must read the channel clock because the irregularities in the spindle rotation may change the pulse width of the EFM signal.

This product has three stages of PLL. The first stage is a wide-range PLL. The output of the first-stage PLL functions as the standard for all clocks in CXD2585Q.

The PLL of the second stage is for generating high-frequency clock indispensable for the digital PLL of the third stage.

The PLL of the third stage is a digital PLL for generating the practical channel clock.

7.4 RF DETECTION

For CD-Rs there is an RF detection circuit to distinguish recorded and unrecorded parts. The detection signal is output from the servo amplifier IC (PA9004A, IC247-pin 61).

RFB and RFT also output the peak value and the bottom value of the HF signal used for OPC operation.

7.5 MIRROR CIRCUIT

A mirror signal equivalent to that of conventional CD players is used for CDs with EFM signals and for recorded parts of CD-Rs and CD-RWs.

For unrecorded parts of a CD-R or CD-RW, the mirror signal peculiar to the CD decoder is generated using the RC (radial contrast) generated by crossing a groove.

7.6 AUDIO CIRCUITS

7.6.1 Analog Audio Input

The audio signal input via JA801 runs through the volume of the VR Assy once and returns to the AUDIO Assy.

The input buffer circuit of IC803 (L-channel) and IC804 (R-channel) is a single-ended/differential conversion circuit composed inverting-inverting circuits.

The audio signal is converted to a differential signal and input to the IC801 A/D converter (AK5340-VS).

7.6.2 A/D Converter

AK5340-VS, made by Asahi Chemical is used as the A/D converter. This is an 18-bit, 2-channel A/D converter, which employs fifth-generation delta-sigma techniques.

It contains two delta-sigma modulators and performs 64-times oversampling of both channels simultaneously.

The input range of the A/D converter is 4.0 Vp-p. So it becomes 0 dB when a signal of 2.08 Vp-p is input to input terminals AIN+ and AIN-.

The control signals of the A/D converter are ADSTBY (pin 10), ADLRCK (pin 14), ADBCLK (pin 15), and ADDATA (pin 16). ADSTBY (pin 10) switches to Power-Down mode at "Hi" and offset calibration begins upon falling from "Hi" to "Lo."

During the offset calibration, the input of each channel is measured as the data for it. At this moment, each audio input terminal is separated from the outside and short-circuited inside.

ADLRCK (pin 14) is the signal from the encoder IC (IC308 LC89585, pin 36), and ADBCLK (pin 15) and ADDATA (pin 16) are signals for the encoder IC (pins 35 and 33).

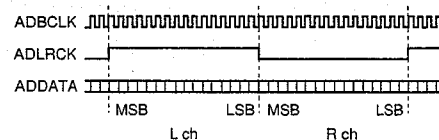


Fig. 7-3 AK5340-VS data output timing

However, A/D Converter of PDR-509 uses PCM1800-1 made by the BURR-BROWN company.

7.6.3 Hi-bit IC (PDR-19RW Only)

The PDR-19RW has a Hi-bit IC. It transforms 16-bit audio data from the encoder into 24-bit audio data.

7.6.4 D/A Converter

The PE8001A is used as the D/A converter (the PDR-509/KU/CA uses the PCM1716).

The PE8001A can switch the characteristics of digital filters. The switching is made in accordance with the serial data output from the mode-control microcomputer. The digital filter settings are switched depending on the product destination, as shown in the table below:

Types of the Digital Filter	Models/Destinations
Normal digital filter	PDR-555RW/KU/CA PDR-V500/KU/CA PDR-509/KU/CA(using PCM1716)
Legato link	PDR-555RW/MY PDR-19RW/KU/CA PDR-509/MY

(With the PCM1716, switched between normal digital filter and slow roll off)

"Enhanced multilevel sigma-delta techniques" are employed for the DAC block. They convert the output from the digital filter block into an 8-level sigma-delta modulation signal. Their anti-jitter efficiency of the operation clock is superior to that of the normal 1-bit DAC.

7.6.5 Analog Audio Output Block

The output from the D/A converter is output via the buffer amplifier, which has a gain of about 7 dB.

There are two audio-mute circuits. One is a mute circuit controlled by a microcomputer, and the other is a zero-detection circuit controlled by the ZERO terminal of the D/A converter.

This ZERO terminal outputs a signal when the audio input to the D/A converter becomes Infinity or Zero for both channels.

7.7 DIGITAL CIRCUITS

7.7.1 Digital Audio Interface Input Block

There are two systems of digital interface input: coaxial and optical. The coaxial input is sent to IC308 (LC89585, pin 4) via the duty-ratio adjustment circuit composed of IC313 (TC74HC00AF) and IC314 (NJM2940M), after its waveform being adjusted by IC305 (TC74HC04AF).

The optical digital input (JA301 (GP1F32R) output) is sent to IC308 (LC89585, pin 1) via the duty-ratio adjustment circuit, composed of IC313 (TC74HC00AF) and IC314 (NJM2940M).

The PDR-509 has no waveform adjustment circuit in the digital input block.

7.7.2 Sampling Rate Converter

The AD1893JST, the asynchronous type, is used as the sampling rate converter.

The sampling rate converter is bypassed as for PDR-509 when the sampling rate of the input is 44.1kHz.

7.7.3 Clock-jitter Suppressor Circuit (PDR-509 only)

The clock-jitter suppressor circuit of the encoder IC is used to absorb the jitter from the digital interface receiver when the sampling rate converter is in through mode.

7.7.4 Data Selector

The DIR block output, the clock-jitter suppressor block output, or the 384-fs clock input from the XTALIN terminal is output from the DACCKOUT and ENCKCKOUT terminals in accordance with a signal from the microcomputer.

7.7.5 Digital Fader, Level Meter, Mute Blocks

The output range of the digital fader block is +17.99 to -66.22 dB. The level meter interface block provides the data select output and the fader output. The selected input data are processed to provide total 16-bit data for L channel and R channel. The level meter interface block has a zero detection circuit, which outputs to microcomputer interface block when detecting that the input data to both channels are all zero.

Muting can be turned on/off for the output from the fader block. The digital volumes of the PDR-509 also use this block. The variable range is +12 dB to -48 dB.

7.7.6 Memory Control

The encoder IC can control an external D-RAM (1 or 4 Megabits). It receives signals from the mute block, the clock-jitter suppressor block and the encode block.

7.7.7 EFM Encoding

Subcode P and Q and the digital audio data from the D-RAM control block are EFM-modulated.

At the same time, subcodes, sync and a merge bit are added. Then, it is NRZI-converted and encoded to EFM signals of the CD format.

7.7.8 Strategy Control

Whereas the signal of 3T to 11T (T=231 nsec) is obtained in the EFM encoder block, the LD power-on time is adjusted in recording so that the pit length becomes ideal for playback.

Specifically, pulses 3T to 11T are processed for -1T and output as 2T to 10T.

However, the optimum pulse width in recording slightly differs depending on the disc types. The PDK033A (strategy control IC) of IC316 performs fine adjustment of this pulse width.

For PDR-509, Strategy control IC becomes IC431 PDK041A.

7.7.9 Digital Audio Interface Modulation

The digital audio interface modulation block receives signals from the CD decoder (IC353, CXD2585Q), DIR block (through input), and A/D converter (IC801, AK5340-VS).

The input signals are converted to the digital audio interface and output from DITOUT (pin 48). The signals are output in the CP1201 (EIAJ) civilian format.

8. DETAILED DESCRIPTIONS OF OUTPUT TERMINAL CONTROL

The terminals controlled by the microcomputer are set in each mode as follows:

8.1 DGAI (microcomputer, pin 48) and D8CM (microcomputer, pin 53) For PDR-509 : DGAI (ATIP decoder, pin 56) and D8CM (ATIP decoder, pin 57) TERMINAL CONTROL

	DGAI	D8CM
TEST mode	L	L
Normal mode		
Not for spindle CLV	L	L
For spindle CLV		
Playing the outer periphery from 18 minutes in absolute time	H	-
Recording on the outer periphery from 18 minutes in absolute time	H	-
CD-R/RW whose program area is less than thirty minutes (regarded as an 8-cm disc)	-	H
Others	L	L

8.2 AGCON (ATIP decoder, pin 52) For PDR-509 : AGCON (ATIP decoder, pin 51) TERMINAL CONTROL

	AGCON
Recording	L
Not recording with RF	H
Not recording without RF	L

8.3 XCD (ATIP decoder, pin 69)

For PDR-509 :

XCD (ATIP decoder, pin 59) TERMINAL CONTROL

	XCD
After inserting a disc	
CD	L
CD-R (New disc)	H
CD-R (Partial disc)	H
CD-R (Finalized disc)	L
CD-RW (New disc)	H
CD-RW (Partial disc)	H
CD-RW (Finalized disc)	H
After finalizing	
CD-R (Partial disc)	H → L
CD-RW (Partial disc)	H → H
Finalized disc	H → H
After All Track Erase	
Finalized disc	H → H
After TOC Erase	

8.4 GAINUP1 (ATIP decoder, pin 45)

RW/XR (ATIP decoder, pin 66)

For PDR-509 :

GAINUP1 (ATIP decoder, pin 52) RW/XR (ATIP decoder, pin 65) TERMINAL CONTROL

	GAINUP1, RW/XR
After inserting a disc	
CD	L
CD-R (New disc)	L
CD-R (Partial disc)	L
CD-R (Finalized disc)	L
CD-RW (New disc)	H
CD-RW (Partial disc)	H
CD-RW (Finalized disc)	H
Recording CD-RW (GAINUP1 is set to "L" in the circuit.)	H
Judging the FZC disc	L
In CD-RW REC setting average	L

8.5 CDROPC (ATIP decoder, pin 46)

For PDR-509 :

CDROPC (ATIP decoder, pin 50) TERMINAL CONTROL

	CDROPC
Performing CD-R running OPC	H (400 msec after starting recording)
AC circuit control ON	
During PCA count or TEST REC	L
During PMA REC	L

8.6 GAINUP3 (ATIP decoder, pin 47)

For PDR-509 :

RWBIAS (ATIP decoder, pin 53) TERMINAL CONTROL

	GAINUP3
Temperature rises over 45°C	H → L
Temperature falls under 40°C	L → H
The upper end of limitation	NG when exceeded
The lower end of limitation	NG when exceeded

8.7 PHYERS (ATIP decoder, pin 52)

For PDR-509 :

ERAS (ATIP decoder, pin 66) TERMINAL CONTROL

	PHYERS
During All Disc Erase	H
During PCA Erase	H

8.8 SSEL (ATIP decoder, pin 51)

For PDR-509 :

SSEL (ATIP decoder, pin 60) TERMINAL CONTROL

	SSEL
Adjusting the TEG error level	H

8.9 ENBL (ATIP decoder, pin 70)

For PDR-509 :

ENBL (Microcomputer, pin 29) TERMINAL CONTROL

	ENBL
When LD is ON	H

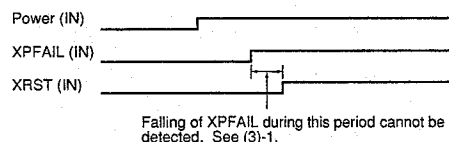
9. OPERATION DESCRIPTIONS

9.1 ABOUT POWER ON/OFF

9.1.1 Power-up (When the power outlet is active)

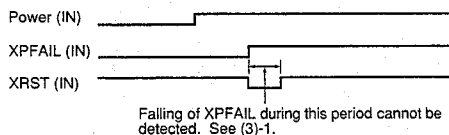
9.1.1.1 Without Backup Power Supply. (When the content of RAM of the microcomputer is cleared.)

- (1) The power turns on.
- (2) XPFAIL becomes "H".
- (3) The reset of the microcomputer then becomes "H" and the microcomputer starts operating. Immediately after the microcomputer starts operating, it confirm that XPFAIL = "H."
- (3)-1 If XPFAIL = "L," the microcomputer immediately returns to STOP mode (power-save mode). In this case, backup process is not performed.



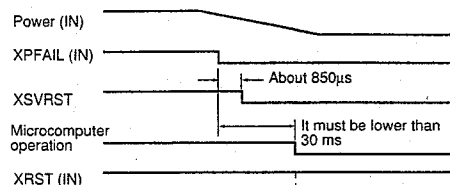
9.1.1.2 With Backup Power Supply

- (1) The power turns on.
- (2) XPFAIL becomes "H," and the reset of the microcomputer becomes "L" at the same time.
- (3) The reset of the microcomputer then becomes "H," and the microcomputer exits STOP mode and starts operating. Immediately after the microcomputer starts operating, it confirm that XPFAIL = "H."
- (3)-1 If XPFAIL = "L," the microcomputer immediately returns to STOP mode (power-save mode) again. In this case, backup process is not performed.



9.1.2 Power Down (When the power outlet is not active or power failure occurs)

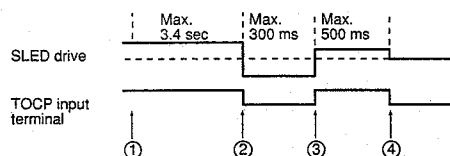
- (1) The power starts turning off, and XPFAIL becomes "L" when the power voltage decreases to some extent.
 - (2) Interrupted at XPFAIL = "L," and the current operating mode, disc data, etc. are backed up.
 - (3) As the reset may become "L" about 3 ms after XPFAIL become L," the microcomputer must enter STOP mode (power-save mode) before that. (This is because resumption is made without data backup if the microcomputer is reset before it enters STOP mode.)
- In STOP mode, the reset is pulled up by the backup power supply.



9.2 ABOUT SERVO CONTROL

9.2.1 Seek Track 0

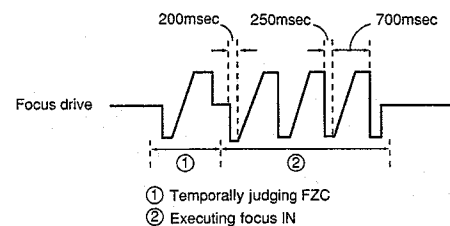
The sled is carried to the TOC area (home position).



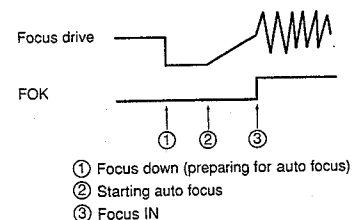
- ① When TOCP is "H," the sled starts moving toward the inner periphery.
- ② When TOCP becomes "L," the sled moves toward the outer periphery.
- ③ When TOCP becomes "H," the sled slowly moves toward the inner periphery.
- ④ When TOCP becomes "L" (detected by an interruption), the sled stops, and the operation finishes.

9.2.2 Focus ON

9.2.2.1 Without a Disc

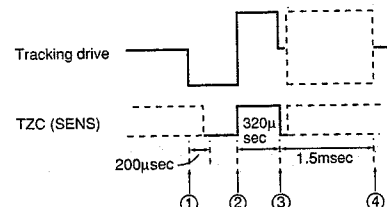


9.2.2.2 With a Disc



9.2.3 One-Track Jump (Direct Sequence)

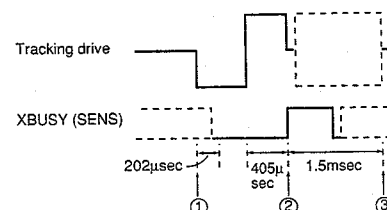
Used for CD-R/RW only.



- ① Starts KICK. TZC blind time: 200 µs
- ② Detects TZC rising in FWD (or falling in REV). Starts BREAK. BREAK time: 320 µs.
- ③ Detects TZC falling in FWD (or rising in REV).
- ④ Finishes GAIN-UP after 1.5 ms.

9.2.4 One-Track Jump (Auto Sequence)

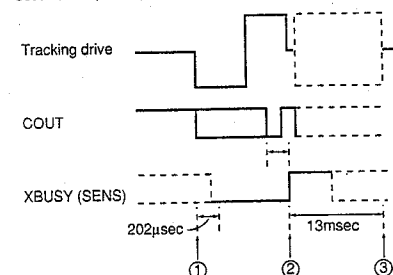
Used for CD/Finalized CD-R only.



- ① Starts the auto sequence (starts a jump).
- ② Detects XBUSY (SENS) rising. (The auto sequence ends.)
- ③ Finishes GAIN-UP after 1.5 ms.

9.2.5 Ten-Track Jump

Used for CD/CD-R/CD-RW.

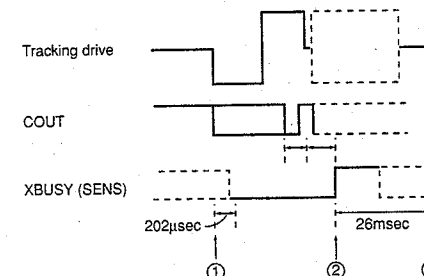


- ① Starts the auto sequence (starts a jump).
- ② Detects XBUSY (SENS) rising. (The auto sequence ends.)
- ③ Finishes GAIN-UP after 13 ms (1 loop).

* : The auto sequence ends when the cycle of COUT exceeds Overflow C (405 µs).

9.2.6 2N-Track Jump

Used for CD/Finalized CD-R only.

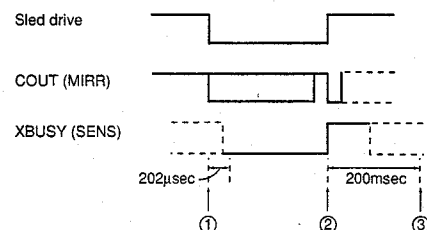


- ① Starts the auto sequence (starts a jump).
- ② Detects XBUSY (SENS) rising. (The auto sequence ends.)
- ③ Finishes GAIN-UP after 26 ms (2 loops).

* : The auto sequence ends after KICK (D) when the cycle of COUT exceeds Overflow C (405 µs).

9.2.7 M-Track Move

Used for CD/Finalized CD-R only.

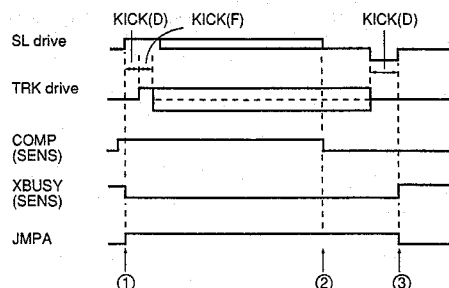


- ① Starts the auto sequence (starts a jump).
- ② Detects XBUSY (SENS) rising. (The auto sequence ends.)
- ③ Executes tracking ON processing after 200 ms

9.2.8 Fine Search

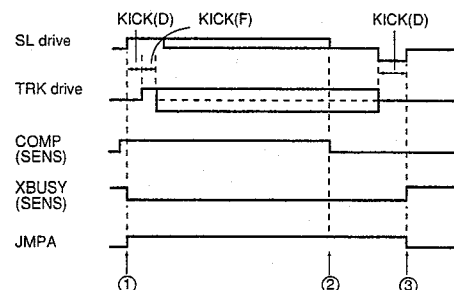
Used for CD-R/RW. There are two formats of Fine Search: M-Track Move format and 2N-Track Jump format, which are used depending on the conditions.

9.2.8.1 M-Track Move Format



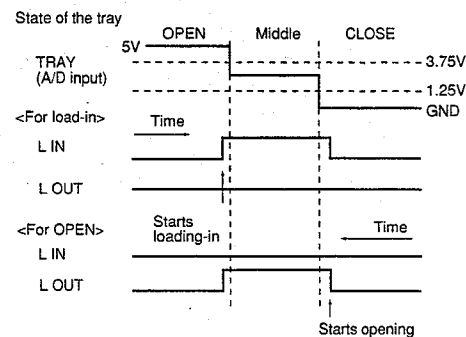
- ① Starts Fine Search (starts a jump).
- ② Detects COMP falling.
- ③ Detects XBUSY (SENS) rising. (The auto sequence ends.)

9.2.8.2 2N-Track Jump Format



- ① Starts Fine Search (starts a jump).
- ② Detects COMP falling.
- ③ Detects XBUSY (SENS) rising. (The auto sequence ends.)

9.2.9 Loading Control



9.2.9.1 Load-in Operation

Starts the load-in operation by setting LIN to "H." Regards that CLOSE is finished if the TRAY(A/D) input becomes lower than 1.25 V, and finishes the operation by setting LIN to "L."

9.2.9.2 Open Operation

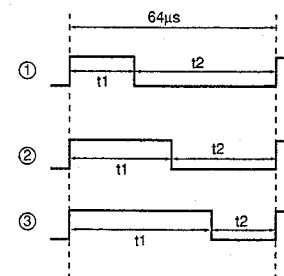
Starts the open operation by setting LOUT to "H." Regards that OPEN is finished if the TRAY(A/D) input becomes higher than 3.75 V, and finishes the operation by setting LOUT to "L."

9.2.10 Spindle Control

9.2.10.1 Spindle Control

The spindle is controlled using the PWM output from the SPSP terminal.

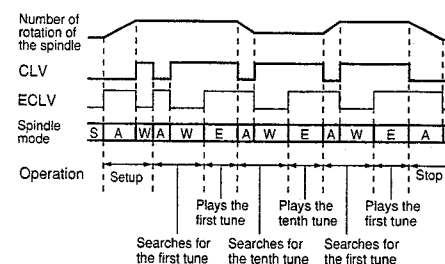
The microcomputer controls it only for CAV control.



- ① Kicking
The current velocity is slower than the target velocity.
(The velocity decreases upon spindle start-up, searching the inner periphery or in CAV.)
- ② Neutral
The target velocity and the current velocity are the same.
(In STOP, or during CAV lock)
- ③ Breaking
The current velocity is faster than the target velocity.
(The velocity increases upon spindle breaking, searching the outer periphery or in CAV.)

9.2.10.2 Spindle Servo Mode Switching

Spindle mode switching in CD-R STOP → PLAY → Search → STOP operations is shown below:



Spindle mode: S = STOP (in stop state)
A = CAV
W = Wobble CLV
E = EFM CLV

9.3 ERASING (CD-RW ONLY)

9.3.1 Last-Track-Erase Operation

The Last-Track-Erase function is to erase the last track of a partial CD-RW disc.

* Writing to PMA is performed when the tray is opened or at the next opportunity of PMA writing.

9.3.2 All-Track-Erase Operation

9.3.2.1 All-Track-Erase of a Partial CD-RW Disc

This function is to erase all tracks of a partial CD-RW disc.

* Writing to PMA is performed when the tray is opened or finished with the next PMA writing.

9.3.2.2 All-Track-Erase of a Finalized CD-RW Disc

This function is to return a finalized CD-RW disc to the state of a blank CD-RW disc so that recording can be made on it again.

9.3.3 TOC-Erase Operation

TOC-Erase is the function to restore a finalized CD-RW disc to a partial CD-RW disc so that additional recording can be made on it again.

9.3.4 All-Disc-Erase Operation

All-Disc-Erase is the function to restore the recorded CD-RW disc (with pits on it) to the state of a blank CD-RW disc (with no pits).

9.3.5 PCA-Erase Operation

PCA-Erase is the function to automatically erase PCA when the PCA-area runs out in PCA recording.

9.4 RID CODES

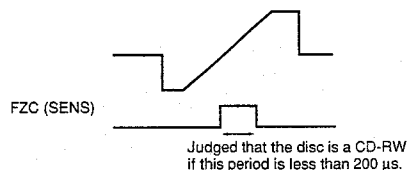
With an audio CD recorder code, the Recorder Identification (RID) codes are recorded in subcode Q channel mode 3 of a disc.

The content written in RID code is the maker code, type code (product model number), and identification code (serial number).

9.5 DISC JUDGMENT

9.5.1 Tentative Judgment Using FZC (Distinguishing Between CD/CD-R and CD-RW)

The judgment is made before the Focus IN operation after the disc is inserted.



Bringing up the focus, the range of FZC is checked at that time. It is judged that the disc is a CD/CD-R if FZC remains "H" for more than 200 μ s. It is judged that the disc is a CD-RW if FZC is not detected or intermittently detected.

9.5.2 Disc Judgment with Each Type of Disc

<CD-RW>

1: Blank Disc

- ① Disc that has no RF in LIA and PMA
 - Brand-new disc
 - Disc after ALL Disc Erase processing
- ② Disc that has RF in PMA, but not in LIA
 - Disc that has only MODE2 in PMA
 - Disc of category ①, calibrated once and ejected.
 - Disc that has MODE0 in PMA
 - Disc of category ①, with ALL Track Erase processing executed after recording, and ejected
- ③ Disc of MODE0 data while it has RF in LIA and PMA
 - Disc processed with only an ALL Track Erase operation after being finalized

2: Partial Disc (Disc which has RF in PMA)

- ④ Disc that does not have RF in LIA
- ⑤ Disc that has RF in LIA
 - Disc processed with ALL Track Erase operation and recorded on after being finalized

3: Finalized Disc (Disc that has TOC in LIA)

- ⑥ Disc that does not have RF in PMA
 - Disc finalized with synchronized recording
- ⑦ Disc that has RF in PMA

<CD-R> Orange book Ver. 2.9/3.0

1: Blank Disc

- ⑧ Disc that does not have RF in LIA and PMA
 - Brand-new disc
- ⑨ Disc that has RF in PMA, but not in LIA
 - Disc that has only MODE2 in PMA
 - Disc of category ③, calibrated once and ejected.

2: Partial Disc (disc that has RF in PMA)

- ⑩ Disc that does not have RF in LIA

3: Finalize Disc (Disc that has TOC in LIA)

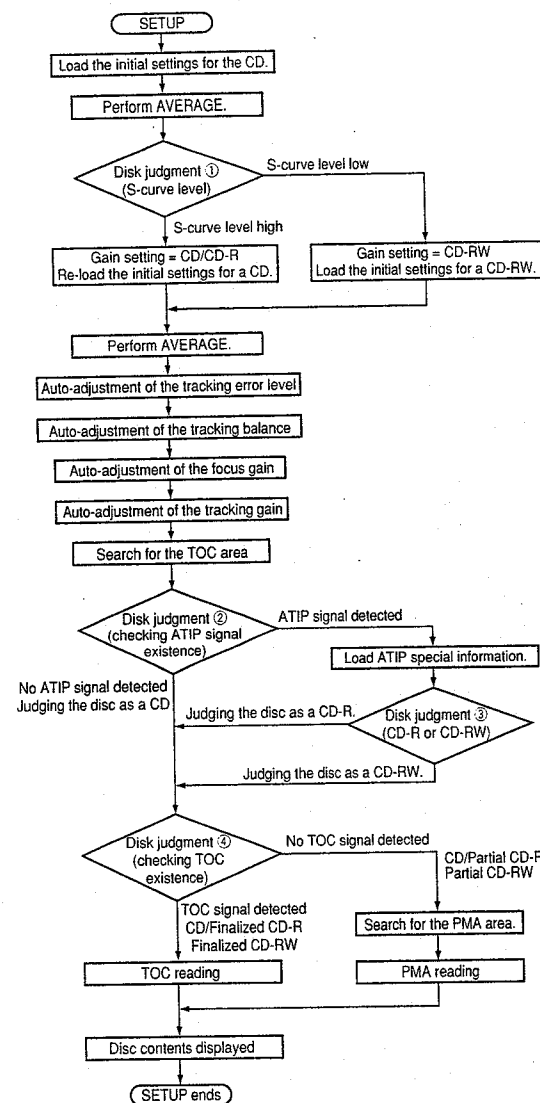
- ⑪ Disc that does not have RF in PMA
 - Disc finalized with synchronized recording
- ⑫ Disc that has RF in PMA

<CD>

- ⑬ 12cm CD
- ⑭ 8cm CD
- ⑮ CD-ROM
- ⑯ CD Extra
- ⑰ CD-I
- ⑱ CDV
- ⑲ Maxi-CD
- ⑳ Others

Note) LIA : Lead In Area
PMA : Program Memory Area

9.6 SETUP (FLOW)



Servo Condition

FOC Servo TRK Servo SPINDL Servo

OFF OFF STOP

OFF OFF STOP

OFF OFF STOP

OFF OFF STOP

OFF OFF STOP

ON OFF CAV

ON OFF CAV

ON ON CAV

ON ON CAV

ON ON W-CLV or RF-CLV

ON ON W-CLV

ON ON RF-CLV

ON ON W-CLV

OFF OFF STOP

9.6.1 Verification of Disc Judgments

1. Tentative judgment by FZC (distinguishing between CD/CD-R and CD-RW)

2. Tentative judgment by checking the RF existence at TOCP

< If RF exists >

The disc may be a finalized CD or CD-R/RW or an erased CD-RW. If the disc was judged to be a CD-RW in FZC tentative judgment, it remains judged as a CD-RW (the disc status is CD-R). If the disc was judged to be a CD/CD-R in FZC tentative judgment, it is tentatively judged to be a CD.

< If RF does not exist >

The disc has strong likelihood of CD-R or CD-RW. If the disc was judged as CD-RW in FZC tentative judgment, it is judged as CD-RW. If the disc was judged to be a CD/CD-R in FZC tentative judgment, it is judged to be a CD.

3. Tentative judgment (3) by checking the RF existence in LIA (99:00:00)

When the setup is finished with the results of tentative judgments 1 and 2, the RF existence is verified while searching for LIA (99:00:00) and executing AGC (gain adjustment).

< If RF exists >

The results of the tentative judgment indicates:
The CD may be a CD.
The CD-R may be a finalized CD-R.
The CD-RW may be finalized CD-RW or erased CD-RW.

< If RF does not exist >

The result of the tentative judgment indicates:
CD → no possibility
The CD-R may be a partial CD-R or blank CD-R.
The CD-RW may be a partial CD-RW or blank CD-RW.

4. Disc determination by reading the ATIP special information

If the result of tentative judgment indicates the disc is a CD-RW, and ATIP also indicates it is a CD-RW, the disc is determined to be a CD-RW.

In the tentative judgment by checking the RF existence in LIA (99:00:00):

- If RF exists → Loads TOC, as there is a possibility that the disc is a finalized CD-RW.
- If no RF exists → Makes a decision depending on the result of PMA loading, since the disc may be a partial CD-RW or blank CD-RW.

If both the result the tentative judgment and ATIP do not indicate it is a CD-RW, the disc is determined to be a CD-R.

In the tentative judgment by checking the RF existence in LIA (99:00:00):

- If RF exists → Loads TOC, as there is a possibility that the disc is a finalized CD-R.
- If no RF exists → Makes a decision depending on the result of PMA loading, since the disc may be a partial CD-RW or blank CD-R.

When the ATIP special information cannot be read, the disc is determined to be a CD.

If the results of FZC tentative judgment and ATIP indicate it to be of a different type, a retry is made by reversing the result of FZC tentative judgment.

5. Disc-type determination by reading TOC

If MODE0 data are detected while reading TOC with CD-RW, the disc status is changed to Partial CD-RW and the operation shifts to PMA reading.

9.6.2 Auto-Adjustments

9.6.2.1 Calibration of Tracking Offset Adjust Ability and Verification of the Temperature Sensor

These are carried out upon POWER ON and SETUP.

(1) Calibration of the tracking offset adjustment ability

- When ADR_RFB and ADR_RFT are 1.5 to 3 V, the values are stored in VRB_REF and VRT_REF respectively.
- When ADR_RFB and ADR_RFT are not 1.5 to 3 V, the mode-control computer is notified through the TOKNG_F setting that the initial values for the RF envelope signal cannot be obtained. In this case, the mode-control computer generates a STOP command if the setup step is before PCA. The values are stored in VRB_REF and VRT_REF, respectively.

(2) Verification of the temperature sensor

Upon POWER ON, the temperature sensor is checked whether the sensor shows a value within -15°C to 70°C. If it is out of the range, the sensor is judged defective, and subsequent operations are made in the temperature sensor defect status.

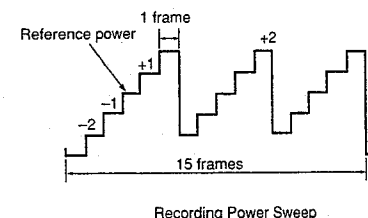
9.6.3 Tracking Error Level Adjustment and Disc Determination

The tracking error level adjustment and disc determination are simultaneously executed when a disc is inserted. The tentative judgment made in this stage checks the RF existence in the lead-in area. If RF exists, the disc is tentatively judged to be a CD. If no RF exists, the disc is tentatively judged to be a CD-R. However, if the result of the tentative judgment using FZC indicates it is a CD-RW, the disc is determined as CD-R regardless of the RF existence.

9.6.4 Recording Power Sweep Mode for Recording Power Calibration

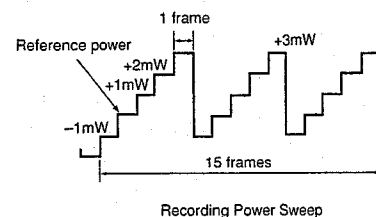
9.6.4.1 Sweep recording on CD-R

- Sweep recording of ± 2 steps of the reference power is made three times.



9.6.4.2 Sweep recording on a CD-RW

- Sweep recording of -1 mW to +3 mW of the reference power is made three times.



9.6.5 Playback RF Estimating Mode for Recording Power Calibration

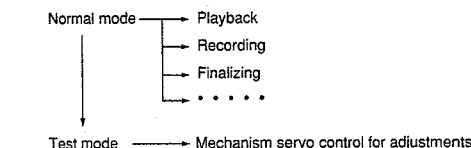
The PCA TEST area recorded in Recording Power Sweep mode is played back to check the RF waveform and find the optimum recording power.

10. ABOUT TEST MODE OPERATIONS

Test mode is provided to permit you to easily achieve adjustments and confirmation required for servicing.

When you set the unit to Test mode, the front-panel keys have different functions from these in Normal mode. By operating these keys in the specified sequence, you can perform the required adjustments and confirmations.

Relationship between Test mode and Normal mode



[Setting the unit to Test mode]

You can set the unit to Test mode with the following procedure:

1. Turn off the power.
2. Short-circuit the pattern for Test mode.
3. Turn on the power.
(When the unit enters Test mode, RAM information of the mechanism control is cleared.)

If Test mode is set correctly, displays different from those in ordinary power-up state are obtained. (All the FL indicators light, and REC LED lights in amber. (For PDR-509, REC LED lights in red.)) If the displays are the same as those in the normal mode, Test mode may not have been set correctly. Repeat steps 1 to 3 above.

Caution: Before setting the unit to Test mode, be sure to set the INPUT selector to ANALOG.

If the selector is not set to ANALOG, malfunctions may occur in Adjustment mode.

[Releasing Test mode]

You can release Test mode with the following procedure:

1. Press the STOP key to stop all operations.
2. Turn off the power.

[Key Functions in Test Mode]

Operations common to Adjustment modes and other modes

Key Name	Operation in Test Mode	Descriptions
FINALIZE	Focus servo close	Turns on the laser diode with the playback power, slowly moving up the focus actuator after moving it down, and closes the focus servo where the focus of the object lens is obtained. If you gently rotate the disc in stop state in this condition by fingers, you may hear the sound generated when the focus servo is operating correctly. If you press the key without loading a disc, the laser diode lights. The focus actuator repeats up and down movements three times after the first down movement, then it returns to the original position.
PLAY	Spindle Servo ON	Starts up the spindle motor for clockwise rotation, and sets the spindle servo to closed loop when the rotation speed of the disc reaches the specified value (about 500 rpm at the inner periphery).
PAUSE	Tracking Servo close/open	If you press this key in a condition that the focus servo and the spindle servo is correctly in a closed loop, the tracking servo is set to a closed loop, the current track number and the elapsed time are displayed on the front panel, and the playback signal is output. If the elapsed time is not displayed, it does not count up regularly, or if the audio is not counted correctly, there may be a defect in the outer peripheral no-sound area of the disc, poor maintenance or other problems. The key functions as a toggle switch. Each press of the key opens or closes the tracking servo in turn. If you press this key without loading a disc, no change occurs.
Key common to MANUAL SEARCH REV and TRACK/MANUAL REV	Carriage Reverse (toward the inner periphery)	Transports the pickup toward the inner periphery of a disc. If you press the key when the tracking servo is in a closed loop, the loop automatically opens. In Test mode, sufficient care must be taken when operating this key, since the motor does not automatically stop even when the pickup reaches the physical end.
Key common to MANUAL SEARCH FWD or TRACK/MANUAL FWD	Carriage Forward (toward the outer periphery)	Transports the pickup to the outer periphery of a disc. If you press the key when the tracking servo is in a closed loop, the loop automatically opens. In Test mode, sufficient care must be taken when operating this key, since the motor does not automatically stop even when the pickup reaches the physical end.
STOP	Stop	Stops all servos and returns them to their initial states. However, the pickup stays in the position it was in when the STOP key was pressed.
OPEN/CLOSE	Disc tray open/close	Opens and closes the disc tray. This key functions as a toggle switch. Each push open or close the disc tray in turn. When you press the key while the disc is rotating, the disc tray opens after the rotation of the disc stops.
REC ↓ REC MUTE	Playback power (CD) Maximum recording power (CD-R, -RW) Laser diode ON (except Adjustment mode)	Pressing the REC key provides the maximum recording power condition, and lights the REC LED in green. Subsequent pressing of the REC/MUTE key with the CD setting lights the REC LED in amber, and outputs the playback power. With CD-R or CD-RW setting, the REC LED lights in red and the maximum recording power is output by normal EFM. If you cancel the maximum recording power with the CD-R or CD-RW setting by pressing the STOP key, the setting automatically returns to that for a CD. Caution: The laser diode may be damaged if you press the key before adjustment. For PDR-509, when the REC MUTE key is pushed, the REC LED lights red, even if the REC key is pushed, the REC LED does not light.

Adjustment modes (with the INPUT selector set to OPTICAL (OPT))

Key Name	Operation in Test mode	Descriptions
DISPLAY OFF		To turn on/off the DISP_OFF LED.
MANUAL WRITE		To turn on/off the [MANUAL] LED.
ERASE	To specify the type of disc.	To switches the servos in accordance with the specified disc. The key input cyclically switches in the sequence of CD → CD-R → CD-RW. The disc segments on the FL display are then lit. Switching is enabled only in STOP state.
INPUT SELECTOR SW	To select the adjustment modes.	When the INPUT selector is not set to ANALOG, the ALC segments light, and the following adjustments are enabled: Be sure to return the selector to ANALOG when no adjustment is made.
AUTO/MANUAL	To select the adjustment modes. To turn off all the FL indications.	When the INPUT selector is not set to ANALOG, the above adjustment modes can be selected. Pressing the key with the INPUT selector set to ANALOG turns off all the FL indications for about 5 seconds.
REC For PDR-509, JOGDIAL Counterclockwise	To change the adjustment value in the minus direction	The adjustment value is changed in the minus direction and displayed.
REC/MUTE For PDR-509, JOGDIAL Clockwise	To change the adjustment value in the plus direction	The adjustment value is changed in the plus direction and displayed.
SKIP SET For PDR-509, JOGDIAL	To register the adjusted value.	The adjusted value is registered. When backup is correctly completed, the "?" segments will go dark.
SKIP CLEAR	To direct the track balancing process. To initialize the adjustment value.	The 32 segments (sampling display) light for a moment upon key input, and the tracking balance process is executed. This key operation must be made after FOCUS ON and SPINDLE ON. When the key is held pressed for 4 seconds, the adjustment value is initialized. When the backup is correctly completed, the "?" segments will go dark.
SYNC (remote control: RANDOM)	To direct the averaging process.	The 48 segments light upon key input, and the averaging process is executed. This key operation must be made in STOP state after specifying the type of disc.

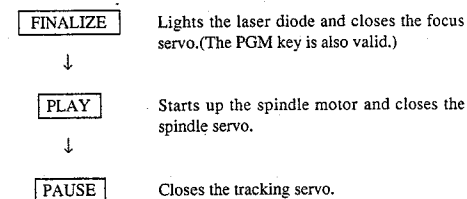
Modes other than adjustment mode (with the INPUT selector set to ANALOG)

Key Name	Operation in Test mode	Descriptions
DISPLAY OFF		To turn on/off the DISP_OFF LED.
MANUAL WRITE		To turn on/off the [MANUAL] LED.
ERASE	To specify the type of disc	To switches the servos in accordance with the specified disc. The key input cyclically switches in the sequence of CD → CD-R → CD-RW. The disc segments on the FL display are then lit. Switching is enabled only in STOP state.
AUTO/MANUAL	To select the adjustment modes. To turn off all the FL indications.	When the INPUT selector is not set to ANALOG, the above adjustment modes can be selected. Pressing the key with the INPUT selector set to ANALOG turns off all the FL indications for about 5 seconds.
REC		Used for outputting the maximum recording power.
REC/MUTE		Used for outputting the maximum recording power.
SKIP ON/OFF For PDR-509, TIME	To switch the displayed time	To turn on/off the SKIP segments. When the SKIP segments are on, the absolute time of a disc is displayed. When the SKIP segments are off, the elapsed Q-data time of each track of a disc is displayed.
SYNC (remote control: RANDOM)	To direct the averaging process	The 48 segments light upon key input, and the averaging process is executed. This key operation must be made in STOP state after specifying the type of disc.

Caution: Each servo operates independently in Test mode. So, for disc playback, you have to operate the keys by the correct procedure and sequentially close the servos.

[Playing a disc in Test mode]

Operate the keys in the following sequence to play a disc.



Operate the keys in a 2- to 3-second intervals

[Switching the time display in Test mode]

When the INPUT selector is set to ANALOG, you can change the time display with tracking on by pressing the SKIP ON/OFF key.

SKIP OFF (SKIP segments lit) : Absolute time (ATIME)

SKIP ON (SKIP segments unlit) : Sub Q TIME

For PDR-509, this key becomes TIME Key.

[Operation for line adjustment in Test mode]**Operating procedure:**

- ① Set to Test mode after setting the INPUT selector to ANALOG.
- ② Make preparations for measurements.
- ③ Set the INPUT selector to OPTICAL (OPT) or COAXIL (COAX). (The ALC segments light.)
- ④ Select the adjustment items with the AUTO/MANUAL key.

You can select the adjustment items for steps ③ and ④ in the combinations listed below.

After operation of steps ③ and ④, the adjustment item number is displayed at TNO of the FL display, and the current set value of that item is displayed at MIN and SEC.

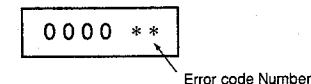
- ⑤ Press the REC key to change the value to the minus direction or the REC MUTE key to change it in the plus direction.
(For PDR-509, the value changes into the direction of the plus if JOGDIAL turns clockwise. The value changes into the direction of the minus if JOGDIAL turns counterclockwise.)
You may see the Q data or ATIP data of the disc by switching the INPUT selector to ANALOG in this state. If you set the INPUT selector back to OPTICAL (OPT) again, the former adjustment item is resumed.
- ⑥ When the desired value is set, register it by pressing the SET key.
The registered value lights and the "??" segments go dark when the backup of the value in EEPROM is completed.

- ⑦ If you wish to initialize adjustment values 1 to 4 to the default values of the microcomputer, press and hold the SKIP CLEAR key for about 4 seconds with the INPUT selector set to OPTICAL (OPT) or COAXIL (COAX). Adjustment values 1 to 4 are initialized and registered in EEPROM.

11. ERROR CODES**11.1 ERROR CODE DISPLAY FOR SERVICE****With PDR-555RW, PDR-19RW, PDR-V500:**

The PDR-555RW, PDR-19RW, and PDR-V500 can display error codes for service.

When the STOP key is held down for about 5 seconds in stop state in Normal mode, an FL display as shown below is obtained.

● Display

An error code for service is displayed in the right two FL digits.

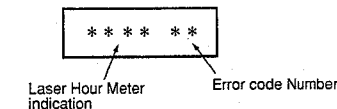
The error code for service is displayed as a number (ERROR NUMBER), which follows a message "CHECK DISC" or "CHECK." For details, see the table below.

With PDR-509

Laser Hour Meter Indication and Error Code Display for Service

The PDR-509 can display the total turn-on time of the laser diode and error codes for service.

When the STOP key is held down for about 5 seconds in stop state in Normal mode, an FL display as shown below is obtained.

● Display

Left 4 FL digits : Total turn-on time of the laser diode

Right 2 FL digits : Error code for service

The total turn-on time of the laser diode is displayed in the range of 0 to 5100.

The error code for service is displayed as a number (ERROR NUMBER), which follows a message "CHECK DISC" or "CHECK." For details, see the table below.

To initialize the total turn-on time of the laser diode, hold STOP key down for about 5 seconds in stop state with the INPUT selector set to ANALOG in Test mode.

The message "CLEAR" is displayed, and the total time is cleared.

Error code table for service

Code	Symptom	Contents of Error	Possible Cause	Checkpoints
H0	No operation even when power is supplied.	Communication between mechanism controller and mode controller is not achieved.	<ul style="list-style-type: none"> Improper soldering Pattern short 	IC204 (PD4956) IC205 (PDJ014) For PDR-509, IC301 (PE5109A) IC351 (PDJ014)
H1	(CHECK display)	Defective mechanism controller terminals	<ul style="list-style-type: none"> Short-circuiting of parts Improper power supply 	IC203 (HD74HC573) For PDR-509, IC371 (HD74HC573)
H2	Pre-recording process does not complete, and the tray does not open. (CHECK display)	Improper input voltage at the mechanism-control terminals (pins 22, 23, 24)		IC247(PA9004) For PDR-509, IC201(PA9007)
H5	Pre-recording process disabled (CHECK display)	Improper IC705 data writing	<ul style="list-style-type: none"> Defect in IC705 For PDR-509, IC303 	IC705 (PYY1196) for PDR-509, IC303 (PYY1196)
L*	The unit stops during the tray open/close operation. (CHECK display)	Improper loading	<ul style="list-style-type: none"> Defective tray position sensor Defective loading motor Improper soldering Pattern short Improper power supply 	IC352 (BA5932) for PDR-509, IC451 (M56788)
E*	The unit stops when PLAY or REC/PAUSE starts. (CHECK display)	Defective slider <ul style="list-style-type: none"> The pickup cannot be returned to the specified position. 	<ul style="list-style-type: none"> Disconnected flexible cable Defective drive circuit Abnormal power supply Abnormal TOC position switch Improper soldering 	S601 (MPU10230) IC352 (BA5932) IC353 (CXD2585Q) for PDR-509, S601 (MPU10230) IC451 (M56788) IC401 (CXD2585Q)
P*	The unit does not read the inserted disc, and stops. (CHECK DISC display)	Defect in spindle <ul style="list-style-type: none"> Disc upside-down. Dirty or cracked disc Abnormal disc rotation No signal obtained from the disc 	<ul style="list-style-type: none"> Defective spindle motor Defective spindle drive circuit Abnormal FG signals Defective WBL circuit Defective decoder circuit Unable to read ATIP or subcode High error rate 	PC651 (NJL5803K) IC352 (BA5932) IC353 (CXD2585Q) for PDR-509, PC651 (NJL5803K) IC451 (M56788) IC401 (CXD2585Q)

Code	Symptom	Contents of Error	Possible Cause	Checkpoints
C*	The unit stops before it enters REC/PAUSE mode.	Defects related to the recording laser power • Dirty or cracked disc • The optimum recording power cannot be obtained. • Trouble in RF detection.	• Defective laser diode • Trouble in RF detection • Defective RFT RFB circuit • Recording power is not sufficient. • Improper soldering, pattern short • Trouble with power supply • Unable to read ATIP or subcode	IC247 (PA9004) IC103 (AK8563) IC208 (TC7S04) IC209 (TC7S14) IC201 (PA9007) IC101 (AK8563) IC363 (TC7S04) IC364 (TC7S14)
F*	The unit stops during playback or recording.	Defective pickup • Unable to focus because of dirt or crack on the inserted disc. • Unable to output the proper laser power	• Defective laser diode • Defective focus drive circuits • Defective pickup • Improper soldering • Pattern short • Trouble of power supply	IC352 (BA5932) IC353 (CXD2585Q) IC451 (M56788) IC401 (CXD2585Q)
A*	The unit stops in a recording-related operation, displaying "CHECK DISC."	• Unable to focus Stop during recording • The unit stops, being obstructed by a dirt or a crack on the disc.	If any hardware trouble occurs before displaying A* or d*, the unit stops displaying a code other than these codes. Therefore, these service codes are generated only for troubles with the disc.	
d*	The unit stops in a recording related operation, displaying "CHECK DISC." The unit does not read the inserted disc, and stops.			

The indication for * shows the mechanism mode listed below:

No.	Mechanism Mode	No.	Mechanism Mode	No.	Mechanism Mode
0	PLAY	5	SETUP	A	REC
1	OPEN	6	TOC READ	B	TOC REC
2	STOP	7	—	C	OPC
3	—	8	SEARCH	D	TOC CHECK
4	—	9	REC/PAUSE	E	PMA, ACTUAL PAUSE REC

Initializing the Error Code Display

To clear the error codes, hold the "MENU" key down for about 10 seconds in Normal mode.

11.2 ABOUT FULL ERROR CODES

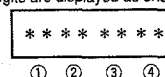
With the PDR-555RW, PDR-19RW, and PDR-V500, a full error code is displayed on the FL display when you press the SKIP PLAY key and MENU key simultaneously.

The full error codes are not backed up, and are cleared when the power is turned off.

With the PDR-509, press the TIME and DISP OFF keys simultaneously.

● Display

The eight digits are displayed as shown below:



The values of each 2 digits for ① to ④ (8 digits in total) are shown below:

Two digits displayed for ① :
The lower digit shows the operation when the error is detected.

x0	: Unknown
x1	: Tray open
x2	: Tray close or open
x3	: SETUP (starting up a disc)
x4	: TOC, PMA read (including SETUP)
x5	: PLAY
x6	: SEARCH
x7	: REC/PAUSE
x8	: REC
x9	: LEAD OUT REC
xA	: TOC REC
xB	: PMA REC
xC	: Power calibration
xD	: TOC CHECK
xE	: ACTUAL PAUSE REC
xF	: Unknown

When "F1" is displayed in two digits for ②, the digits for ① show the number of the defective pin of the mechanism control.

Two digits displayed for ② : Error Mode

Errors Generated in the Mechanism Control

Mode : Mode Name

No.

00	: Unfixed mode (inner condition unknown, upon hardware reset)
01	: Invalid mode
02	: STOP
03	: Laser diode on (playback power)
04	: Focus ON
05	: Spindle ON
06	: Tracking ON

07	: Direct sequence forward 1-track jump using DIRC
08	: Direct sequence reverse 1-track jump using DIRC
09	: Direct sequence forward 1-track jump repeat using DIRC
10	: Direct sequence reverse 1-track jump repeat using DIRC
11	: Auto sequence 10-track forward jump repeat
12	: Auto sequence 10-track reverse jump repeat
13	: Auto sequence 50-track forward jump repeat
14	: Auto sequence 50-track reverse jump repeat
15	: Auto sequence forward M-track movement
16	: Auto sequence reverse M-track movement
17	: PAUSE
18	: PLAY
19	: Seek track 0
20	: Blank search
21	: REC
22	: REC to PAUSE (REC END)
23	: ATIP TIME search
24	: Q-code TIME search
25	: Q-code track search
26	: Forward 300-track movement
27	: Reverse 300-track movement
30	: TOC area search
31	: Tray open
32	: Tray close
33	: Setup (→ PLAY)
34	: TOC read
35	: PLAY normal
36	: Search → PLAY
37	: REC. PAUSE
39	: Lead-out REC
40	: TOC (lead-in) REC
41	: PMA REC
42	: PCA REC
43	: TOC check
44	: Actual REC PAUSE
45	: Initializing
47	: 2-track jump setting in pause mode
48	: 1-track jump setting in pause mode
51	: Search → PAUSE
52	: PMA read
53	: Laser diode nominal recording power output
54	: Searching area with Q code
57	: Laser diode maximum recording power output
58	: Laser diode recording power continuous sweep mode
59	: Slider forward movement
60	: Slider reverse movement
61	: Calculating the track pitch and the line velocity of the disc by measuring T0 and T1.
62	: Auto sequence 1-track forward jump
63	: Auto sequence 1-track reverse jump

- 64 : Auto sequence 1-track forward jump repeat
 65 : Auto sequence 1-track reverse jump repeat
 66 : Auto sequence 10-track forward jump
 67 : Auto sequence 10-track reverse jump
 68 : (Sound-generating) Scan mode using auto sequence 10-track forward jump.
 69 : (Sound-generating) Scan mode using auto sequence 10-track reverse jump.
 70 : Auto sequence 50-track forward jump
 71 : Auto sequence 50-track reverse jump
 72 : High-speed scan mode using auto sequence 50-track forward jump.
 73 : High-speed scan mode using auto sequence 50-track reverse jump.
 74 : Several forward jumps in DTRNUM by combining auto sequence 2N-track jumps
 75 : Several reverse jumps in DTRNUM by combining auto sequence 2N-track jumps
 76 : REC mode continuous operation after resuming from a power failure
 78 : Blank search
 79 : Resume mode from "tracing error" and "out of focus" during REC

Errors the Mode Control Generates

Mode : Mode Name

No.

91 : Loading error

C7 : Cannot enter REC/PAUSE of power calibration even when 60 seconds elapsed.

d0 : Stops owing to a resume failure or STOP key input.

d4 : Insufficient data in TOC PMA read

d7 : RF check failure at REC/PAUSE

db : PMA REC does not finish even when 60 seconds have elapsed

dd : Stops owing to a TOC check error or STOP key input

df : Cannot enter REC/PAUSE even when 60 seconds have elapsed, cannot start REC even when 10 seconds have elapsed, or resuming from tracing error does not complete even when 60 seconds have elapsed

F0 : Communication error of the mechanism control

F1 : Hardware error of the mechanism control: The number of the defective pin of the mechanical control is displayed at TRACK.

F2 : A/D input (RFT, RFB) error of the mechanism control

F5 : RID serial number error

48

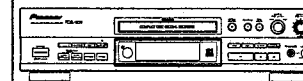
**Two digits displayed for ③ :
Recording submode when the error was generated**

- 10 : While setting REC/PAUSE
 20 : During REC/PAUSE
 30 : During REC
 40 : While stopping REC
 50 : Unlocking, during SCMS stop

**Two digits displayed for ④ :
Other condition when the error was generated is displayed by a HEX code**

- bit 7 : Out of focus
 bit 6 : Sync loss detected during REC
 bit 5 : tracing error during REC
 bit 4 : TOC read error (insufficient data)
 bit 3 : Improper A/D value of RFT, RFB
 bit 2 : No meanings
 bit 1 : No meanings
 bit 0 : No meanings

Service Manual

Pioneer

ORDER NO.
RRV2167

COMPACT DISC RECORDER

PDR-509

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Type	Model PDR-509	Power Requirement	Remarks
KU/CA	○	AC120V	
MY	○	AC220-230V	
MV	○	AC220-230V	

This service manual should be used together with the following manual (s).

Model	Order No.	Remarks
PDR-509	RRV2055	Service guide

FOR U.S. MODELS

NECESSARY INFORMATION FOR DHHS RULES MARKED ON THE REAR BASE AND ON THE TOP OF CD MECHANISM AS BELOW.

DANGER – LASER RADIATION WHEN OPEN.
AVOID DIRECT EXPOSURE TO BEAM.

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1. SAFETY INFORMATION

This service manual is intended for qualified service technicians ; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual. Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.



WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 – Proposition 65



NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols  (fast operating fuse) and/or  (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible  (fusible de type rapide) et/ou  (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

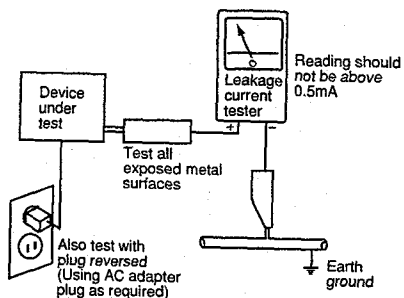
(FOR USA MODEL ONLY)

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.



AC Leakage Test

ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a Δ on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

IMPORTANT
THIS PIONEER APPARATUS CONTAINS LASER OF CLASS IIIb. SERVICING OPERATION OF THE APPARATUS SHOULD BE DONE BY A SPECIALLY INSTRUCTED PERSON.

LASER DIODE CHARACTERISTICS
MAXIMUM OUTPUT POWER: 23 mW
WAVELENGTH: 778 – 787 nm

LABEL CHECK

MV Type

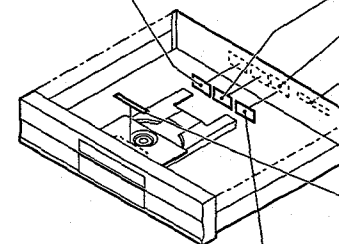
CAUTION
INVISIBLE LASER RADIATION WHEN OPEN, AVOID EXPOSURE TO BEAM
PRW1018

MY Type

VARO!
Avettomien ja suojauslaitteen ohitettessa olet alttiina näkyvättömälle lasersträilylle. Älä katso sisään.
VAROING!
Oeynlig laserstrålning när denna del är öppnad och spärran är urkopplad. Seakta ej strålen.
PRW1023

MY and MV Types

CLASS 1 LASER PRODUCT
Printed on Rear Panel



KU/CA Type

DANGER — LASER RADIATION WHEN OPEN. AVOID DIRECT EXPOSURE TO BEAM.
Printed on Rear Panel

DANGER — LASER RADIATION WHEN OPEN. AVOID DIRECT EXPOSURE TO BEAM.
PRW1516-A

ADVARSEL
USYNLIG LASERSTRÅLING VED ÅBNING NÅR SIKKERHEDS-SPERRER ER UDDE AF FUNKTION. UNDGÅ UDSEJTELSE FOR STRÅLING
VORSICHT!
UNSICHTBARE LASERSTRÅHLUNG TRITZ AUS, WENN DECKEL (ODER KLAPPE) GEÖFFNET IST! NICHT DEM STRAHL AUSSETZEN!
VW10184

MY Type

Additional Laser Caution

1. Laser Interlock Mechanism

The position of the switch (S601) on the SERVO MECHANISM Assy for detecting loading state is detected by the system microprocessor, and the design prevents laser diode oscillation when the switch (S601) is not on CLMP terminal side (CLMP signal is OFF or high level.). Thus, the interlock will no longer function if the switch (S601) is deliberately set to CLMP terminal side (low level).

The interlock also does not function in the test mode *. Laser diode oscillation will continue, if pin 1 of M51593FP (IC101) on the PRE-AMP BOARD ASSY mounted on the CD-R PICKUP is connected to GND, or pin 19 is connected to low level (ON), or else the terminals of Q101 are shorted to each other (fault condition).

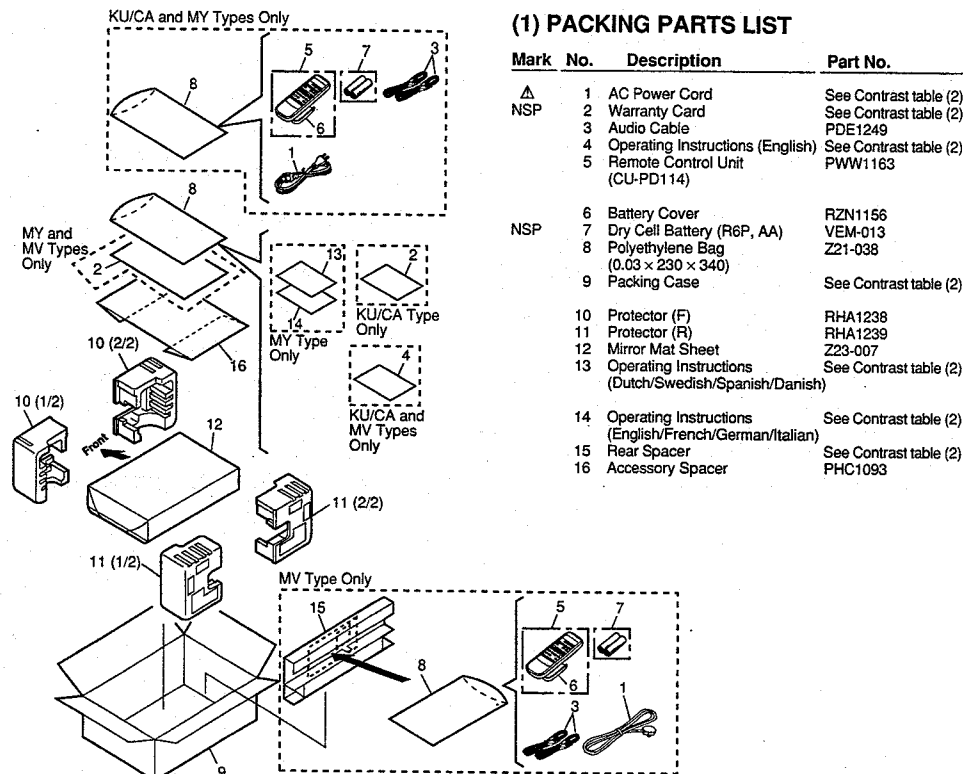
2. When the cover is opened with the servo mechanism block removed and turned over, close viewing of the objective lens with the naked eye will cause exposure to a Class 1 laser beam.

* Refer to page 52.

2. EXPLODED VIEWS AND PARTS LIST

NOTES: ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
 ● The Δ mark found on some component parts indicates the importance of the safety factor of the part.
 Therefore, when replacing, be sure to use parts of identical designation.
 ● Screws adjacent to ∇ mark on the product are used for disassembly.

2.1 PACKING

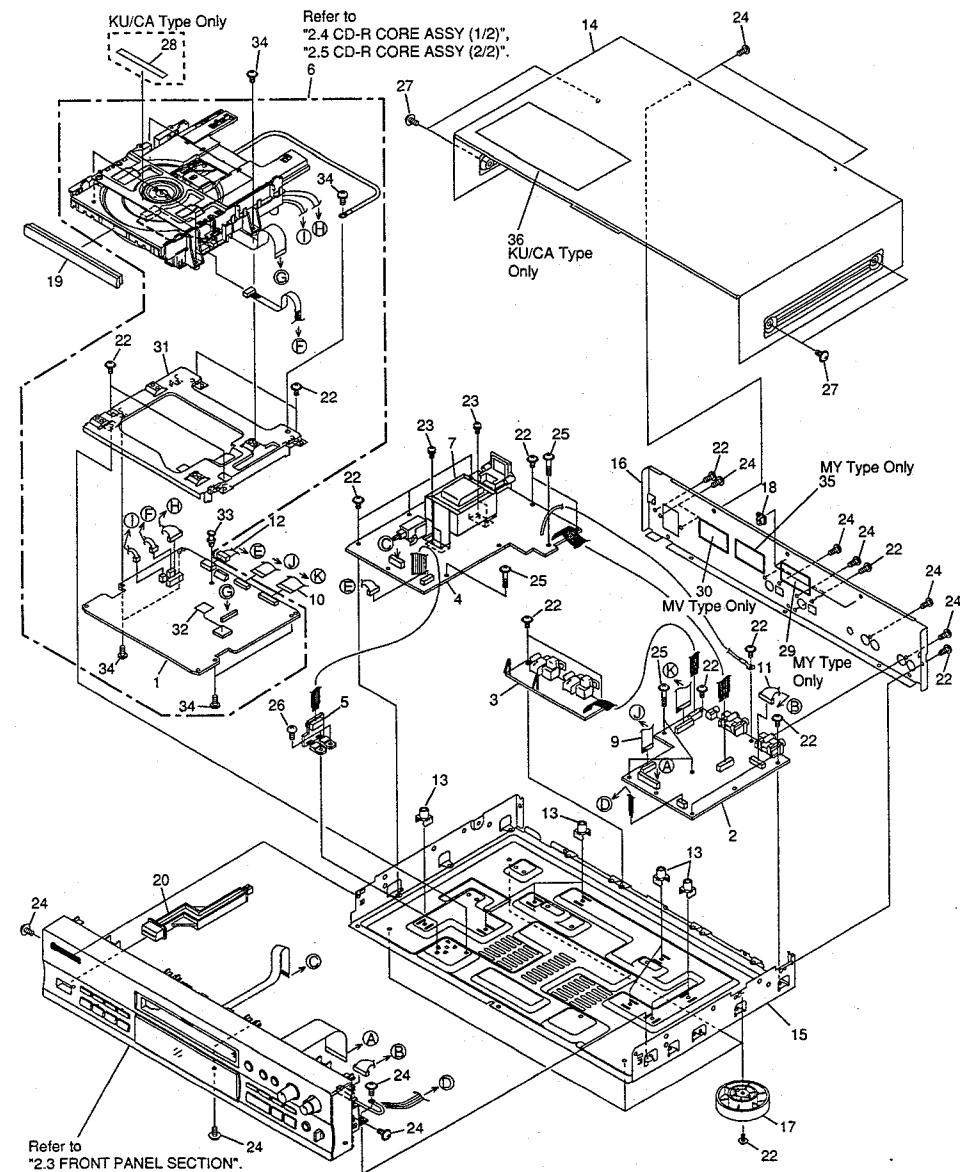


(2) CONTRAST TABLE

PDR-509/KU/CA, MY and MV are constructed the same except for the following :

Mark	No.	Symbol and Description	Part No.			Remarks
			KU/CA Type	MY Type	MV Type	
Δ NSP	1	AC Power Cord	ADG7021	ADG1127	ADG7004	
	2	Warranty Card	ARY7023	ARY7022	ARY7022	
	4	Operating Instructions (English)	PRB1296	Not used	PRB1296	
	9	Packing Case	PHG2382	PHG2383	PHG2396	
	13	Operating Instructions (Dutch/Swedish/Spanish/Danish)	Not used	PRD1057	Not used	
	14	Operating Instructions (English/French/German/Italian)	Not used	PRE1287	Not used	
	15	Rear Spacer	Not used	Not used	RHC1072	

2.2 EXTERIOR SECTION



(1) EXTERIOR SECTION PARTS LIST

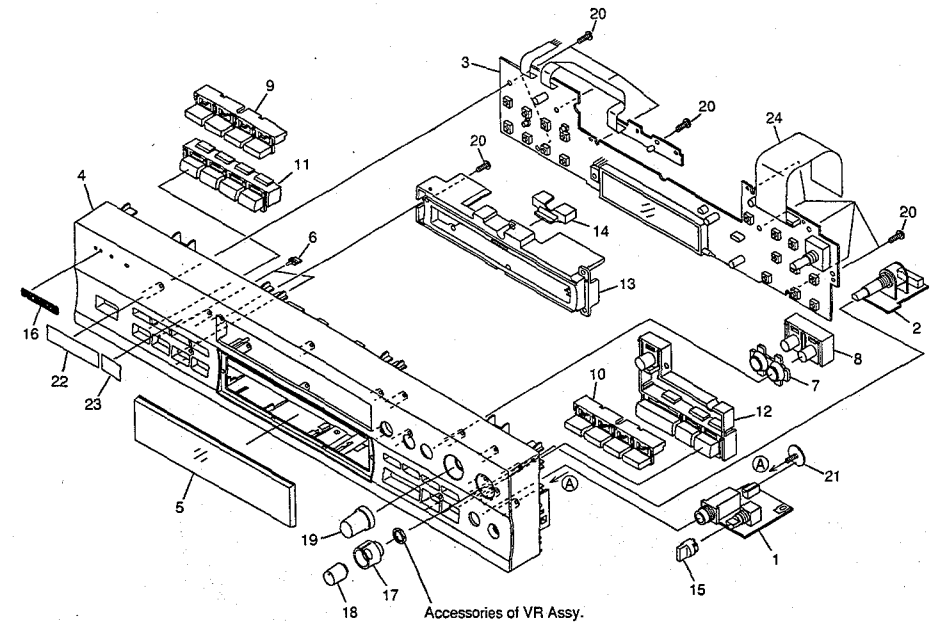
Mark	No.	Description	Part No.
	1	CD-R CORE Assy	PYY1273
	2	AUDIO Assy	See Contrast table (2)
	3	DIGITAL I/O Assy	See Contrast table (2)
	4	POWER SUPPLY Assy	See Contrast table (2)
	5	REG Assy	See Contrast table (2)
NSP	6	CD-R CORE Assy	PXA1625
Δ	7	Power Transformer (T1)	See Contrast table (2)
	8	
	9	15P Flexible Cable /30V	PDD1197
	10	21P Flexible Cable/30V	PDD1198
	11	Connector Assy (8P)	PDE1305
	12	Connector Assy	PG06KK-F25
	13	PCB Mold	AMR2115
	14	Bonnet	PYY1147
NSP	15	Under Base	PNA2514
	16	Rear Base	See Contrast table (2)
	17	Insulator	PNW2766
NSP	18	Mini Clamp	VEC1312
	19	Tray Panel	PNW2932
	20	Power Button B	RAC2207
	21	
	22	Screw	ABA1011
	23	Screw	RBA1132
	24	Screw	BBZ30P080FZK
	25	Screw	IBZ30P150FCC
	26	Screw	BBZ30P060FMC
	27	Screw	FBT40P080FZK
NSP	28	Laser Caution Label	See Contrast table (2)
	29	Caution Label HE	See Contrast table (2)
	30	Caution Label	See Contrast table (2)
NSP	31	Mecha Base	PNB1613
	32	Radiation Sheet	PEB1305
	33	PCB Spacer	AEC1371
	34	Screw	BBZ30P080FCC
	35	Caution Label	See Contrast table (2)
	36	Disc Caution Label	See Contrast table (2)

(2) CONTRAST TABLE

PDR-509/KU/CA, MY and MV are constructed the same except for the following :

Mark	No.	Symbol and Description	Part No.			Remarks
			KU/CA Type	MY Type	MV Type	
Δ	2	AUDIO Assy	PWZ3996	PWZ3997	PWZ3997	
	3	DIGITAL I/O Assy	PWZ4020	PWZ4021	PWZ4021	
	4	POWER SUPPLY Assy	PWZ4008	PWZ4009	PWZ4009	
	5	REG Assy	PWZ4012	PWZ4013	PWZ4013	
	7	Power Transformer (T1)	PTT1356	PTT1357	PTT1357	
NSP	16	Rear Base 509KU	PNA2525	Not used	Not used	
	16	Rear Base 509MY	Not used	PNA2526	PNA2526	
	28	Lase Caution Label	PRW1516	Not used	Not used	
	29	Caution Label HE	Not used	PRW1233	Not used	
	30	Caution Label	Not used	Not used	PRW1018	
	35	Caution Label	Not used	VRW1094	Not used	
	36	Disc Caution Label	PRW1532	Not used	Not used	

2.3 FRONT PANEL SECTION



(1) FRONT PANEL SECTION PARTS LIST

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	HEADPHONE Assy	PWZ3988	11	Mode Button		PAC1873
	2	VR Assy	PWZ3992	12	Play Button B		RAC2204
	3	OPERATING Assy	See Contrast table (2)	13	Sub Panel		PNW2797
	4	Front Panel	See Contrast table (2)	14	Function Lens		PNW2796
	5	Display Window	See Contrast table (2)	15	Headphone Knob		PAC1707
	6	LED Lens	PNW2745	16	Name Plate		PAM1776
	7	REC Ring	PNW2795	17	VOL Knob L		PAC1902
	8	REC Button	PAC1876	18	VOL Knob R		PAC1903
	9	Manual Button L	PAC1974	19	JOG Knob B		RAC2210
	10	Manual Button R	PAC1975	20	Screw		PPZ30P100FMC
				21	Screw		ABA1005
				22	Getter Label		See Contrast table (2)
				23	CD-R Getter		PRW1547
				24	19P Flexible Cable/60V		PDD1196

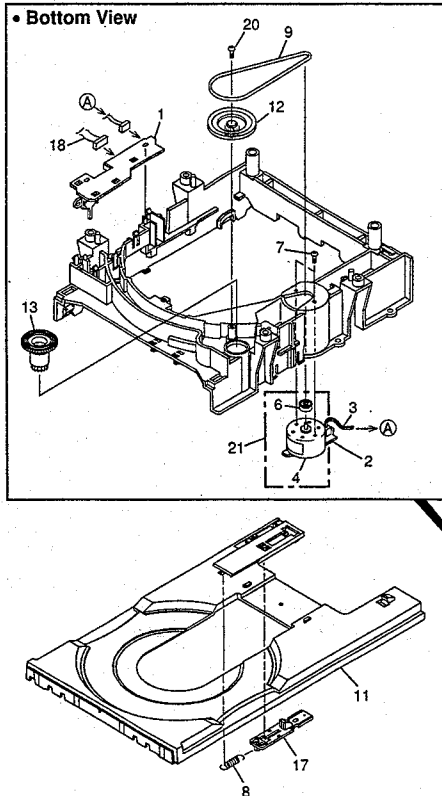
(2) CONTRAST TABLE

PDR-509/KU/CA, MY and MV are constructed the same except for the following :

Mark	No.	Symbol and Description	Part No.			Remarks
			KU/CA Type	MY Type	MV Type	
	3	OPERATING Assy	PWZ3977	PWZ3978	PWZ3978	
	4	Front Panel 509KU	PNW2928	Not used	Not used	
	4	Front Panel 509MY	Not used	PNW2929	PNW2929	
	5	Display Window	PAM1804	PAM1805	PAM1805	
	22	Getter Label 509KU	PRW1548	Not used	Not used	
	22	Getter Label 509MY	Not used	PRW1549	PRW1549	

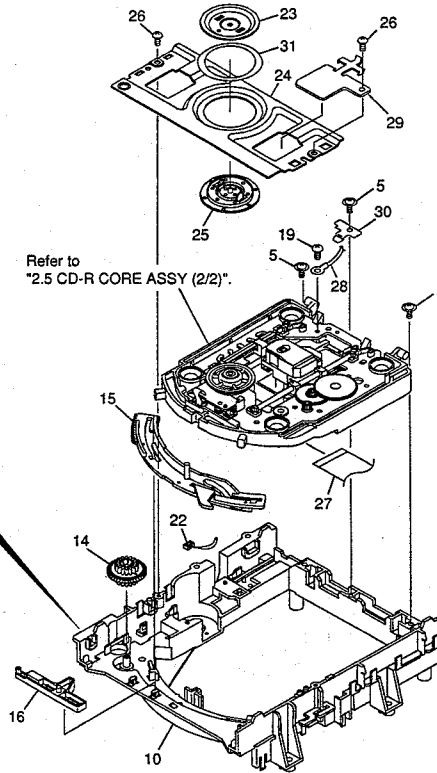
2.4 CD-R CORE ASSY (1/2)

• Bottom View



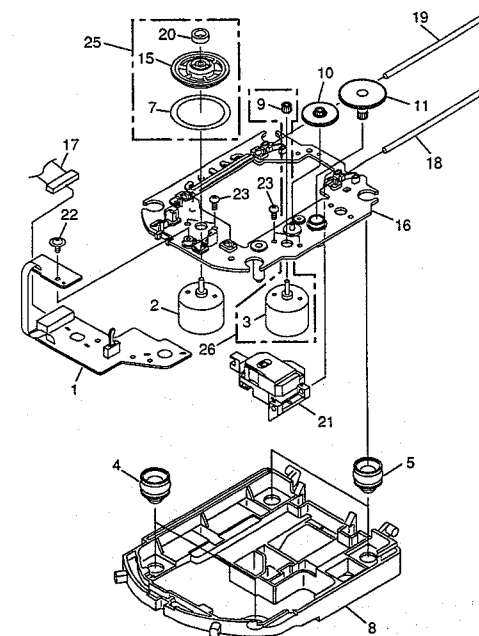
• CD-R CORE ASSY (1/2) PARTS LIST

Mark	No.	Description	Part No.
NSP	1	LOADING A Assy	PWZ3760
NSP	2	LOADING B Assy	PWZ3761
	3	Connector Assy (LOADING B CN551 ↔ LOADING A CN502)	PG02KK-E15
	4	DC Motor (LOADING)	PXM1027
	5	Screw	DBA1006
	6	Motor Pulley	PNW1634
	7	Screw	VBA1055
	8	Tray Stopper Spring	VBH1277
	9	Rubber Belt	VEB1260
	10	Loading Base	VNL1844
	11	Tray	VNL1731
	12	Gear Pulley	VNL1733
	13	Loading Gear	VNL1734
	14	Drive Gear	VNL1735



Mark	No.	Description	Part No.
	15	Drive Cam	VNL1736
	16	Lock Plate	VNL1820
	17	Tray Stopper	VNL1739
	18	Connector Assy (LOADING A CN501 ↔ CD-R CORE CN451)	PF03KK-E37
	19	Screw	BBZ26P040FMC
	20	Screw	IPZ20P080FMC
	21	Loading Motor Assy	VXX2505
	22	Binder	PEC-107
	23	Clamper Plate	VNE2068
	24	Bridge	VNE2069
	25	Clamper	VNL1738
	26	Screw	IPZ26P060FMC
	27	32P Flexible Cable / 30V (CD-R Pickup ↔ CD-R CORE CN101)	PDD1195
NSP	28	Earth Lead Unit	PDF1200
	29	Tray Holder	PNM1341
	30	Stopper	DNH2076
	31	Spacer	PNM1334

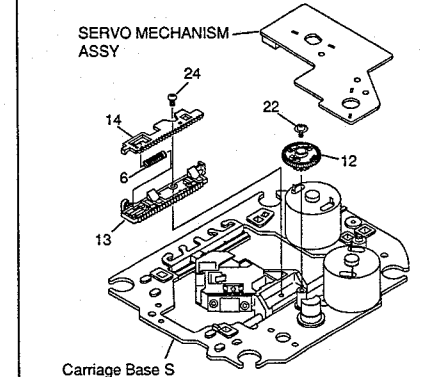
2.5 CD-R CORE ASSY (2/2)



• CD-R CORE ASSY (2/2) PARTS LIST

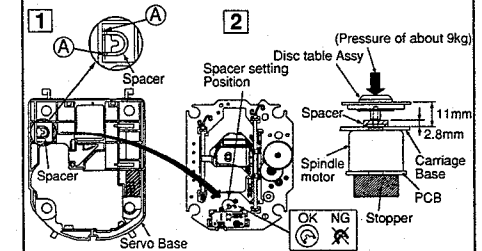
Mark	No.	Description	Part No.
NSP	1	SERVO MECHANISM Assy	PWZ3759
	2	DC Motor Assy (SPINDLE)	PEA1235
NSP	3	DC Motor (CARRIAGE)	PXM1042
	4	Float Rubber A	AEB7063
	5	Float Rubber B	AEB7066
NSP	6	Rack Spring	DBH1285
NSP	7	Reflection Sheet	PNM1325
	8	Servo Base	PNW2853
	9	Pinion Gear	PNW2854
	10	Gear A	PNW2855
	11	Gear B	PNW2856
	12	Gear C	PNW2857
	13	Rack	PNW2858
	14	Rack Stopper	PNW2859
NSP	15	Disc Table	PNW2860
	16	Carriage Base S	PNW2874
	17	Connector Assy (SERVO MECHANISM CN601 ↔ CD-R CORE CN452)	PG09KK-E17
	18	Guide Bar	VLL1488
	19	Sub Guide Bar	VLL1489
NSP	20	Magnet	VYM1024
	21	CD-R Pickup	PEA1351
	22	Screw	IPZ20P060FMC
	23	Screw	PMZ20P030FMC
	24	Screw	JGZ17P030FMC
	25	Disc Table Assy	PEA1349
	26	Carriage Motor Assy	PEA1350

• Bottom View



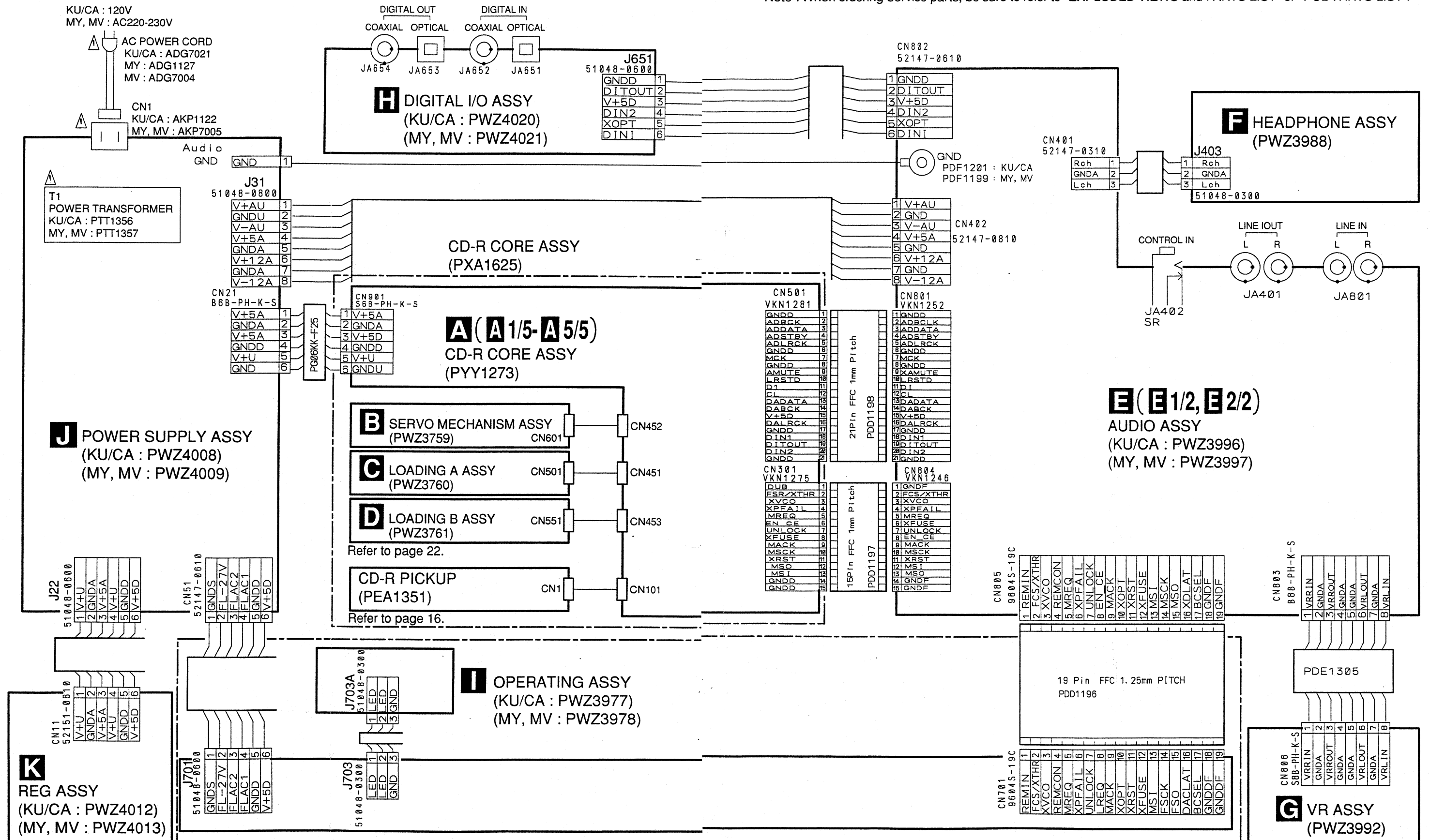
• How to Install the Disc Table

- Use nippers or other tool to cut the two sections marked (A) in figure 1.
- While supporting the spindle motor shaft with the stopper, put spacer on top of the carriage base, and stick the disc table on top (takes about 9kg pressure). Take off the spacer.



3.2 OVERALL WIRING DIAGRAM

Note : When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".



3.3 CD-R CORE ASSY (1/5)

A1/5 CD-R CORE ASSY (PYY1273)
• MECHA. CONTROL μ COM BLOCK

A

B

C

D

NOTES

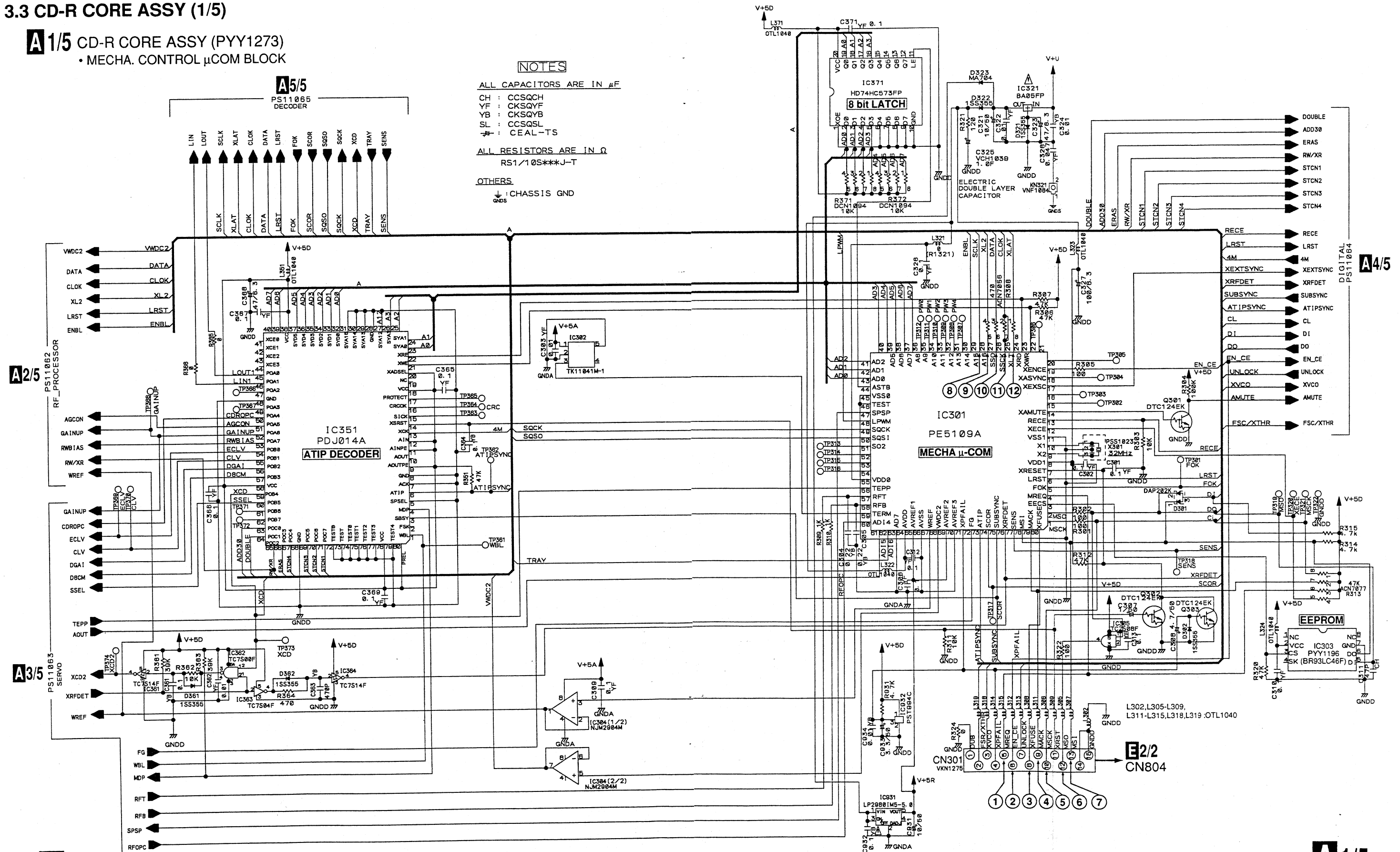
ALL CAPACITORS ARE IN μ F

CH : CCSQCH
YF : CKSQYF
YB : CKSQYB
SL : CCSQSL
* : CEAL-TS

ALL RESISTORS ARE IN Ω
RS1/10S***J-T

OTHERS

CHASSIS GND

**A4/5****A1/5** 15

3.4 CD-R CORE ASSY (2/5) and CD-R PICKUP

NOTES

ALL CAPACITORS ARE IN μ F

CH : CCSQCH
YF : CKSQYF
YB : CKSQYB
SL : CCSQSL
CEAL-TS

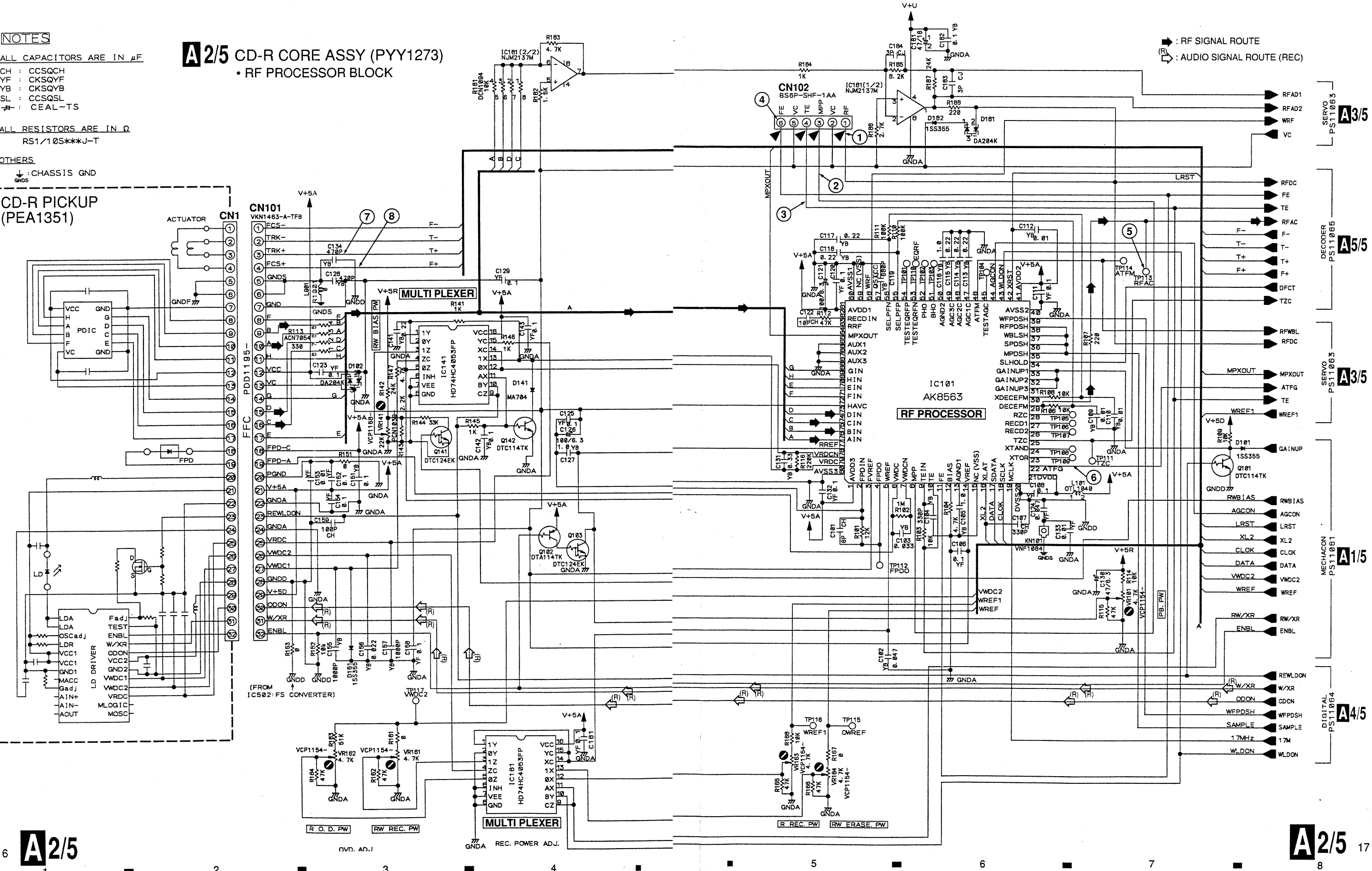
ALL RESISTORS ARE IN Ω
RS1/10S***J-T

OTHERS

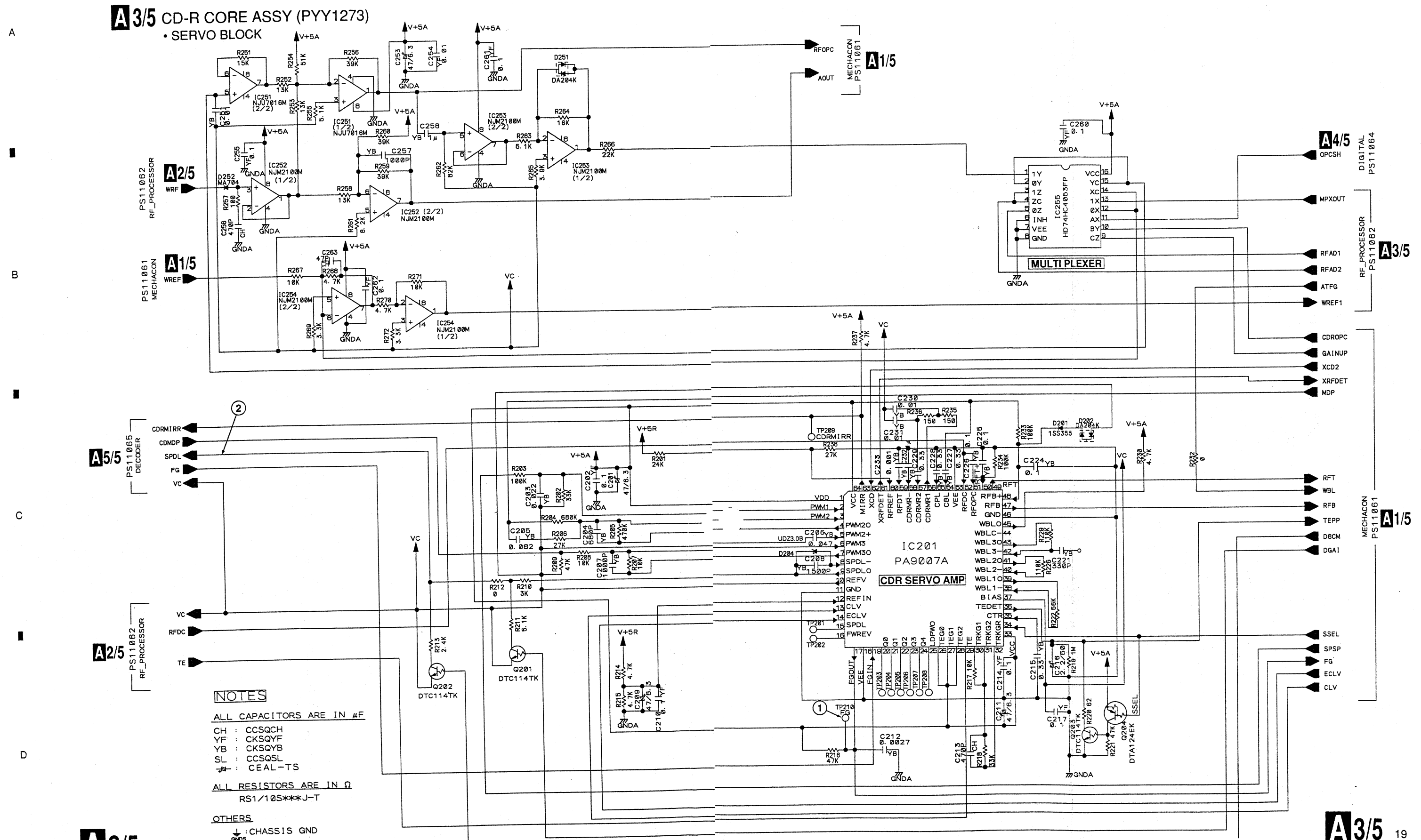
CHASSIS GND

CD-R PICKUP
(PEA1351)

A2/5 CD-R CORE ASSY (PYY1273)
• RF PROCESSOR BLOCK

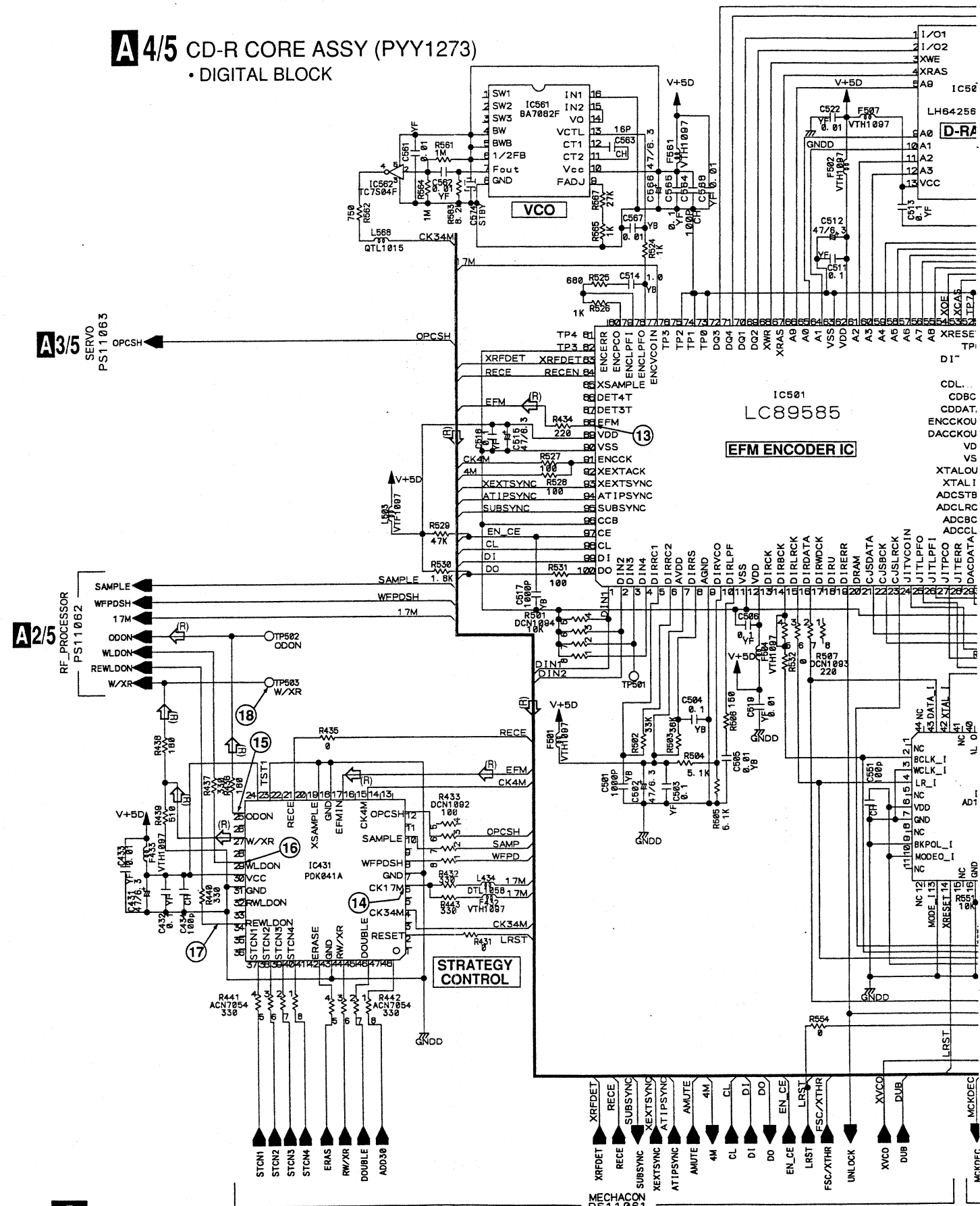


3.5 CD-R CORE ASSY (3/5)

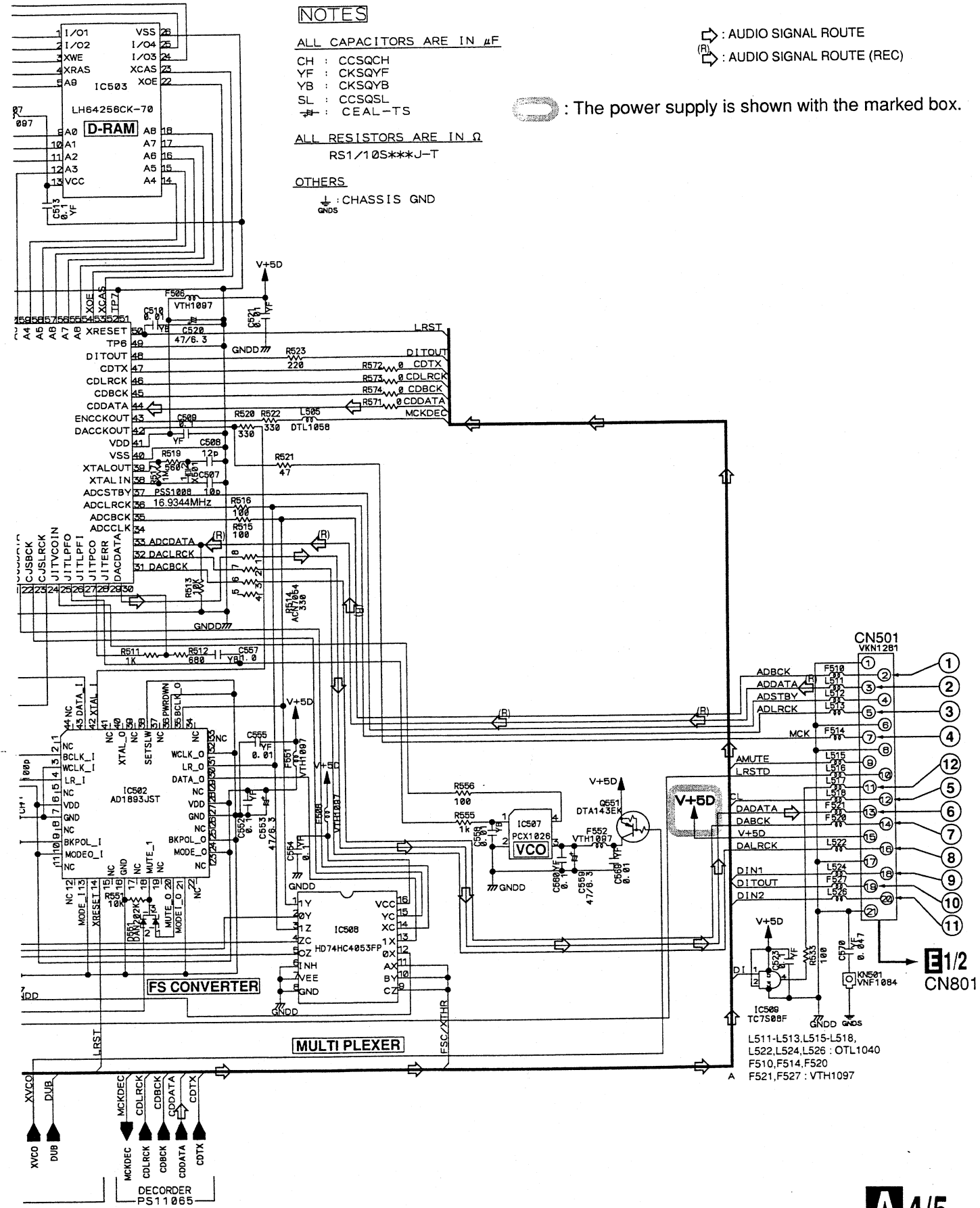


3.6 CD-R CORE ASSY (4/5)

A 4/5 CD-R CORE ASSY (PYY1273)
• DIGITAL BLOCK

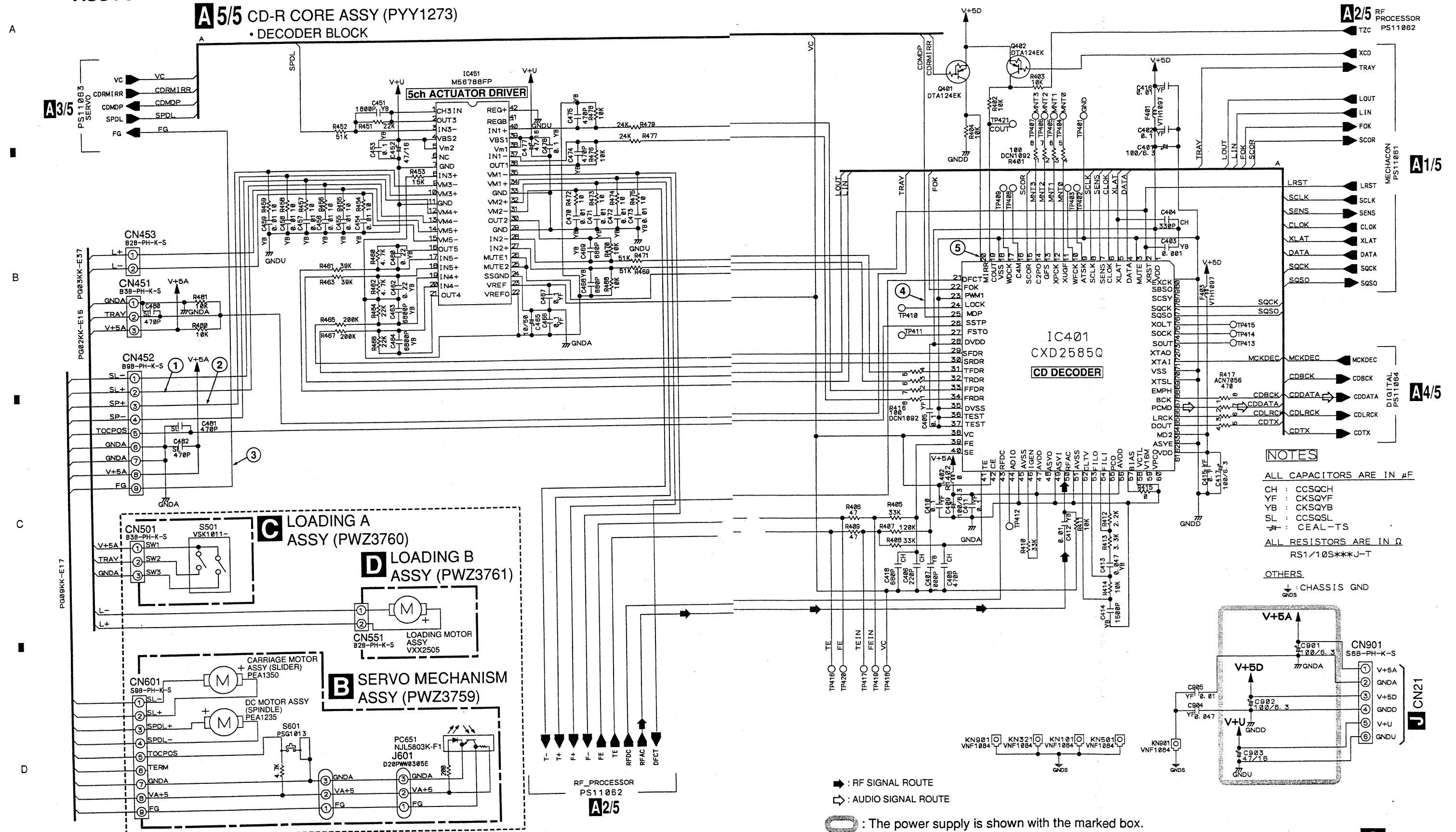


A1/5

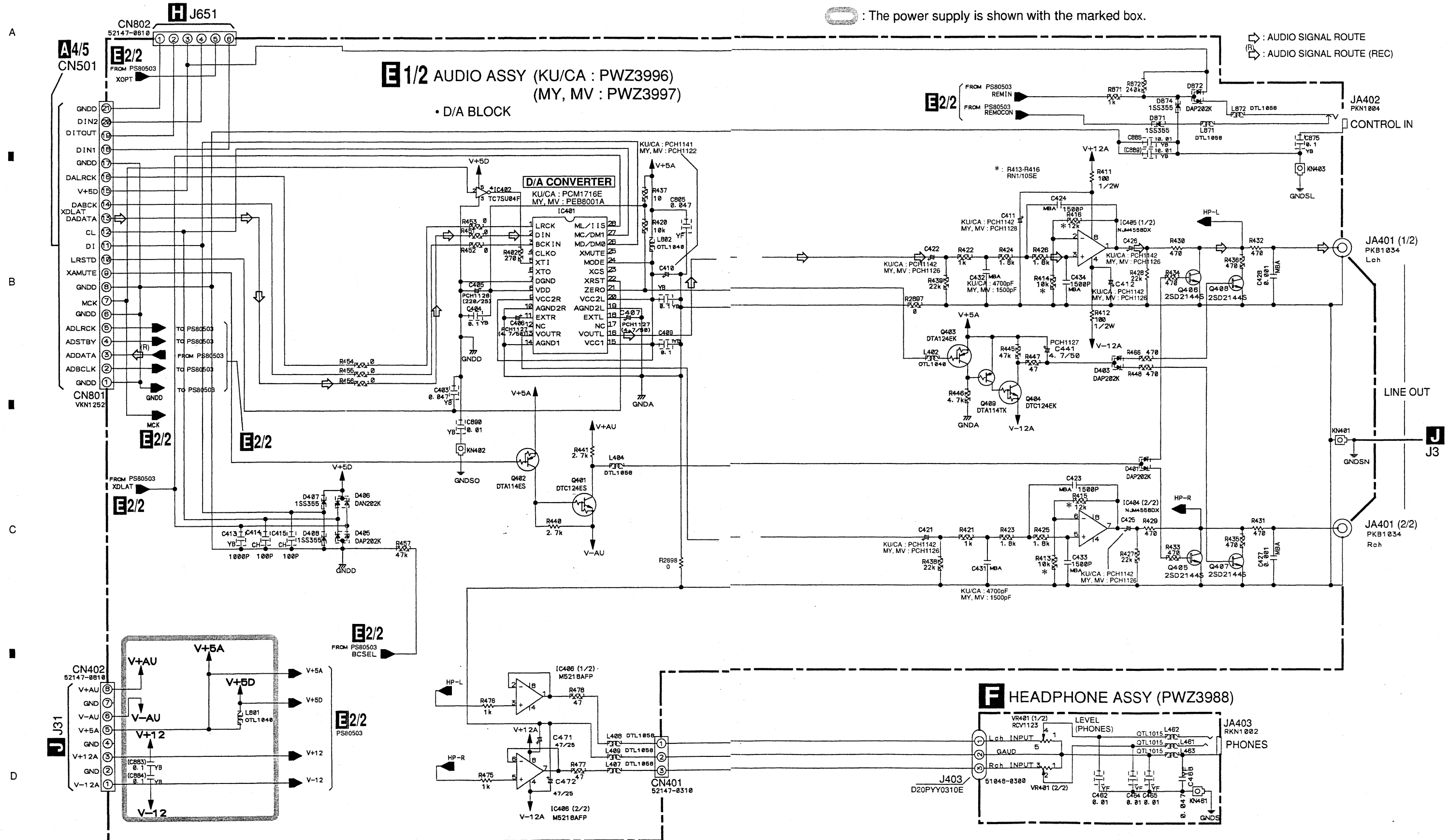


A **4/5** 21

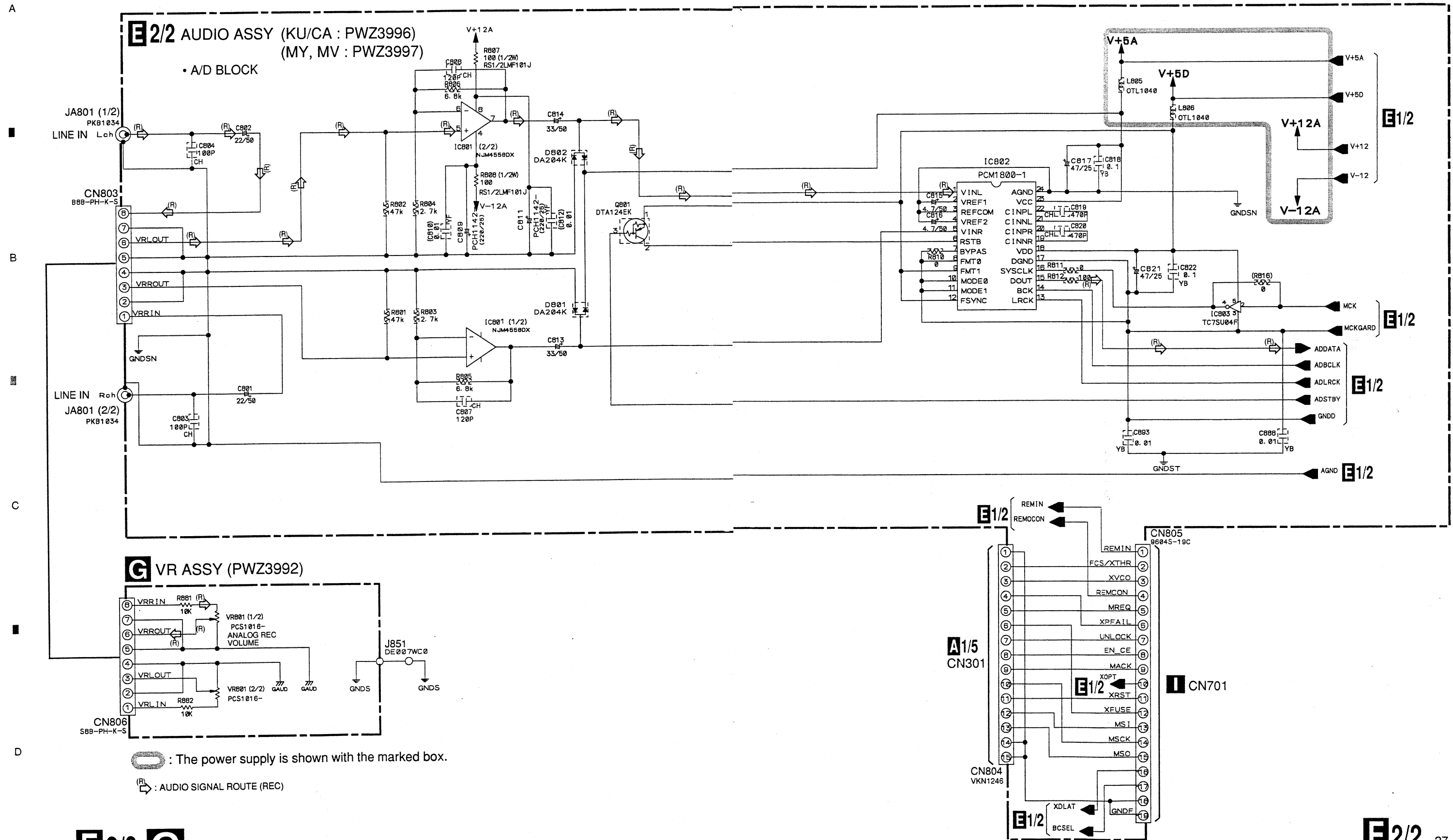
3.7 CD-R CORE (5/5), SERVO MECHANISM, LOADING A and LOADING B ASSYS



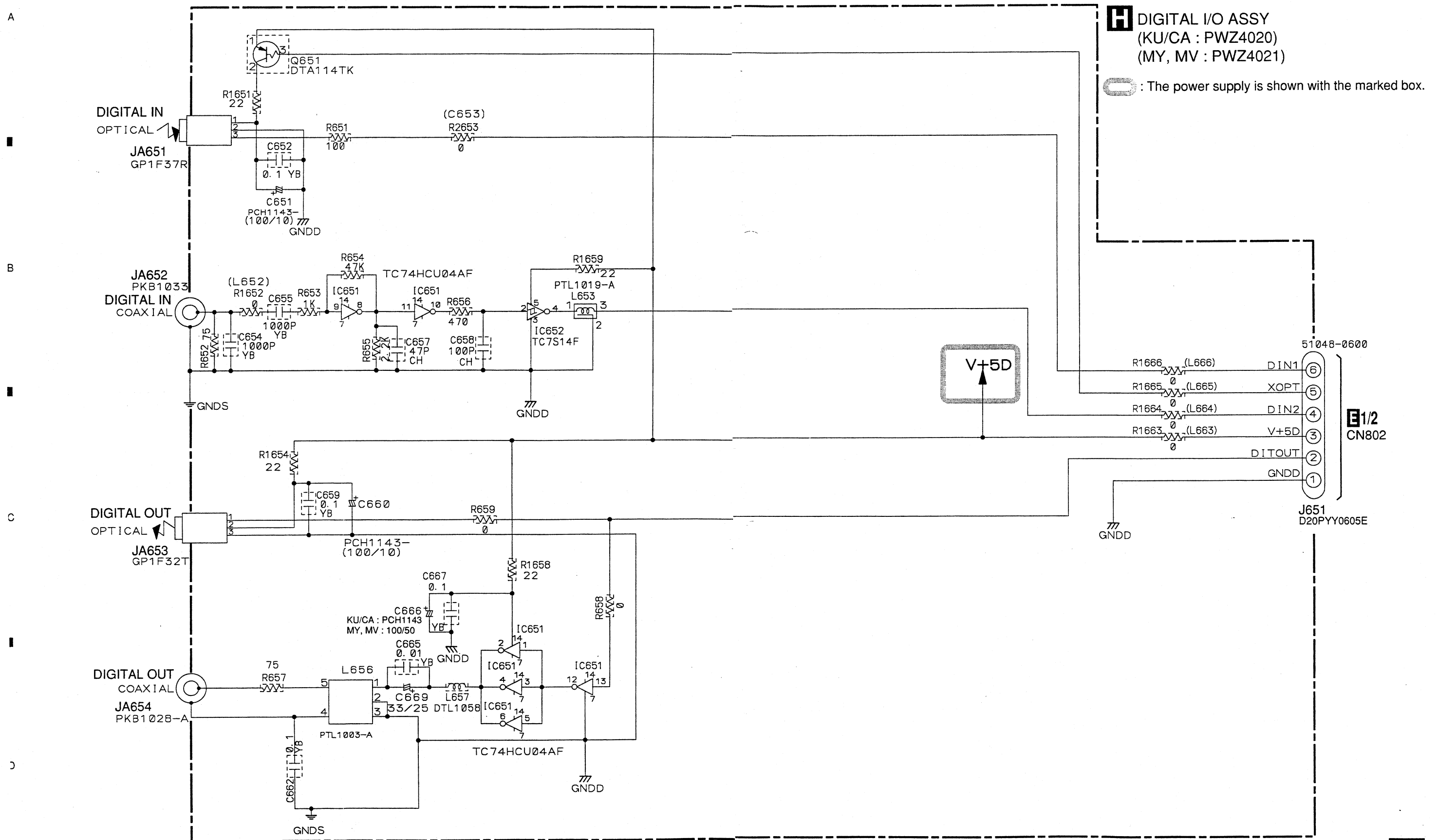
3.8 AUDIO (1/2) and HEADPHONE ASSYS



3.9 AUDIO (2/2) and VR ASSYS




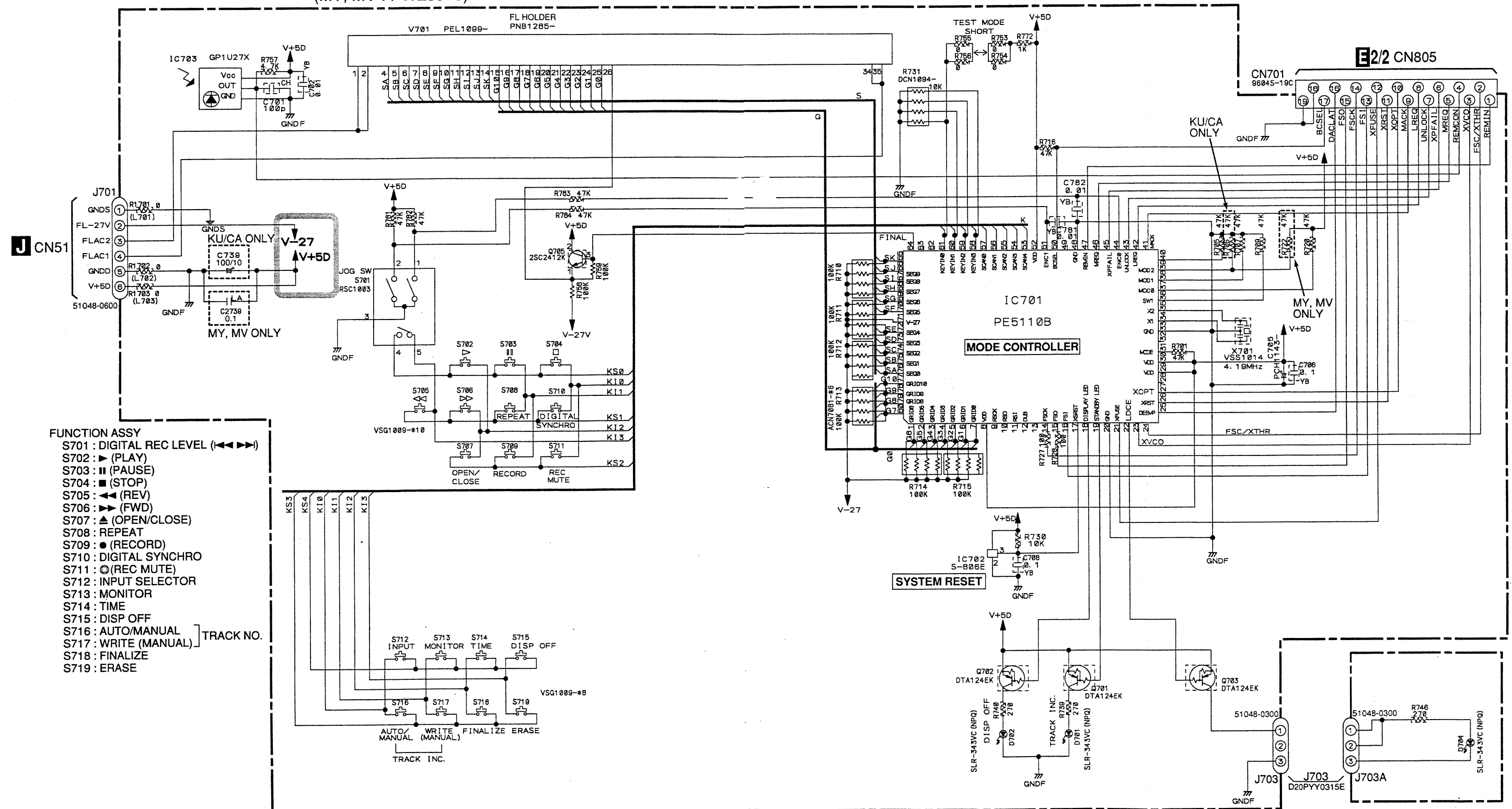
3.10 DIGITAL I/O ASSY



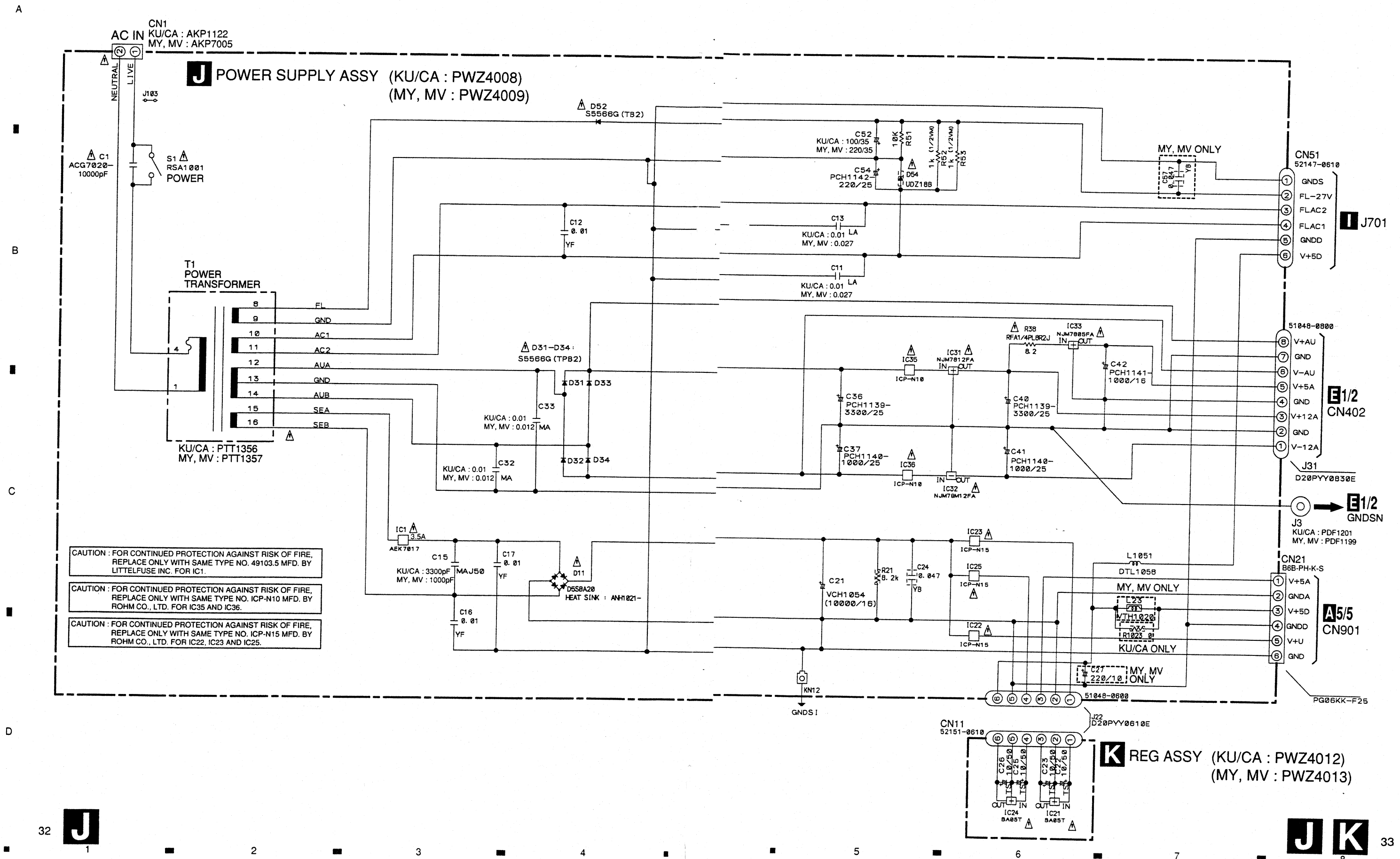
3.11 OPERATING ASSY

I OPERATING ASSY (KU/CA : PWZ3977)
(MY, MV : PWZ3978)

 : The power supply is shown with the marked box.



3.12 POWER SUPPLY and REG ASSYS



VOLTAGES and WAVEFORMS

A1/5 CD-R CORE ASSY

Media	Pickup Position	DGA1 (IC351-pin56)	D8CM (IC351-pin57)
CD	12cm Inner	0V	0V
	12cm Outer	5V	0V
	8cm Inner	0V	0V
	8cm Outer	5V	0V
CD-R CD-RW	12cm Inner	5V	0V
	12cm Outer	5V	0V
	8cm Inner	0V	5V
	8cm Outer	5V	5V

	at FS = 44 kHz (at FS Converter through)	Others
FSR/XTHR (CN301-pin 2)	0V	5V
	at DIGITAL LOCK	at DIGITAL UNLOCK
XVCO (CN301-pin3)	0V	5V
UNLOCK (CN301-pin7)	0V	5V
XPFAIL (CN301-pin4)	5V	-
XRST (CN301-pin11)	5V	-

A3/5 CD-R CORE ASSY

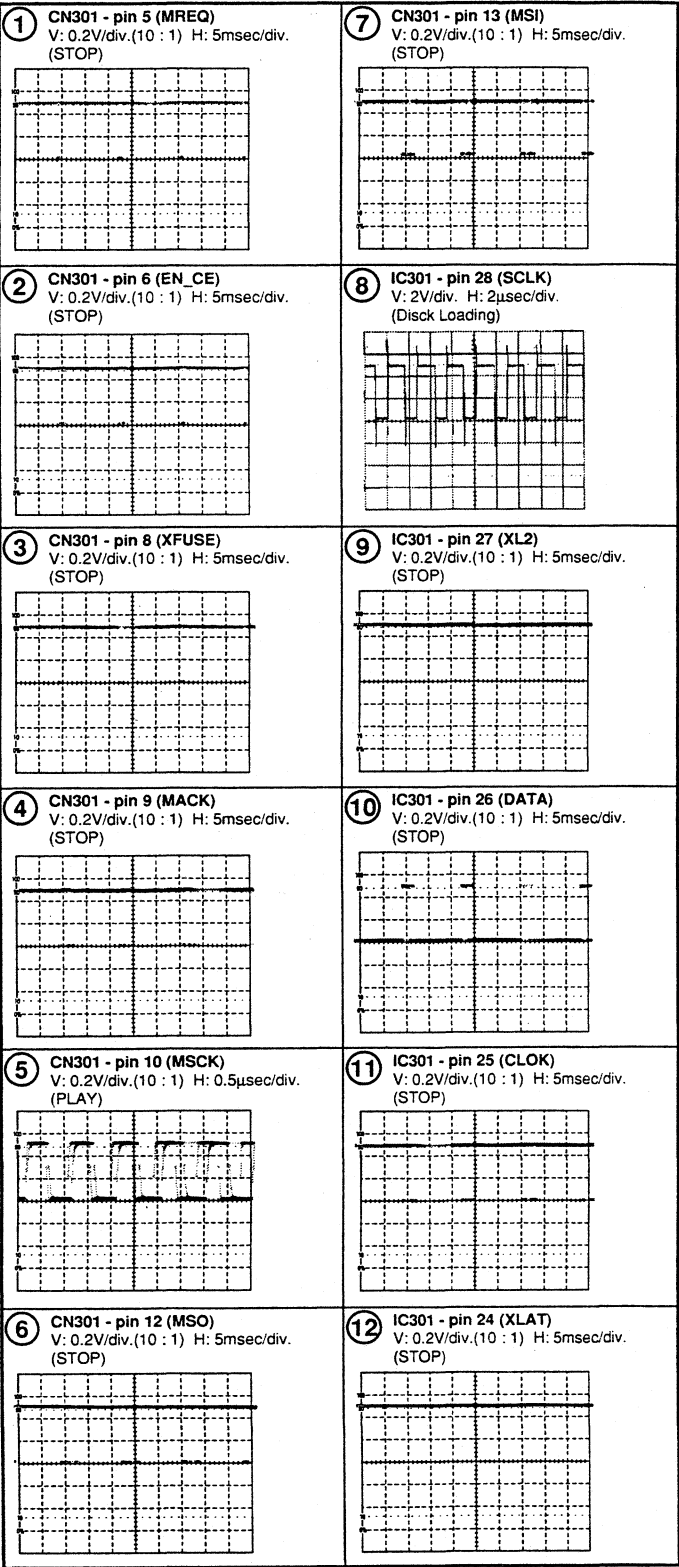
Operating Mode	CLV (IC201-pin13)	ECLV (IC201-pin14)
STOP	0V	0V
CAV	0V	5V
CLV	5V	0V
ECLV	5V	5V

A4/5 CD-R CORE ASSY

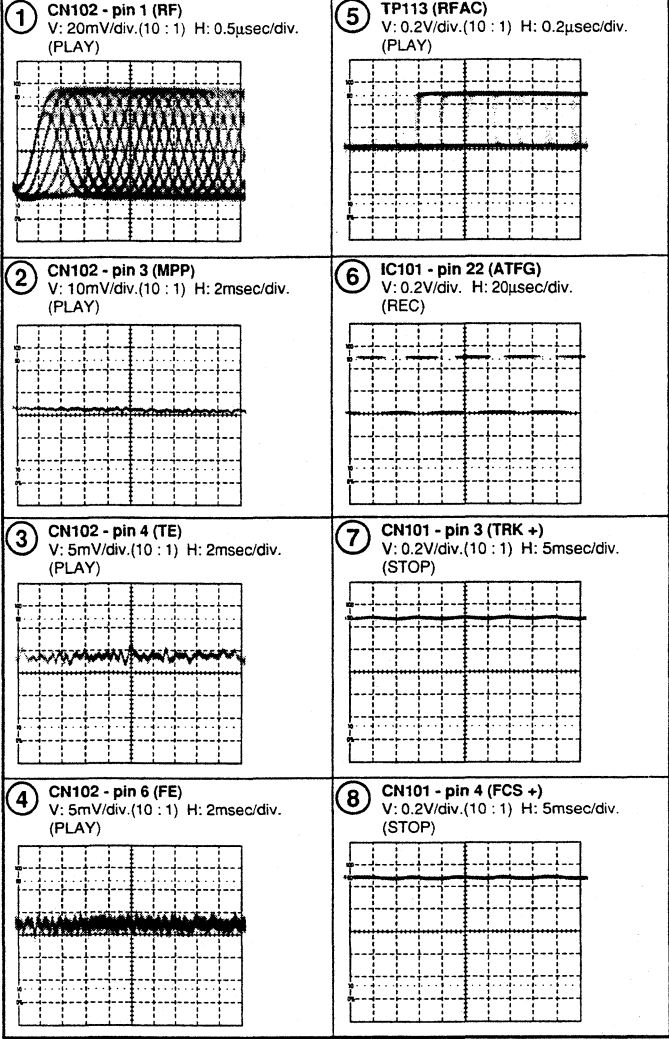
	A/D Converter used	
	at Analog REC Pause or REC	Others
ADSTBY (CN501-pin4)	0V	5V
	at MUTE ON (Audio Signal Not Output)	at MUTE OFF (Audio Signal Output)
AMUTE (CN501-pin9)	5V	0V
LRSTD (CN501-pin10)	5V	-

Note :
The encircled numbers denote measuring point in the schematic diagram.

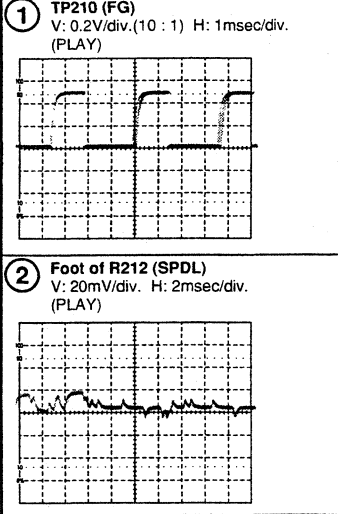
A1/5 CD-R CORE ASSY



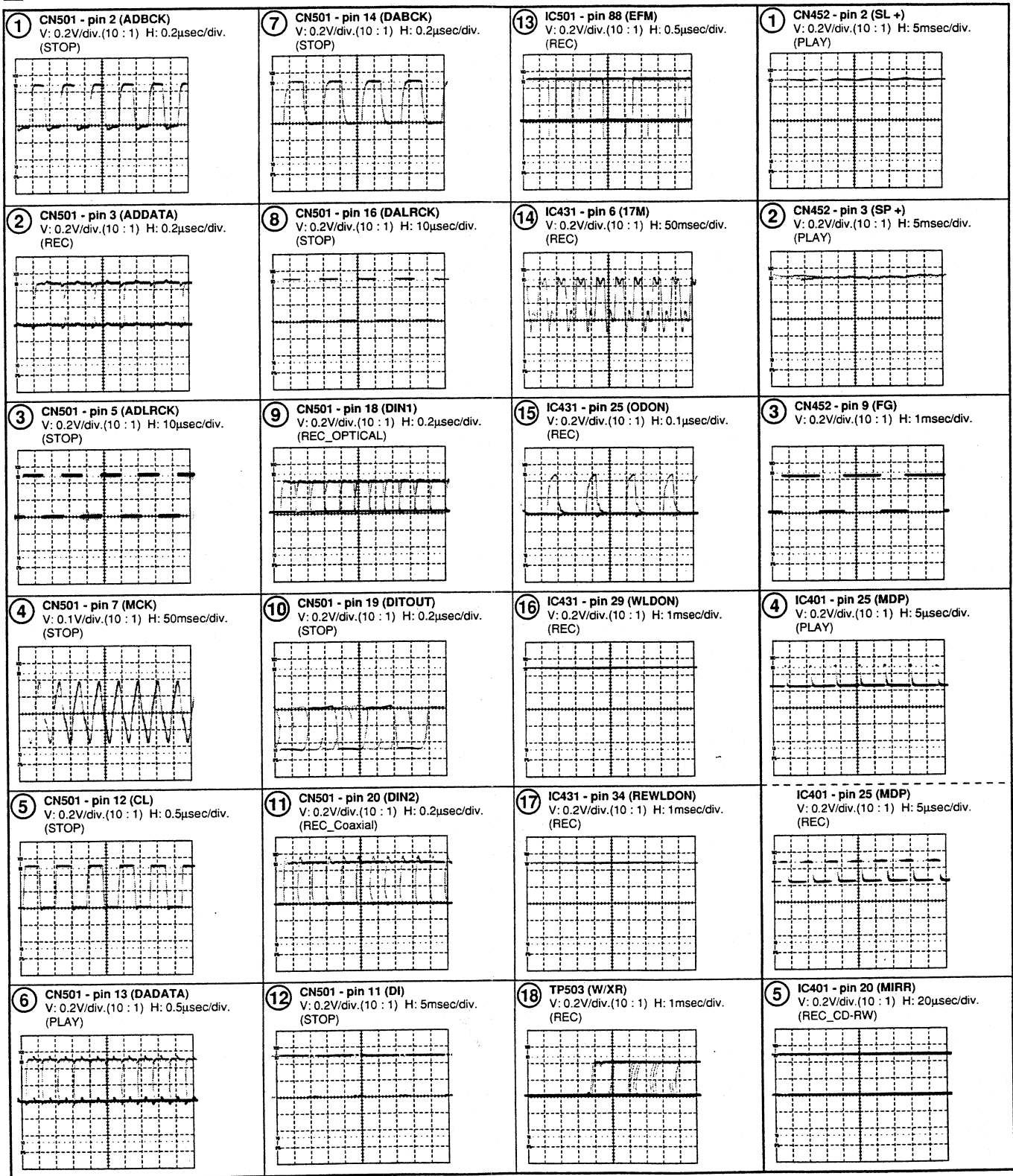
A2/5 CD-R CORE ASSY



A3/5 CD-R CORE ASSY



A4/5 CD-R CORE ASSY



A5/5 CD-R CORE ASSY

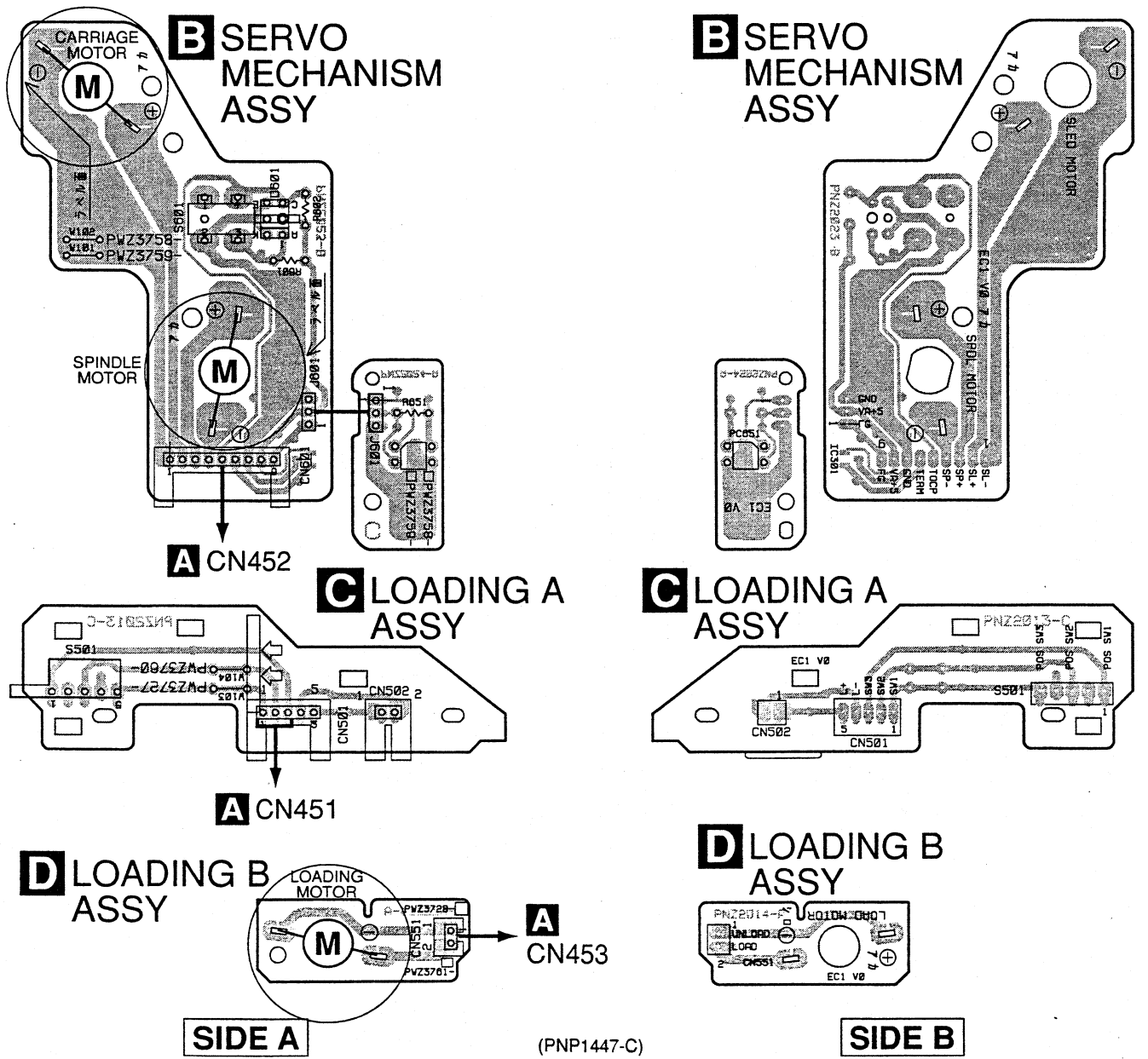
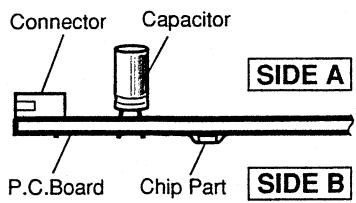
4. PCB CONNECTION DIAGRAM
4.1 SERVO MECHANISM, LOADING A and LOADING B ASSYS

NOTE FOR PCB DIAGRAMS :

1. Part numbers in PCB diagrams match those in the schematic diagrams.
2. A comparison between the main parts of PCB and schematic diagrams is shown below.

Symbol in PCB Diagrams	Symbol in Schematic Diagrams	Part Name
		Transistor
		Transistor with resistor
		Field effect transistor
		Resistor array
		3-terminal regulator

3. The parts mounted on this PCB include all necessary parts for several destinations.
- For further information for respective destinations, be sure to check with the schematic diagram.
4. View point of PCB diagrams.

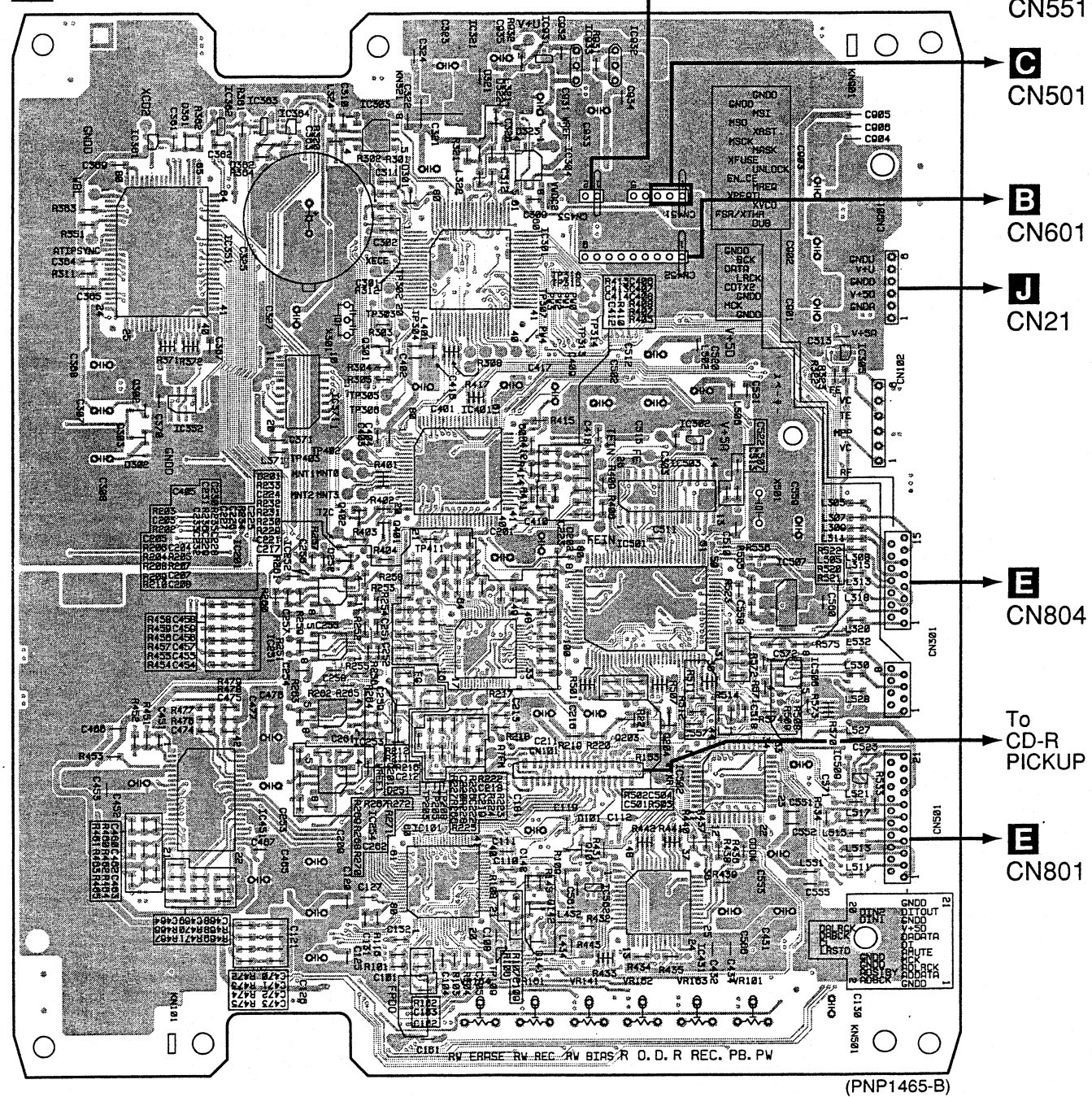


4.2 CD-R CORE ASSY

A

A CD-R CORE ASSY

• This PCB is a four-layered board.



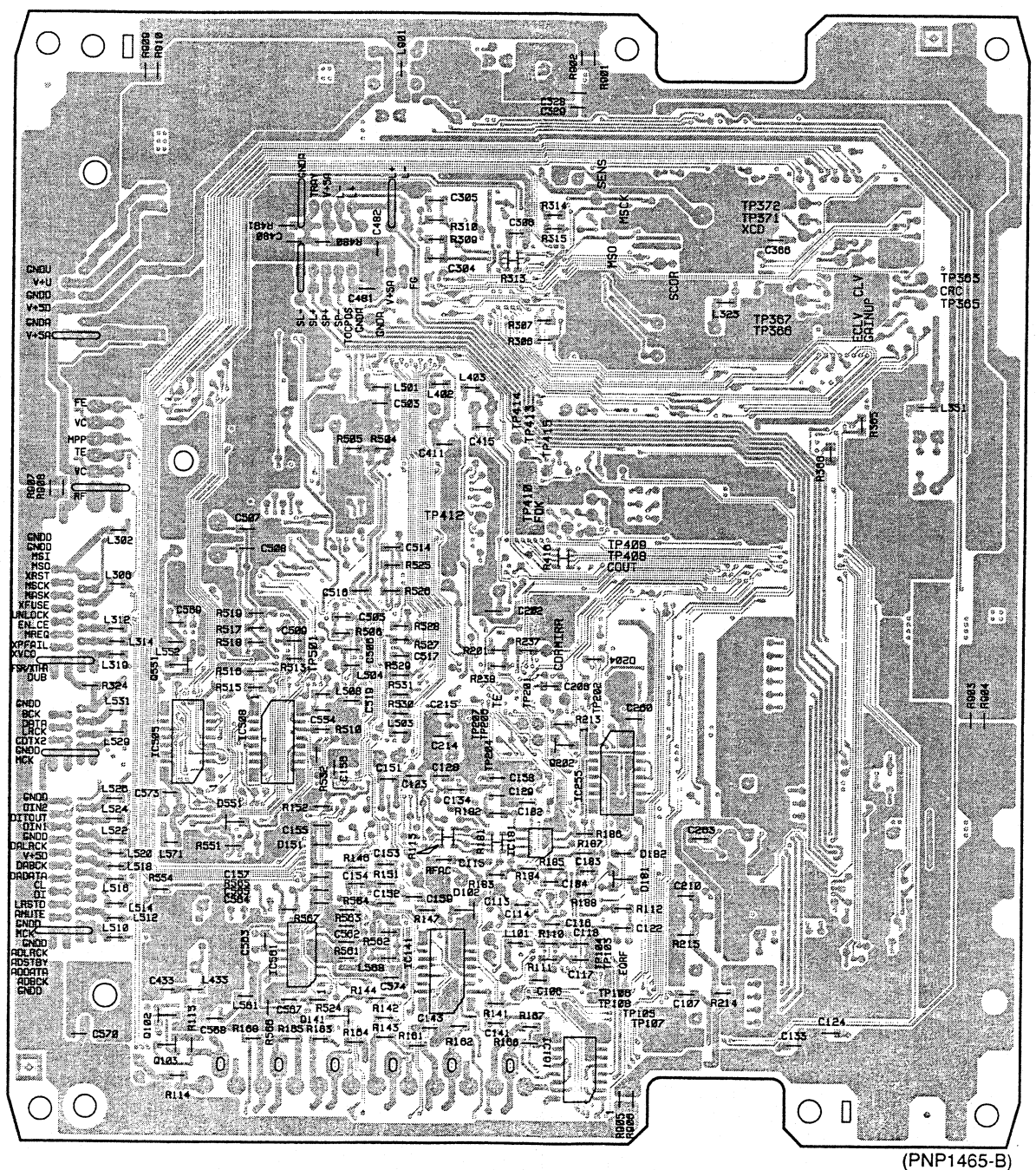
VR164 VR161 VR141 VR162 VR163 VR101

IC361-IC364	IC303	IC931-IC933	IC302	IC305
IC351	Q301	IC301 IC304	IC503	
Q302 Q303 IC352	Q402 Q401	IC401	IC501	IC507 IC506
IC451	IC251-IC254	IC201	Q203 Q203	IC502
	Q201	IC101	Q142	IC562 IC431

SIDE A

A CD-R CORE ASSY

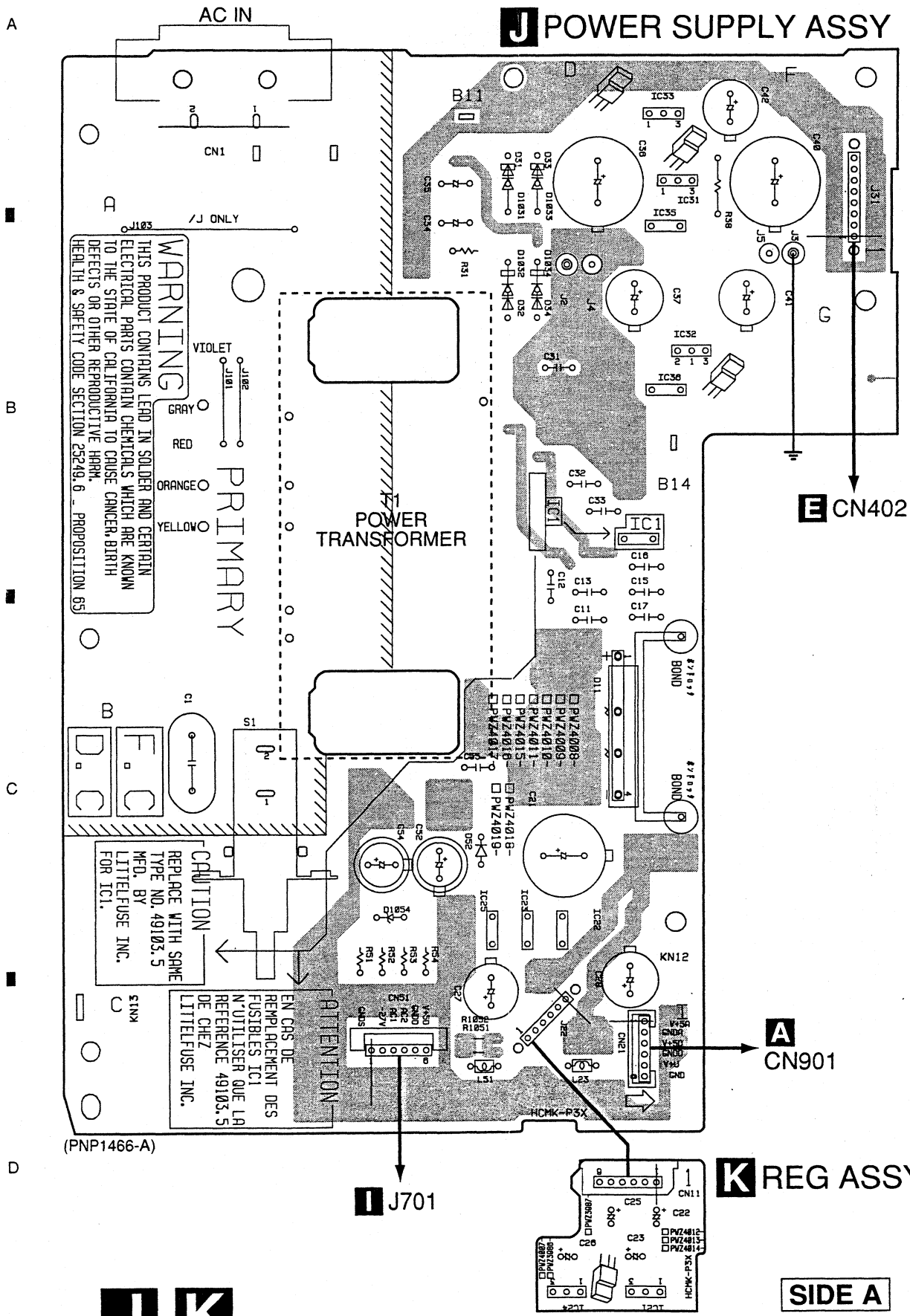
• This PCB is a four-layered board.



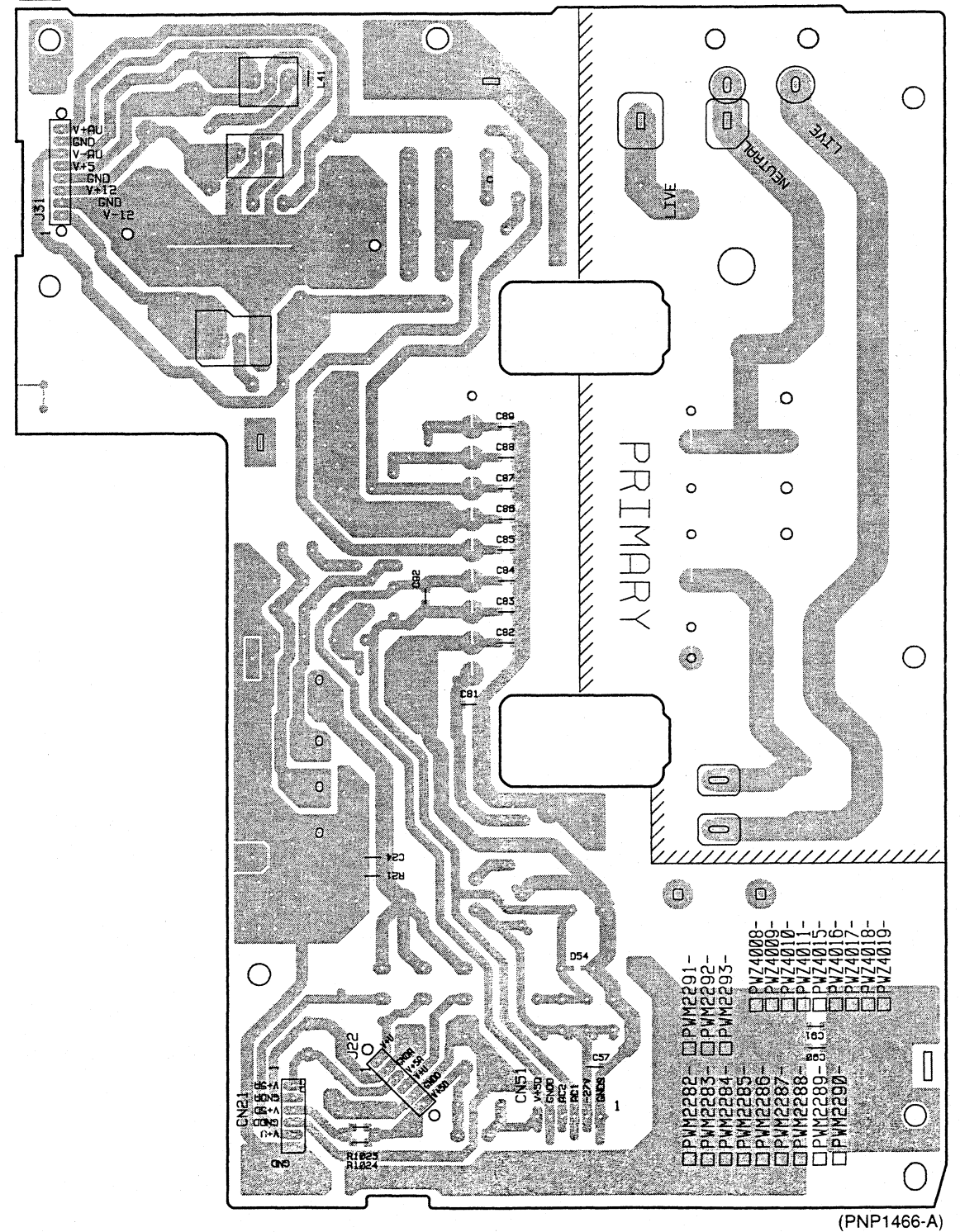
Q551	IC505	IC508	Q202 IC255
IC102	IC561	IC141	IC181
Q103	Q141		IC161

SIDE B

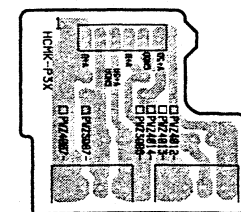
4.5 POWER SUPPLY and REG ASSYS



J POWER SUPPLY ASSY



K REG ASSY

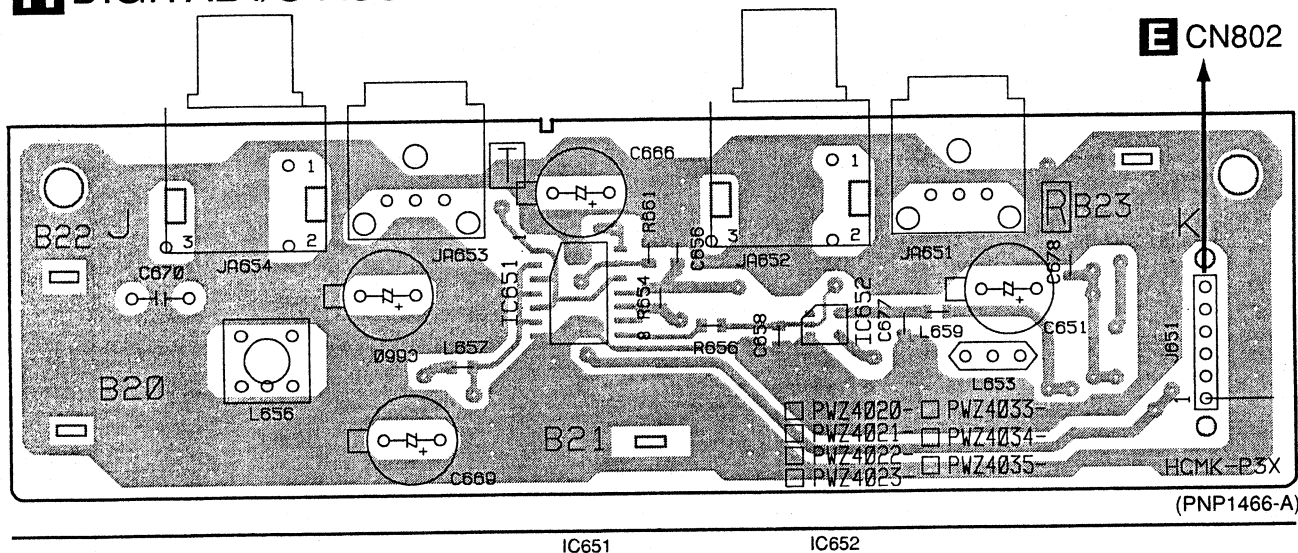


SIDE B

J K

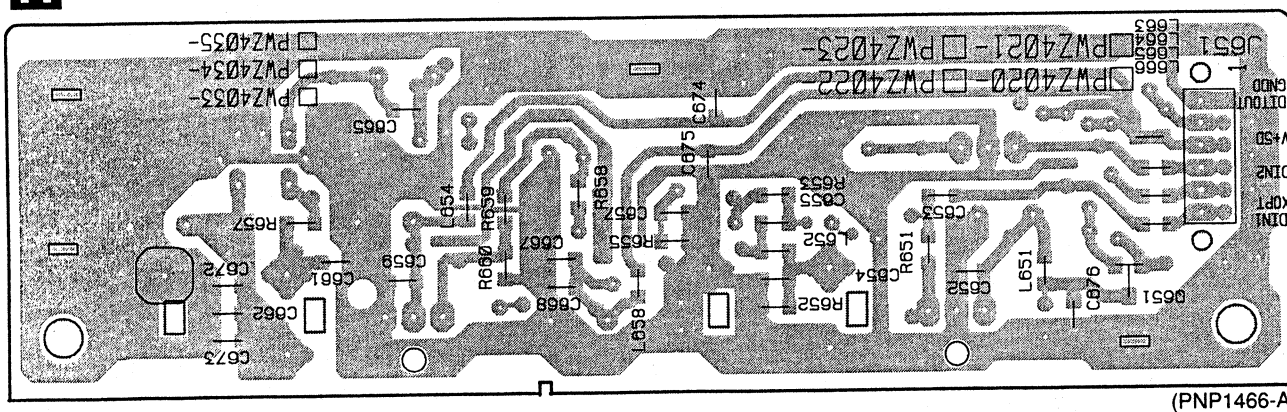
4.6 DIGITAL I/O ASSY

H DIGITAL I/O ASSY



SIDE A

H DIGITAL I/O ASSY



SIDE B

5. PCB PARTS LIST

NOTES: ●Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
●The Δ mark found on some component parts indicates the importance of the safety factor of the part.
Therefore, when replacing, be sure to use parts of identical designation.
●When ordering resistors, first convert resistance values into code form as shown in the following examples.
Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).
560 Ω → 56 × 10¹ → 561RD1/4PU 5 6 1 J
47k Ω → 47 × 10¹ → 473RD1/4PU 4 7 3 J
0.5 Ω → R50RN2H R 5 0 K
1 Ω → 1R0RS1P 1 R 0 K
Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).
5.62k Ω → 562 × 10¹ → 5621RN1/4PC 5 6 2 1 F

■ LIST OF WHOLE PCB ASSEMBLIES

Mark	Symbol and Desacription	Part No.			Remarks
		KU/CA Type	MY Type	MV Type	
NSP	CD-R CORE ASSY	PYY1273	PYY1273	PYY1273	
	MECHANISM ASSY	PWX1570	PWX1570	PWX1570	
	— SERVO MECHANISM ASSY	PWZ3759	PWZ3759	PWZ3759	
	— LOADING A ASSY	PWZ3760	PWZ3760	PWZ3760	
NSP	— LOADING B ASSY	PWZ3761	PWZ3761	PWZ3761	
NSP	MAIN ASSY	PWM2282	PWM2283	PWM2283	
	— AUDIO ASSY	PWZ3996	PWZ3997	PWZ3997	
	— HEADPHONE ASSY	PWZ3988	PWZ3988	PWZ3988	
	— VR ASSY	PWZ3992	PWZ3992	PWZ3992	
	— DIGITAL I/O ASSY	PWZ4020	PWZ4021	PWZ4021	
	— OPERATING ASSY	PWZ3977	PWZ3978	PWZ3978	
	— POWER SUPPLY ASSY	PWZ4008	PWZ4009	PWZ4009	
	— REG ASSY	PWZ4012	PWZ4013	PWZ4013	

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
A	CD-R CORE ASSY				IC361,IC364		TC7S14F
	SEMICONDUCTORS				IC302		TK11041M-1
	Δ	IC502	AD1893JST		Q102		DTA1141TK
		IC101	AK8563		Q204,Q401,Q402		DTA124EK
		IC321	BA05FP		Q551		DTA143EK
		IC561	BA7082F				
		IC303 (BR93LC46F)	PYY1196		Q101,Q142,Q201-Q203		DTC114TK
		IC401	CXD2585Q		Q103,Q141,Q301-Q303		DTC124EK
		IC141,IC161,IC255,IC508	HD74HC4053FP		D101,D151,D182,D201,D302		1SS355
		IC371	HD74HC573FP		D321,D322,D361,D362		1SS355
	IC501	LC89585		D102,D181,D202,D251		DA204K	
Δ	IC503	LH64256CK-70		D551		DAN202K	
	IC931	LP2980IM5-5.0		D301		DAP202K	
	IC451	M56788FP		D141,D252,D323		MA704	
	IC252-IC254	NJM2100M		D204		UD23.0B	
	IC181	NJM2137M		COILS AND FILTERS			
	IC304	NJM2904M		L434,L505 CHIP COIL		DTL1058	
	IC251	NJU7016M		L101,L302,L305-L309		OTL1040	
	IC201	PA9007A		CHIP SOLID INDUCTOR			
	IC507	PCX1026		L311-L315,L318,L319		OTL1040	
	IC351	PDJ014A		CHIP SOLID INDUCTOR			
	IC431	PK041A		L322-L324,L351,L371		OTL1040	
	IC301	PE5109A		CHIP SOLID INDUCTOR			
	IC932	PST994C		L511-L513,L515-L518,L522		OTL1040	
	IC362	TC7S00F		CHIP SOLID INDUCTOR			
	IC363,IC562	TC7S04F		L524,L526 CHIP SOLID INDUCTOR		OTL1040	
	IC305,IC509	TC7S08F					

Mark	No.	Description	Part No.
	L568	CHIP SOLID INDUCTOR	QTL1015
	F401,F403,F432,F433	CHIP SOLID INDUCTOR	VTF1097
	F501-F504,F506-F508,F510	CHIP SOLID INDUCTOR	VTF1097
	F514,F520,F521,F527	CHIP SOLID INDUCTOR	VTF1097
	F551,F552,F561	CHIP SOLID INDUCTOR	VTF1097

CAPACITORS

C122,C507	CCSQCH100D50
C159,C434,C551,C564	CCSQCH101J50
C508	CCSQCH120J50
C563	CCSQCH160J50
C406	CCSQCH221J50
C107,C404	CCSQCH331J50
C263,C311	CCSQCH470J50
C213,C256,C408	CCSQCH471J50
C418	CCSQCH681J50
C101	CCSQCH680D50
C518	CCSQCH820J50
C183,C184	CCSQCHJ3R0C50
C480-C482	CCSQSL471J50
C321,C465,C931	CEAL100M50
C121,C126,C327,C401,C409	CEAL101M6R3
C417,C901,C902	CEAL101M6R3
C307	CEAL1R0M50
C216	CEAL2R2M50
C933	CEAL3R3M50
C181,C452,C477,C903	CEAL470M16
C130,C201,C209,C211,C253	CEAL470M6R3
C323,C368,C431,C502,C512	CEAL470M6R3
C515,C520,C553,C559,C566	CEAL470M6R3
C308	CEAL4R7M50
C155,C157,C207,C233	CKSQYB102K50
C257,C403,C407,C501,C517	CKSQYB102K50
C109,C110,C112,C230,C231	CKSQYB103K50
C251,C324,C412,C454-C459	CKSQYB103K50
C470-C473,C505,C510,C558	CKSQYB103K50
C567,C934	CKSQYB103K50
C142,C151,C182,C224-C226	CKSQYB104K25
C361,C364,C453,C476	CKSQYB104K25
C504,C932	CKSQYB104K25
C105,C116,C127,C232,C258	CKSQYB105K10
C514,C557	CKSQYB105K10
C208,C414	CKSQYB152K50
C451	CKSQYB182K50
C156,C203	CKSQYB223K50
C113-C115,C117,C118,C141	CKSQYB224K16
C304,C305,C460,C462	CKSQYB224K16
C212	CKSQYB272K50
C104,C221	CKSQYB331K50
C103	CKSQYB333K50
C131,C215,C227-C229	CKSQYB334K16
C128,C134,C363,C474,C475	CKSQYB471K50
C206,C413	CKSQYB473K25
C102	CKSQYB473K50
C119,C204,C468,C469	CKSQYB681K50
C463,C464	CKSQYB682K50
C205	CKSQYB823K25

Mark	No.	Description	Part No.
	C133,C153,C254,C303,C322		CKSQYF103Z50
	C362,C416,C433,C519		CKSQYF103Z50
	C521,C522,C555,C561,C562		CKSQYF103Z50
	C568,C569,C905		CKSQYF103Z50
	C106,C108,C111,C120,C123		CKSQYF104Z25
	C125,C129,C132,C143,C152		CKSQYF104Z25
	C154,C158,C161,C202,C210		CKSQYF104Z25
	C214,C217,C255,C260-C262		CKSQYF104Z25
	C301,C302,C306,C309,C310		CKSQYF104Z25
	C312,C313,C326,C365-C367		CKSQYF104Z25

RESISTORS

R113,R441,R442,R514 (330Ω)	ACN7054
R308,R417 (470Ω)	ACN7056
R313 (47kΩ)	ACN7077
R401,R416,R433 (100Ω)	DCN1092
R507 (220Ω)	DCN1093
R181,R371,R372,R501 (10kΩ)	DCN1094
R570 (0Ω)	DCN1106
R143 (2.2kΩ)	PCN1039
VR101,VR161-VR164 (4.7kΩ)	VCP1154
VR141 (22kΩ)	VCP1158
Other Resistors	RS1/10S□□□□

OTHERS

X501	CRYSTAL (16.9344MHz)	PSS1008
X301	CERAMIC (32MHz)	PSS1023
CN453	KR CONNECTOR	B2B-PH-K-S
CN451	KR CONNECTOR 3P	B3B-PH-K-S
CN901	KR CONNECTOR	S6B-PH-K-S
CN301	15P FFC CONNECTOR	VKN1275
CN501	21P FFC CONNECTOR	VKN1281
CN101	32P FFC CONNECTOR	VKN1463
KN101,KN321,KN501,KN901	EARTH METAL FITTING	VNF1084

SERVO MECHANISM ASSY

SEMICONDUCTOR

PC651	NJL5803K-F1
-------	-------------

SWITCH

S601	PSG1013
------	---------

RESISTORS

All Resistors	RD1/4PU□□□□
---------------	-------------

OTHERS

J601	JUMPER WIRE	D20PWW0305E
CN601	KR CONNECTOR	S9B-PH-K-S

Mark	No.	Description	Part No.
		C LOADING A ASSY	
		SWITCH	
	S501		VSK1011

OTHERS

CN501	KR CONNECTOR	S3B-PH-K-S
-------	--------------	------------

D LOADING B ASSY

OTHERS

CN551	KR CONNECTOR	B2B-PH-K-S
-------	--------------	------------

E AUDIO ASSY

(1) CONTRAST TABLE

PWZ3996 and PWZ3997 are constructed the same except for the following :

Mark	Symbol and Description	Part No.		Remarks
		PWZ3996	PWZ3997	
IC401	PCM1716E	PE8001A	PE8001A	
C410	PCM1141	PCM1122	PCM1122	
	(1000μF/16V)	(1000μF/16V)	(1000μF/16V)	
C411, C412	PCM1142	PCM1128	PCM1128	
	(220μF/25V)	(220μF/25V)	(220μF/25V)	
C421, C422, C425, C426	PCM1124	PCM1126	PCM1126	
	(47μF/50V)	(100μF/50V)	(100μF/50V)	
C431, C432	QCMBA472J50	QCMBA152J50	QCMBA152J50	

(2) PARTS LIST FOR PWZ3996

SEMICONDUCTORS

IC406	M5218AFP
IC404,IC801	NJM4558DX
IC401	PCM1716E
IC802	PCM1800-1
IC402,IC803	TC7SU04F
Q405-Q408	2SD2144S
Q402	DTA114ES
Q409	DTA114TK
Q403,Q801	DTA124EK
Q404	DTC124EK
Q401	DTC124ES
D407,D408,D871,D874	1S355
D801,D802	DA204K
D406	DAN202K
D401,D403,D405,D872	DAP202K

COILS

L404,L407-L409,L871,L872	DTL1058
CHIP COIL	
L402,L801,L802,L805,L806	OTL1040
CHIP SOLID INDUCTOR	

CAPACITORS

C414,C415,C803,C804	CCSQCH101J50
C807,C808	CCSQCH121J50
C819,C820	CCSQCH471J50
C801,C802	CEAT220M50
C813,C814	CEAT330M50

Mark	No.	Description	Part No.
	C471,C472,C817,C821		CEAT470M25
	C815,C816		CEAT4R7M50
	C413		CKSQYB102K50
	C885,C888-C890,C893		CKSQYB103K50
	C408,C409,C818,C822,C875		CKSQYB104K25
	C403,C404		CKSQYB473K50
	C805		CKSQYF473Z50
	C427,C428		QCMBA102J50
	C423,C424,C433,C434		QCMBA152J50
	C431,C432		QCMBA472J50

C421,C422,C425,C426 (47μF/50V)	PCH1124
C406,C407,C441 (4.7μF/50V)	PCH1127
C405 (220μF/25V)	PCH1128
C410 (1000μF/16V)	PCH1141
C411,C412,C809,C811 (220μF/25V)	PCH1142

RESISTORS

R440	RD1/2VM272J
R475,R476	RD1/4VM102J
R427,R428	RD1/4VM223J
R429-R432	RD1/4VM471J
R411,R412	RDR1/2PM101J
R413,R414	RN1/10SE1002D
R415,R416	RN1/10SE1202D
R807,R808	RS1/2LMF101J
R441	RS1LMF272J
Other Resistors	RS1/10S□□□□

OTHERS

CN401	3P JUMPER CONNECTOR	52147-0310
CN802	6P JUMPER CONNECTOR	52147-0610
CN402	8P JUMPER CONNECTOR	52147-0810
CN805	19P FFC CONNECTOR	9604S-19C
JA401,JA801	2P PIN JACK	PKB1034
JA402	REMOTE CONTROL JACK	PKN1004
	PCB BINDER	VEF1040
CN804	15P FFC CONNECTOR	VKN1246
CN801	21P FFC CONNECTOR	VKN1252
KN401,KN402,KN404	EARTH METAL FITTING	VNF1084

F HEADPHONE ASSY

COILS

L461-L463	CHIP SOLID INDUCTOR	QTL1015
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CAPACITORS

C468	CKCYF473Z50
C462,C464,C465	CKSQYB103K50

RESISTOR

VR401	VARIABLE 500B	RCV1123
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OTHERS

J403	3P CABLE HOLDER	51048-0300
JA403	JUMPER WIRE 3P	D20PY0310E
KN410	HEADPHONE JACK	RKN1002
	EARTH METAL FITTING	VNF1084

PDR-509

Mark	No.	Description	Part No.
G		VR ASSY	
		RESISTORS	
	VR801	VARIABLE	PCS1016
	Other Resistors		RS1/10S□□□□

H DIGITAL I/O ASSY

(1) CONTRAST TABLE

PWZ4020 and PWZ4021 are constructed the same except for the following :

Mark	Symbol and Description	Part No.		Remarks
		PWZ4020	PWZ4021	
	C666	PCH1143 (100μF/10V)	CEAT101M50	

(2) PARTS LIST FOR PWZ4020

SEMICONDUCTORS

IC651	TC74HCU04AF
IC652	TC7S14F
Q651	DTA114TK

COILS

L657	CHIP COIL	DTL1058
L656	PULSE TRANS.	PTL1003
L653	EMI FILTER	PTL1019

CAPACITORS

C658	CCSQCH101J50
C657	CCSQCH470J50
C669	CEAT330M25
C654	CKSQYB102K50
C655,C665	CKSQYB103K50
C652,C659,C662,C667	CKSQYB104K25
C651,C660,C666 (100μF/10V)	PCH1143

RESISTORS

All Resistors	RS1/10S□□□□
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OTHERS

J651	6P CABLE HOLDER	51048-0600
JA653	JUMPER WIRE 6P	D20PYY0605E
JA651	OPTICAL LINK OUT	GP1F32T
JA654	OPTICAL RECEIVE MOD.	GP1F37R
	1P JACK (ORG)	PKB1028
JA652	1P JACK (ORG)	PKB1033
	PCB BINDER	VEF1040

Mark	No.	Description	Part No.
I		OPERATING ASSY	
		(1) CONTRAST TABLE	
		PWZ3977 and PWZ3978 are constructed the same except for the following :	

Mark	Symbol and Description	Part No.		Remarks
		PWZ3977	PWZ3978	
	C739	CEAT101M10	Not used	
	C2739	Not used	CFTLA104J50	
	R706	RS1/10S473J	Not used	
	R722	Not used	RS1/10S473J	

(2) PARTS LIST FOR PWZ3977

SEMICONDUCTORS

IC701	PE5110B
IC702	S-806E
Q705	2SC2412K
Q701-Q703	DTA124EK
D701,D702,D704	SLR-343VC(NPQ)

SWITCHES

S701	RSC1003
S702-S719	VSG1009

CAPACITORS

C701	CCSQCH101J50
C739	CEAT101M10
C702,C781,C782	CKSQYB103K50
C706,C708	CKSQYB104K25
C705 (100μF/10V)	PCH1143

RESISTORS

R710-R715 (100kΩ)	ACN7081
R731 (10kΩ)	DCN1094
Other Resistors	RS1/10S□□□□

OTHERS

	3P CABLE HOLDER	51048-0300
	6P CABLE HOLDER	51048-0600
CN701	19P FFC CONNECTOR	9604S-19C
J703	JUMPER WIRE 3P	D20PYY0315E
J701	JUMPER WIRE 6P	D20PYY0615E
IC703	REMOTE RECEIVER UNIT	GP1U27X
V701	FL TUBE	PEL1099
X701	CERAMIC (4.19MHz)	VSS1014

Mark	No.	Description	Part No.
J		POWER SUPPLY ASSY	
		(1) CONTRAST TABLE	
		PWZ4008 and PWZ4009 are constructed the same except for the following :	

Mark	Symbol and Description	Part No.		Remarks
		PWZ4008	PWZ4009	
	L23	Not used	VTH1020	
	C11, C13	CKCYF103Z50	CFTLA273J50	
	C15	CKCYB332K2H	QOMA102J50	
	C27	Not used	CEAT221M10	
	C32, C33	QOMA103K2E	QOMA123K2E	
	C52	CEAT101M35	CEAT221M35	
	R1023	RS1/10S0R0J	Not used	
	CN1 1P AC INLET	AKP1122	AKP7005	
	J3 EARTH LEAD UNIT	PDF1201	PDF1199	

(2) PARTS LIST FOR PWZ4008

SEMICONDUCTORS

Δ IC1 (3.5A)	AEK7017
Δ IC35,IC36	ICP-N10
Δ IC22,IC23,IC25	ICP-N15
Δ IC33	NJM7805FA
Δ IC31	NJM7812FA
Δ IC32	NJM79M12FA
Δ D11	D5SBA20
Δ D31-D34,D52	S5566G
Δ D54	UDZ18B

COIL

L1051 CHIP COIL	DTL1058
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SWITCH

Δ S1	RSA1001
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CAPACITORS

Δ C1 (10000pF/AC250V)	ACG7020
C52	CEAT101M35
C11-C13,C16,C17	CKCYF103Z50
C15	CKCYB332K2H
C24,C57	CKSQYB473K50
C32,C33	QOMA103K2E
C36,C40 (3300μF/25V)	PCH1139
C37,C41 (1000μF/25V)	PCH1140
C42 (1000μF/16V)	PCH1141
C54 (220μF/25V)	PCH1142
C21 (10000μF/16V)	VCH1054

RESISTORS

R52,R53	RD1/2VM102J
R51	RD1/4VM103J
R38	RFA1/4PL8R2J
Other Resistors	RS1/10S□□□□

OTHERS

	6P CABLE HOLDER	51048-0600
	8P CABLE HOLDER	51048-0800
Δ CN51	6P JUMPER CONNECTOR	52147-0610
CN1	1P AC INLET	AKP1122
	HEAT SINK B	ANH1021
CN21	KR CONNECTOR	B6B-PH-K-S

Mark	No.	Description	Part No.
		REG ASSY	
		Although MY, MV and KU/CA types are different in part number of REG Assy, they consist of the same components.	
		SEMICONDUCTORS	
	Δ IC21,IC24		BA05T
		CAPACITORS	
	C22,C23,C25,C26		CEAT100M50
		OTHERS	
	CN11	6P JUMPER CONNECTOR	52151-0610

K REG ASSY

Although MY, MV and KU/CA types are different in part number of REG Assy, they consist of the same components.

SEMICONDUCTORS

Δ IC21,IC24	BA05T
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CAPACITORS

C22,C23,C25,C26	CEAT100M50
-----------------	------------

OTHERS

CN11	6P JUMPER CONNECTOR	52151-0610
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6. ADJUSTMENT

6.1 DISCS TO BE USED

1. When adjusting the servo system adjustment
CD : Test disc for adjustment (STD-903 or equivalent)

6.2 MEASURING INSTRUMENTS

- (1) Laser Power Meter
Following power meter manufactured by Advantest Corporation or equivalent :
TQ8210 + TQ82017
TQ8215 + TQ82021
TQ8215 + TQ82010 + TQ82017
LE8010 (by LEADER)
- (2) Oscilloscope
- (3) CD Jitter Meter

6.3 TEST MODE

6.3.1 Test Mode

For adjustment, set the unit to Test mode. To enter Test mode, turn on the unit with the Test Mode Short-Circuit pattern on the OPERATING ASSY. In Test mode, all the displays (FL, LEDs) on the unit should be lit. If not, turn the power off and repeat the same steps again.

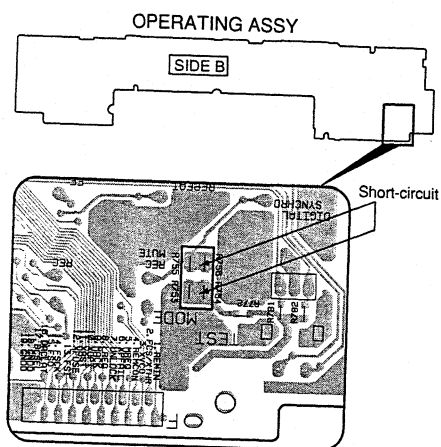


Fig. 1 Enter the Test mode

6.3.2 Operations in Test Mode

In Test mode, the following adjustment functions are assigned to the buttons, as explained below.

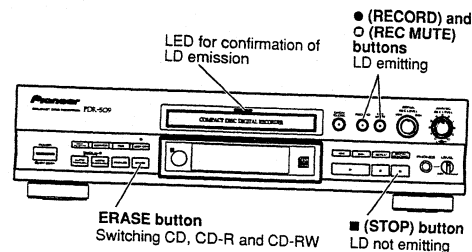


Fig. 2 During adjustment of LD power
(Input selector: analog)

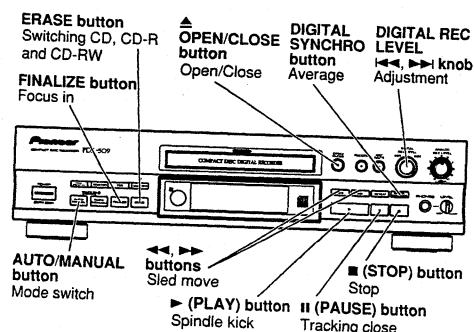


Fig. 3 During adjustment of servo system
(Input selector: optical)

6.4 ADJUSTMENT 1 (LASER DIODE POWER ADJUSTMENT)

CD-R CORE ASSY

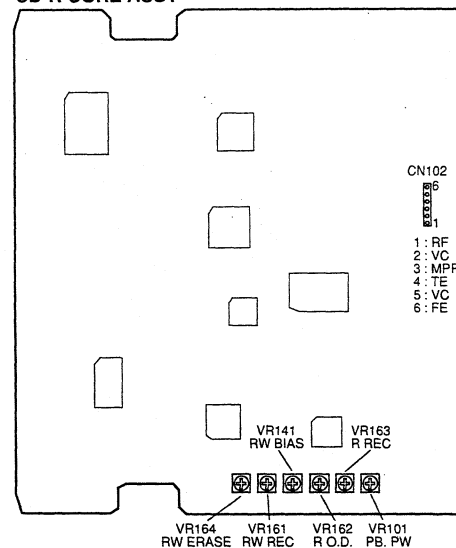


Fig. 4 Adjustment points

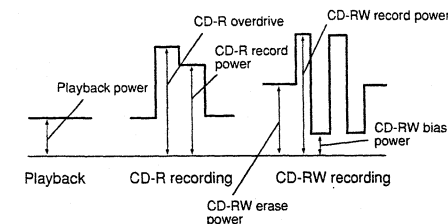


Fig. 5 Output power of the laser diode

- Note 1 : Attach the remote sensor of the laser power meter to a point angled away about 10 degrees against the pickup lens and where the maximum power is detected, so that there will be no light reflected onto the pickup.
- Note 2 : When adjusting with VRs, first turn them completely counter-clockwise and then adjust clockwise, so that the value to be reached is not exceeded.

The following adjustments 1 through 3 must be done with the Input Selector set to the Analog position (LD power adjustment mode).

**DANGER – LASER RADIATION WHEN OPEN.
AVOID DIRECT EXPOSURE TO BEAM.**

6.4.1 Playback Power Adjustment

Test Point	Pickup objective lens
Adjustment Point	VR101 (PB. PW)
Adjustment Value	0.60 mW \pm 0.05 mW

[Procedure]

- (1) Check that "CD" is displayed on the FL display. If "CD-R" or "CD-RW" is displayed, press the STOP button repeatedly until "CD" is displayed.
- (2) Press the RECORD button.
- (3) Press the REC MUTE button. The LED for confirmation of LD emission will light in red. The LD is emitting in this status.
- (4) Turn VR101 clockwise until the adjustment value to be reached is obtained.
- (5) Press the STOP button to shut off the LD.

6.4.2 CD-R Record Power Adjustment

**DANGER – LASER RADIATION WHEN OPEN.
AVOID DIRECT EXPOSURE TO BEAM.**

Test Point	Pickup objective lens
Adjustment Point	VR163 (R REC), VR162 (R O.D.)
Adjustment Value	VR163 : 4.60 mW \pm 0.1 mW VR162 : Addition of 0.1 mW \pm 0.01 mW to the adjustment value of VR163
<p>[Procedure]</p> <ol style="list-style-type: none"> Turn VR163 and VR162 completely counterclockwise to set their power output to minimum. Press the ERASE button once so that "CD-R" appears on the FL display. If the indication is "CD" or "CD-RW," press the ERASE button repeatedly until "CD-R" is displayed on the FL display. Press the RECORD button. Press the REC MUTE button. The LED for confirmation of LD emission will light in red. The LD is emitting in this status. <p>Adjustment of CD-R record power</p> <ol style="list-style-type: none"> Turn VR163 clockwise until the adjusted value is 4.60 mW \pm 0.1 mW. <p>Adjustment of CD-R overdrive power</p> <ol style="list-style-type: none"> Turn VR162 clockwise until the adjusted value becomes adjustment value at Step 5 above + (0.1 mW \pm 0.01 mW). Press the STOP button to shut off the LD. 	

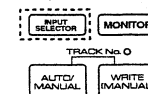
6.4.3 CD-RW Record Power Adjustment

**DANGER – LASER RADIATION WHEN OPEN.
AVOID DIRECT EXPOSURE TO BEAM.**

Test Point	Pickup objective lens
Adjustment Point	VR141 (RW BIAS), VR161 (RW REC), VR164 (RW ERASE)
Adjustment Value	VR141 : 0.40 mW \pm 0.05 mW VR161 : 2.00 mW \pm 0.1 mW VR164 : 5.70 mW \pm 0.1 mW
<p>[Procedure]</p> <ol style="list-style-type: none"> Turn VR141, VR161 and VR164 completely counterclockwise to set their power output to minimum. Press the ERASE button twice so that "CD-RW" appears on the FL display. If the indication is "CD" or "CD-R," press the ERASE button repeatedly until "CD-RW" appears on the FL display. Press the RECORD button. Press the REC MUTE button. The LED for confirmation of LD emission will light in red. The LD is emitting in this status. <p>Adjustment of BIAS power</p> <ol style="list-style-type: none"> Turn VR141 clockwise until the adjusted value is 0.40 mW \pm 0.05 mW. <p>Adjustment of CD-RW record power</p> <ol style="list-style-type: none"> Turn VR161 clockwise until the adjusted value is 2.00 mW \pm 0.1 mW. <p>Adjustment of CD-RW erase power</p> <ol style="list-style-type: none"> Turn VR164 clockwise until the adjusted value is 5.70 mW \pm 0.1 mW. Press the STOP button to shut off the LD. 	

6.5 ADJUSTMENT 2 (SERVO SYSTEM ADJUSTMENT)

For servo adjustment, set the INPUT SELECTOR to OPTICAL.



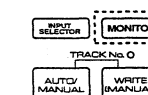
Use the DIGITAL REC LEVEL Knob to make the adjustments.



To register an adjustment, press the DIGITAL REC LEVEL Knob.



To reset the adjusted values to the initial settings, press and hold the MONITOR button for 4 seconds.



CD-R CORE ASSY

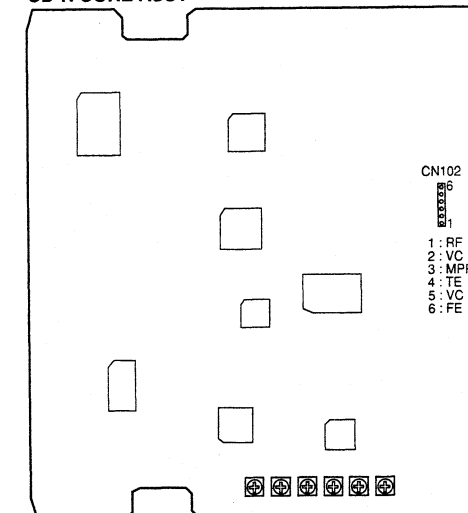


Fig. 6 Adjustment points

6.5.1 Focus Offset Adjustment

Test Point	CN102 - pin 6 (FE)
Adjustment Point	DIGITAL REC LEVEL knob
Adjustment Value	0 mV \pm 10 mV
Symptom when out of adjustment	The model does not focus-in
<p>[Procedure]</p> <ol style="list-style-type: none"> Press the AUTO/MANUAL button until "01 F4" appears on the FL display. Adjust with the DIGITAL REC LEVEL knob until the value for Pin 6 of CN102 is 0 mV \pm 10 mV. Press the DIGITAL REC LEVEL knob to register the adjustment. Once the adjustment is registered with the DIGITAL REC LEVEL knob, "?" on the FL display will disappear. 	

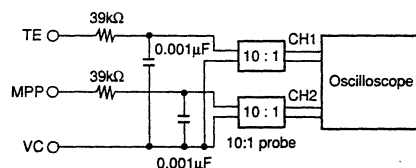
6.5.2 M-S Mix Ratio Adjustment

Test Point	CN102 - pin 4 (TE) and pin 3 (MPP)	Test Disc	STD-903
Adjustment Point	DIGITAL REC LEVEL knob		
Adjustment Value	Adjust until the value of the output signals from pin 4 (TE) and pin 3 (MPP) of CN102 are the same, or the differential output of these signals is minimal.		
Symptom when out of adjustment	Sound broken, record characteristics deteriorate		

[Procedure]

- (1) Press the AUTO/MANUAL button so that "02 F3" appears on the FL display.
- (2) Press the FINALIZE button for focus-in.
- (3) Press the PLAY button for CAV-servo spindle kick (the status where the spindle rotates with the focus servo on and tracking servo off).
- (4) Adjust with the DIGITAL REC LEVEL knob until the value to be reached is obtained.
- (5) Press the DIGITAL REC LEVEL knob to register the adjustment.
Once the adjustment is registered with the DIGITAL REC LEVEL knob, "?" on the FL display will disappear.
- (6) Press the STOP button to stop the unit.

Note: For adjustment, use the following circuits.



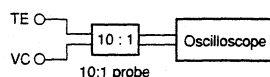
Note: Adjustment must be done around mid-radius on a disc.

6.5.3 Tracking Offset Adjustment

Test Point	CN102 - pin 4 (TE)
Adjustment Point	DIGITAL REC LEVEL knob
Adjustment Value	0 mV ± 10 mV

[Procedure]

- (1) Press the AUTO/MANUAL button so that "03 F6" appears on the FL display.
- (2) Adjust with the DIGITAL REC LEVEL knob until the above adjustment value to be reached is obtained.
- (3) Press the DIGITAL REC LEVEL knob to register the adjustment.
Once the adjustment is registered with the DIGITAL REC LEVEL knob, "?" on the FL display will disappear.



Note: Perform the adjustment in Stop mode.

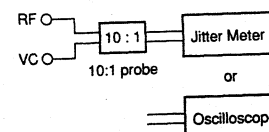
This adjustment is possible with the low-pass filter used in adjustment 5 above attached.

6.5.4 Focus Bias Adjustment

Test Point	CN102 - pin 1 (RF)	Test Disc	STD-903
Adjustment Point	DIGITAL SYNCHRO button, DIGITAL REC LEVEL knob		
Adjustment Value	Adjust until RF jitter is minimal or that the eye pattern of the RF waveform is most open.		
Symptom when out of adjustment	Sound broken, record characteristics deteriorate		

[Procedure]

- (1) Press the DIGITAL SYNCHRO button in Stop mode.
Note: Make sure the unit is in Stop mode.
- (2) Check that "48" appears on the FL display.
- (3) Press the AUTO/MANUAL button so that "04 34" appears on the FL display.
- (4) Press the FINALIZE button for focus-in.
- (5) Press the PLAY button for CAV-servo spindle kick.
- (6) Press the PAUSE button to close the tracking servo, then set the unit to Playback mode.
- (7) Adjust with the DIGITAL REC LEVEL knob until the above adjustment value to be reached is obtained.
Press the DIGITAL REC LEVEL knob to register the adjustment. Once the adjustment is registered with the DIGITAL REC LEVEL knob, "?" on the FL display will disappear.
- (8) Press the STOP button to stop the unit.



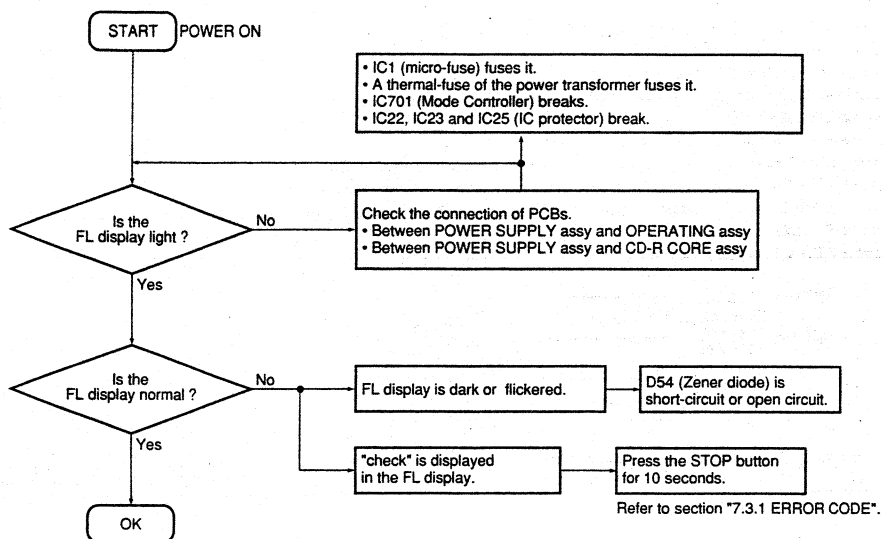
Note: Adjustment must be done around mid-radius on a disc.

7. GENERAL INFORMATION

7.1 DIAGNOSIS

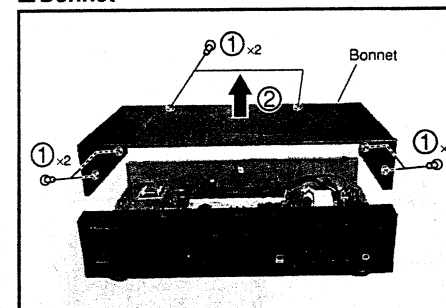
7.1.1 TROUBLE SHOOTING

■ Power isn't turn on. FL display isn't light up. FL display is abnormal.

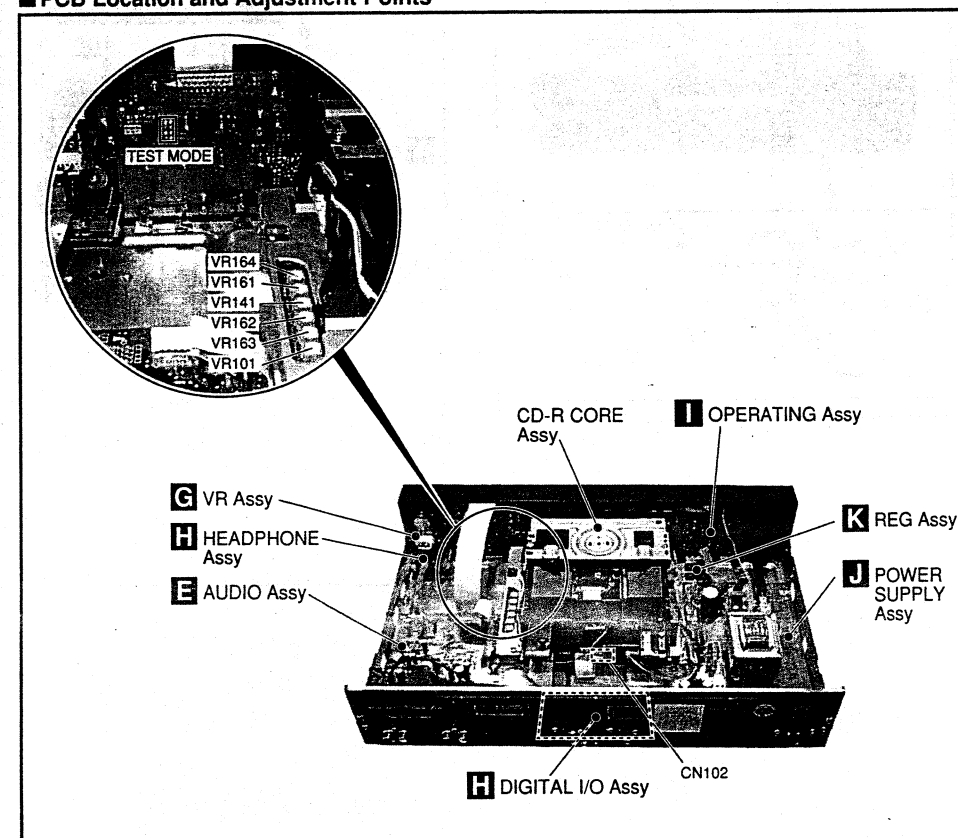


7.1.2 DISASSEMBLY

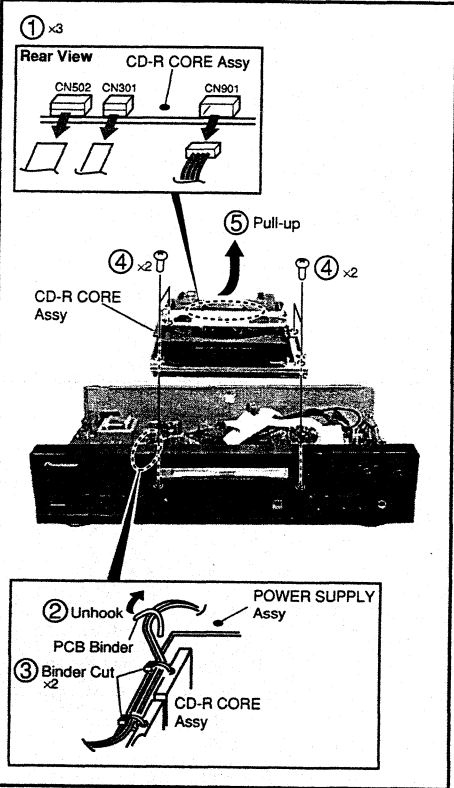
■ Bonnet



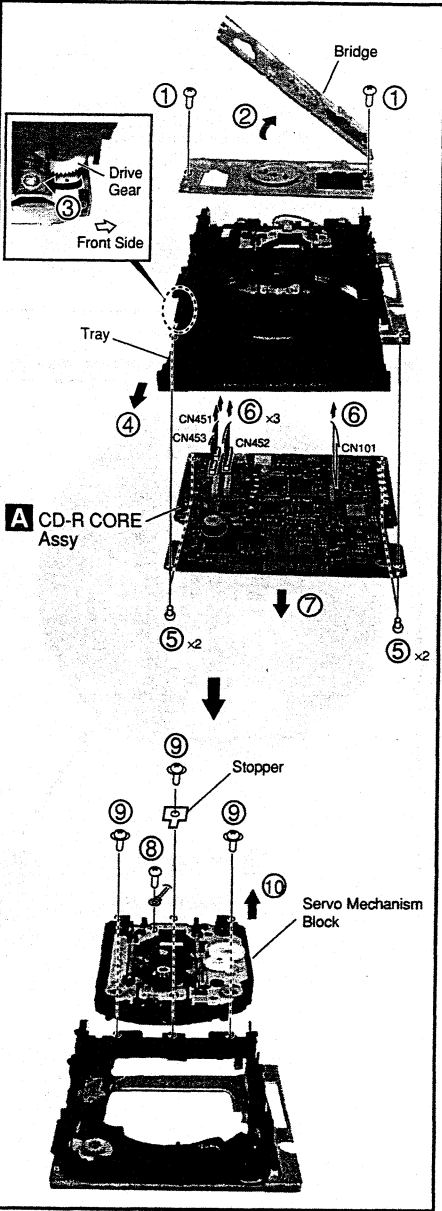
■ PCB Location and Adjustment Points



■ CD-R CORE ASSY
• CD-R CORE Assy



• Servo Mechanism Block

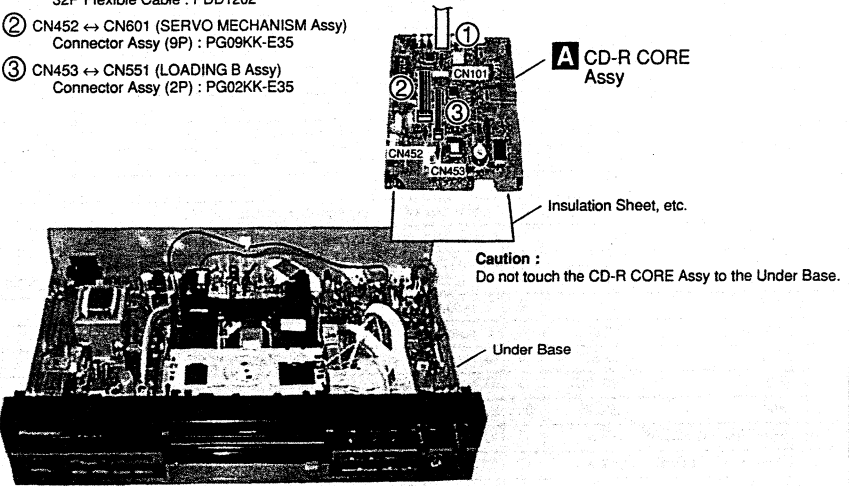


7.1.3 DIAGNOSIS OF CD-R CORE ASSY

When diagnosing the CD-R CORE Assy, use the following Flexible Cable and Connector Assys for service.

• Flexible Cable and Connector Assys for service

- ① CN101 ↔ CN1 (Pickup)
32P Flexible Cable : PDD1202
- ② CN452 ↔ CN601 (SERVO MECHANISM Assy)
Connector Assy (9P) : PG09KK-E35
- ③ CN453 ↔ CN551 (LOADING B Assy)
Connector Assy (2P) : PG02KK-E35



7.2 PARTS

7.2.1 IC

• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

•List of IC

PE5109A, M56788FP, PCM1800-1, PE5110B

■ PE5109A (CD-R CORE ASSY : IC301)

• Mechanism Control IC

•Pin Function

No.	Mark	Pin Name	I/O	Pin Function
1	P32/XCLK0/SCL	MSCK	I/O	Serial transfer clock output of clock synchronous system (Set to Input port at not used.)
2	P33/SO0/SDA	MSO	I/O	Serial transfer data output of clock synchronous system (Set to Input port at not used.)
3	P34/TO0	EECS	O	Enable output for writing and reading of the EEPROM data
4	P35/TO1	MREQ	O	Serial hand shake to the mode controller "L"
5	P36/TO2	FOK	I	FOCUS OK input (L: FOCUS OK)
6	P37/TO3	LRST	O	Reset output for the servo and digital system ICs (L: Reset)
7	XRESET	XRESET	I	Reset input (L: Reset)
8	VDD1	+5V	-	+5V
9	X2	CLOCK	-	Crystal input for system clock (32MHz)
10	X1	CLOCK	-	Crystal output for system clock (32MHz)
11	VSS1	GND	-	GND
12	P00	XECE	O	Enable output for reading the jig for test "L"
13	P01	RECE	O	Laser diode recording power ON/OFF ON: H
14	P02	XAMUTE	O	AUDIO last stage mute "L" (according to the mode controller) MUTE ON: during REC/PAUSE, at input selector switch and during STOP
15	P03	TP302	O	"L" outputs
16	P04	TP303	O	"L" outputs
17	P05	XEXSC	O	External sync enable output of LC89585 "L"
18	P06	XASYNC	O	ATIP frame sync "L"
19	P07	XENCE	O(I)	Serial enable output of LC89585 "H" (Set to Input port at not used.)
20	P67/XREFRQ/HLDK	TP305	O	"L" outputs
21	P66/XWAIT/HLDK	TP306	O	"L" outputs
22	P65/XWR	XWR	O	Strobe signal output for read operation of the external memory
23	P64/XRD	XRD	O	Strobe signal output for write operation of the external memory
24	P63/A19	XLT	O	Latch output of CXD2585Q command
25	P62/A18	SSCK	O	Serial clock output for CXD2585Q command
26	P61/A17	SSO	O	Serial data output for CXD2585Q command
27	P60/A16	ALAT	O	Latch output for AK8563 command
28	P57/A15	SCLK	O	Serial clock output for serial readout of CXD2585Q
29	P56/A14	ENBL	O	Laser diode ON/OFF H: ON
30	P55/A13	TP307	O	"L" outputs
31	P54/A12	LDPW4	O	Recording laser power monitor output
32	P53/A11	LDPW3		
33	P52/A10	LDPW2		
34	P51/A9	LDPW1		
35	P50/A8	LDPW0	O	Data address line
36	P47/AD7	AD7		
37	P46/AD6	AD6		
38	P45/AD5	AD5		
39	P44/AD4	AD4		
40	P43/AD3	AD3		

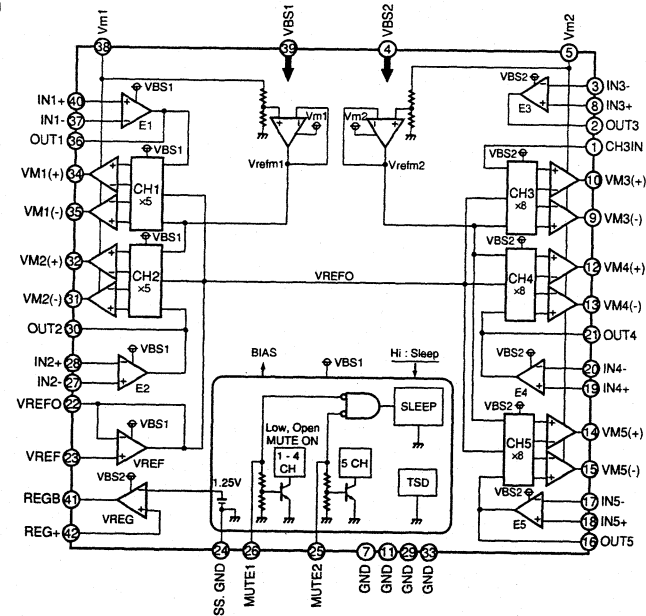
No.	Mark	Pin Name	I/O	Pin Function
41	P42/AD2	AD2	O	Data address line
42	P41/AD1	AD1		
43	P40/AD0	AD0		
44	ASTB/CLKOUT	ASTB	O	External latch signal of lower address signal for external memory access
45	Vss0	GND	-	GND
46	TEST	GND	-	GND
47	P10/PWM0	SPSP	O(A)	Spindle drive PWM output in the Spindle CAV
48	P11/PWM1	LPWM	O(A)	Loading motor output (PWM) AT PWM is not used: "H" (fixed to "H")
49	P12/ASCK2/XSCK2	SOCK	O	Serial clock output for sub-Q of CXD2585Q
50	P13/RXD2/SI2	SQSI	I	Serial data input for sub-Q of CXD2585Q
51	P14/TXD2/SO2	SO2	O	Serial data output
52	P15	TP314	O	"L" outputs
53	P16	TP315	O	"L" outputs
54	P17	TP316	O	"L" outputs
55	VDD0	+5V	-	+5V
56	P70/ANI0	TEPP	I(A)	Tracking error peak to peak (for tracking gain adjustment)
57	P71/ANI1	RFT	I(A)	A/D input of upper side envelope of Playback RF
58	P72/ANI2	RFB	I(A)	A/D input of lower side envelope of Playback RF
59	P73/ANI3	TEMP	I(A)	A/D input of temperature sensor
60	P74/ANI4	RFOPC	I(A)	Running OPC return light 1
61	P75/ANI5	VWDC2	I(A)	Running OPC return light 2
62	P76/ANI6	TRAY	I(A)	A/D input of loading position (OPEN/CLAMP)
63	P77/ANI7	AD7	I(A)	Not used
64	AVDD	Avdd	-	+5V
65	AVREF1	Avref1	-	+5V
66	AVSS	AVss	-	GND
67	ANO0	WREF	O(A)	Recording power 1
68	ANO1	VWDC2R	O(A)	Outputs for strategy setting
69	AVREF2	AVref2	-	+5V
70	AVREF3	AVref3	-	GND
71	P20/NMI	XPFAIL	I	Power failure detection
72	P21/INTP0	FG	I	Spindle FG detection
73	P22/INTP1	ATIP	I	ATIP SYNC detection
74	P23/INTP2/C1	SCOR	I	EFM decoder frame sync detection
75	P24/INTP3	SUBSYNC	I	EFM decoder frame sync detection
76	P25/INTP4/ASCK-/XSCK1	XRFDT	I	EFM playback RF detection
77	P26/INTP5	ITSIN	I	SENS input
78	P27/SI0	MSI	I	Serial transfer DATA input of the clock sync. system
79	P30/RXD/SI1	MACK	I	Serial hand shake CLOCK input to the mode controller
80	P31/TXD/SO1	XFUSE	I	"L" during communicate with the mode controller

Note: (A) in item I/O shows "ANALOG".

M56788FP (CD-R CORE ASSY : IC451)

• 5 Channel Actuator Driver

Block Diagram



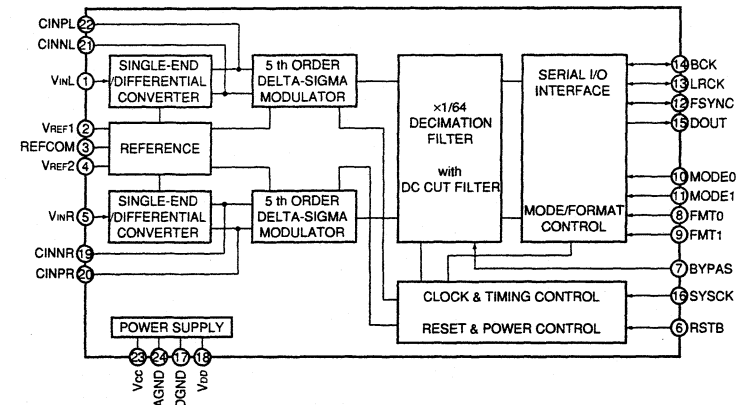
Pin Function

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	CH3IN	I	CH3 non-inverted input	22	VREF0	O	Reference voltage output
2	OUT3	O	E3 amplifier output	23	VREF	I	Reference voltage input
3	IN3-	I	E3 amplifier inverted input	24	SS.GND	-	Small signal GND
4	VBS2	-	Bootstrap power supply	25	MUTE2	-	CH5 mute
5	Vm2	-	Motor power supply	26	MUTE1	-	CH1 to 4 mute
6	N.C	-	N.C	27	IN2+	I	E2 amplifier non-inverted input
7	GND	-	Motor GND	28	IN2-	I	E2 amplifier inverted input
8	IN3+	I	E3 amplifier non-inverted input	29	GND	-	Motor GND
9	VM3(-)	O	CH3 inverted output	30	OUT2	O	E2 amplifier output
10	VM3(+)	O	CH3 non-inverted output	31	VM2(-)	O	CH2 inverted output
11	GND	-	Motor GND	32	VM2(+)	O	CH2 non-inverted output
12	VM4(+)	O	CH4 non-inverted output	33	GND	-	Motor GND
13	VM4(-)	O	CH4 inverted output	34	VM1(+)	O	CH1 non-inverted output
14	VM5(+)	O	CH5 non-inverted output	35	VM1(-)	O	CH1 inverted output
15	VM5(-)	O	CH5 inverted output	36	OUT1	O	E1 amplifier output
16	OUT5	O	E5 amplifier output	37	IN1-	I	E1 amplifier inverted input
17	IN5-	I	E5 amplifier inverted input	38	Vm1	-	Motor power supply
18	IN5+	I	E5 amplifier non-inverted input	39	VBS1	-	Bootstrap power supply
19	IN4+	I	E4 amplifier non-inverted input	40	IN1+	I	E1 amplifier non-inverted input
20	IN4-	I	E4 amplifier inverted input	41	REGB	-	Regulator PNP base control
21	OUT4	O	E4 amplifier output	42	REG+	-	Regulator voltage setting resistor

PCM1800-1 (AUDIO ASSY : IC802)

• A/D Converter

Block Diagram



Pin Function

No.	Pin Name	I/O	Pin Function
1	VINL	I	Analog input L ch
2	VREF1	-	Decoupling capacitor of reference 1
3	REFCOM	-	Reference decoupling common
4	VREF2	-	Decoupling capacitor of reference 2
5	VINR	I	Analog input R ch
6	RSTB	I	Reset input Active "L"
7	BYPAS	I	LCF bypass control
8	FMT0	I	Audio data format 0
9	FMT1	I	Audio data format 1
10	MODE0	I	Master/Slave mode selection 0
11	MODE1	I	Master/Slave mode selection 1
12	FSYNC	I/O	Frame sync input/output
13	LRCK	I/O	Sampling clock input/output
14	BCK	I/O	Bit clock input/output
15	DOUT	O	Audio data output
16	SYSCLK	I	System clock input 256fs, 384fs or 512fs
17	DGND	-	Digital GND
18	VDD	-	Digital power supply
19	CINNRR	-	Anti-aliasing filter capacitor (-), R ch
20	CINPR	-	Anti-aliasing filter capacitor (+), R ch
21	CINNLL	-	Anti-aliasing filter capacitor (-), L ch
22	CINPL	-	Anti-aliasing filter capacitor (+), L ch
23	VCC	-	Analog power supply
24	AGND	-	Analog GND

■ PE5110B (FUNCTION ASSY : IC701)

• Mode Controller

●Pin Function

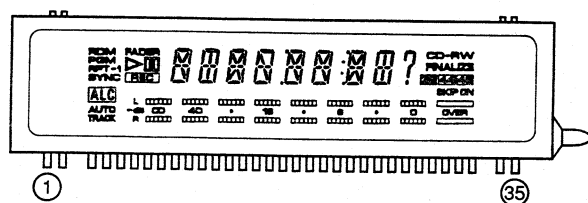
No.	Mark	Pin Name	I/O	Pin Function
1	FIP6	GRID 6	O	FL grid output 5
2	FIP5	GRID 5	O	FL grid output 6
3	FIP4	GRID 4	O	FL grid output 7
4	FIP3	GRID 3	O	FL grid output 8
5	FIP2	GRID 2	O	FL grid output 9
6	FIP1	GRID 1	O	FL grid output 10
7	FIP0	GRID 0	O	FL grid output 11
8	VDD	—	—	Connect to VDD
9	SCOK	—	O	Not used "L" outputs
10	SO0	—	O	Not used "L" outputs
11	SI0	—	O	Not used "L" outputs
12	P24	XTAL	O	XTAL ON/OFF (At digital selection without FS converter : L)
13	P23	XEVCO	O	Encoder VCO ON/OFF (At CD : H)
14	SCK1	FSCK	I/O	Serial clock of the mechanism controller LSI
15	SO1	FSO	O	Serial output of the mechanism controller LSI
16	SI	FSI	I	Serial input of the mechanism controller LSI
17	RESET	XRESET	I	Reset input of the mode controller (L : Reset)
18	P74	DISP_L	O	"DISP OFF" LED lights up output (L: lights up)
19	P73	LCLOCK	O	"AUTO/MANUAL" LED lights up output (L: lights up)
20	AVSS	GND	I	Connect to GND
21	P17	XFUSE	O	During use the serial communication between the mode controller and LC89585 (During use : L)
22	P16	CENT_L	O	"CENTER" LED lights up output (L : lights up)
23	P15	XVCO	O	PLL ON/OFF (At digital selection without FS converter : L (PLL oscillation))
24	P14	FS_THR	O	FS through output (Digital input at FS through ON and 44.1kHz : L)
25	P13	DACLAT	O	Communication latch output for D/A converter
26	P12	XRST	O	Reset output for mechanism controller and ATIP decoder (L: reset)
27	P11	XOPT	O	Optical input selection (At optical input selection : L)
28	P10	—	O	Not used "L" outputs (prepare the parallel remote control key input)
29	AVDD	VDD	—	Connect to VDD
30	AVREF	VDD	—	Connect to VDD
31	P04	—	—	—
32	XT2	—	O	Not used
33	VSS	GND	—	Connect to VDD
34	X1	—	I	System oscillation 4.19MHz
35	X2	—	O	—
36	P37	SW1	I	Demo mode ON/OFF (H fixed: No demo mode)
37	P36	FS_SW	I	FS through ON/OFF switching input (H: FS through)
38	P35	HIB_SW	I	Hi-bit mode ON/OFF switching input (H: Hi-bit)
39	P34	LGT_SW	I	LEGATO ON/OFF switching input (H: LEGATO ON)
40	P33	RREQ	O	CE output for jig communication

No.	Mark	Pin Name	I/O	Pin Function
41	P32	MACK	O	Communication response for mechanism controller (H to L: communication permission) (L to H: Communication end)
42	P31	LREQ	O	CE signal for LC89585 (L: Enable)
43	P30	UNLOCK	I	Digital unlock detection
44	INTP3	POT_INT	I	Rotary encoder SW operation detection (↓ interrupt)
45	INTP2	XPFAIL	I	Power down detection (L: power down)
46	INTP1	MREQ	I	Mechanism controller communication request (interrupt)
47	INTP0	REMIN	I	Remote control input (interrupt)
48	IC	VPP	I	Connect to GND
49	P72	ROT3	I	Not used "L" outputs
50	P71	ROT2	I	"H" outputs when playing the CD/CD-R/CD-RW discs in the Hi-bit mode
51	P70	ROT1	I	Rotary encoder SW direction judgment input
52	VDD	VDD	—	Connect to VDD
53	P127	SCAN4	O	Key matrix output 4
54	P126	SCAN3	O	Key matrix output 3
55	P125	SCAN2	O	Key matrix output 2
56	P124	SCAN1	O	Key matrix output 1
57	P123	SCAN0	O	Key matrix output 0
58	P122	KEYIN3	I	Key matrix input 3
59	P121	KEYIN2	I	Key matrix input 2
60	P120	KEYIN1	I	Key matrix input 1
61	P117	KEYIN0	I	Key matrix input 0
62	P116	—	O	Not used "L" outputs
63	P115	SCMS	O	Prepare the mode switch ("L" outputs)
64	P114	FINL_SEG	O	FINALIZE-segment output (At lights up: H)
65	P113	SEG 10	O	FL segment output 10
66	P112	SEG 9	O	FL segment output 9
67	P111	SEG 8	O	FL segment output 8
68	P110	SEG 7	O	FL segment output 7
69	P107	SEG 6	O	FL segment output 6
70	P106	SEG 5	O	FL segment output 5
71	VLOAD	—	—	VLOAD
72	P105	SEG 4	O	FL segment output 4
73	P104	SEG 3	O	FL segment output 3
74	P103	SEG 2	O	FL segment output 2
75	P102	SEG 1	O	FL segment output 1
76	P101	SEG 0	O	FL segment output 0
77	P100	GRID10	O	FL grid output 10
78	FIP9	GRID 9	O	FL grid output 9
79	FIP8	GRID 8	O	FL grid output 8
80	FIP7	GRID 7	O	FL grid output 7

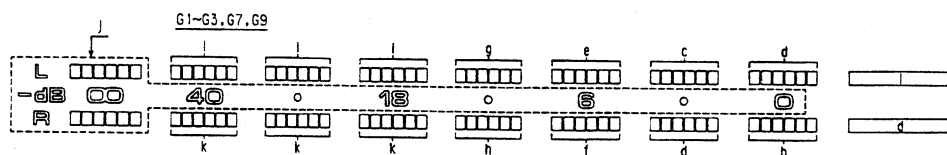
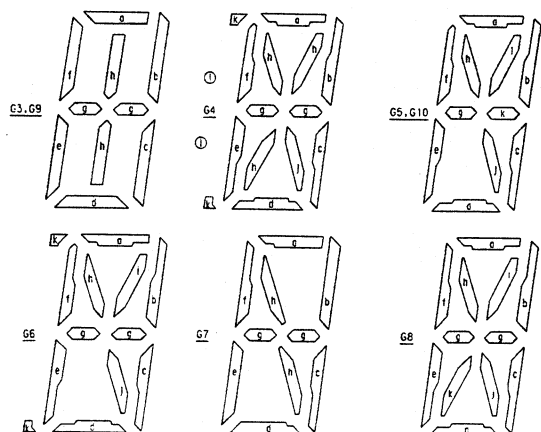
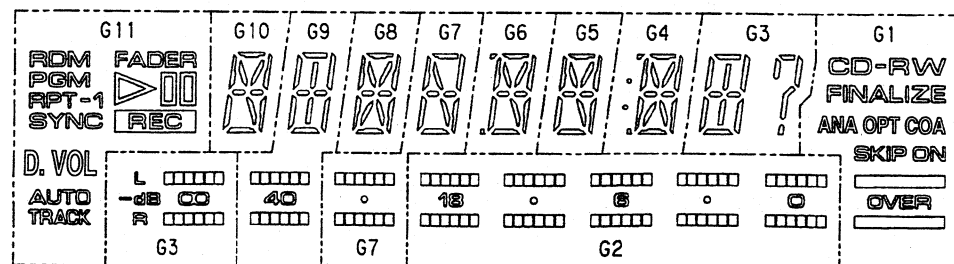
7.2.2 DISPLAY















■ PEL1099 (FUNCTION ASSY : V701)

- FL TUBE



- **Anode & Grid Assignment**



	G1	G2	G3	G4	G5	G6	G7	G8	G9	G10	G11
S1	a	a	a	a	a	a	a	a	a	a	FADER
S2	OVER	b	b	b	b	b	b	b	b	b	
S3	(CD-R)W	c	c	c	c	c	c	c	c	c	
S4	ANA	d	d	d	d	d	d	d	d	d	
S5	OPT	e	e	e	e	e	e	e	e	e	RDM
S6	CD(-RW)	f	f	f	f	f	f	f	f	f	PGM
S7	(CD)-R(W)	g	g	g	g	g	g	g	g	g	RPT
S8	ON	h	h	h	h	h	h	h	h	h	-1
S9	i	i	?	i	i	i	i	i	i	i	SYNC
S10	COA	j	j	j	j	j	j	j	j	j	D. VOL
S11	SKIP	k		k	k	k	k	k	k	k	AUTO TRACK
S12	FINALIZE										

- **Pin Assignment**

[illegible]

F1,F2:Filament G1~G11:Grid S1~S12:Anode NP:No Pin NL:No Lead

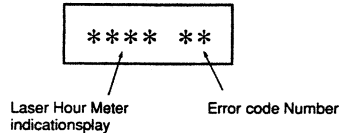
7.3 EXPLANATION

7.3.1 ERROR CODE

Laser Hour Meter Indication and Error Code Display for Service
The PDR-509 can display the total turn-on time of the laser diode and error codes for service.

When the STOP key is held down for about 5 seconds in stop state in Normal mode, an FL display as shown below is obtained.

• Display



Left 4 FL digits : Total turn-on time of the laser diode
Right 2 FL digits : Error code for service

The total turn-on time of the laser diode is displayed in the range of 0 to 5100.

The error code for service is displayed as a number (ERROR NUMBER), which follows a message "CHECK DISC" or "CHECK." For details, see the table below.

To initialize the total turn-on time of the laser diode, hold the STOP key down for about 5 seconds in stop state with the INPUT selector set to analog in Test mode.

The message "CLEAR" is displayed, and the total time is cleared.

Error code table for service

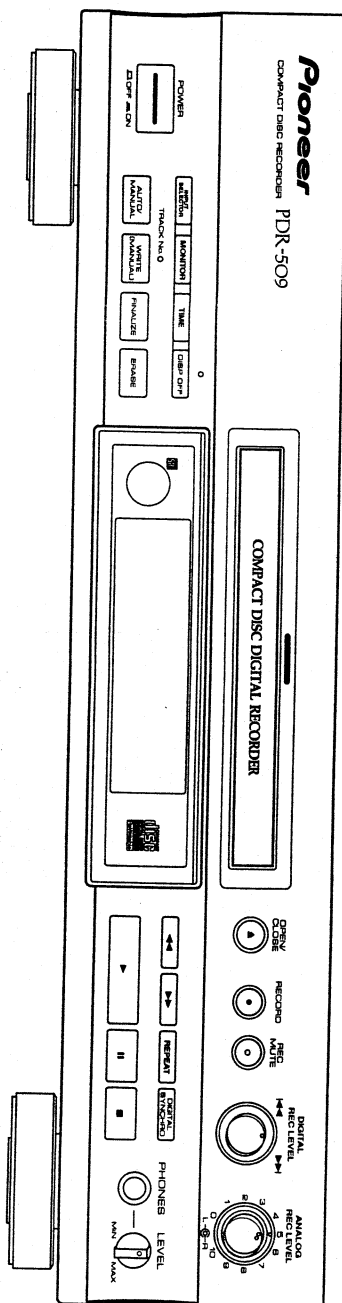
Code	Symptom	Contents of Error	Possible Cause	Checkpoints
H0	No operation even when power is supplied.	Communication between mechanism controller and mode controller is not achieved.	• Improper soldering • Pattern short	IC301 (PE5109A) IC351 (PDJ014A)
H1	(CHECK display)	Defective mechanism controller terminals	• Short-circuiting of parts • Improper power supply	IC371 (HD74HC573FP)
H2	Pre-recording process does not complete, and the tray does not open. (CHECK display)	Improper input voltage at the mechanism-control terminals (pins 22, 23, 24)		IC201 (PA9007A)
H5	Pre-recording process disabled (CHECK display)	Improper IC303 data writing	• Defect in IC303	IC303 (PYY1196)
L•	The unit stops during the tray open/close operation. (CHECK display)	Improper loading	• Defective tray position sensor • Defective loading motor • Improper soldering • Pattern short • Improper power supply	IC451 (M56788FP)
E•	The unit stops when PLAY or REC/PAUSE starts. (CHECK display)	Defective slider • The pickup cannot be returned to the specified position.	• Disconnected flexible cable • Defective drive circuit • Abnormal power supply • Abnormal TOC position switch • Improper soldering	S601 (PSG1013) *1 IC451 (M56788FP) IC401 (CXD2585Q)
P•	The unit does not read the inserted disc, and stops. (CHECK DISC display)	Defect in spindle • Disc upside-down. • Dirty or cracked disc • Abnormal disc rotation • No signal obtained from the disc	• Defective spindle motor • Defective spindle drive circuit • Abnormal FG signals • Defective WBL circuit • Defective decoder circuit • Unable to read ATIP or subcode • High error rate	PC651 *1 (NJL5803K-F1) IC451 (M56788FP) IC401 (CXD2585Q)

*1 : SERVO MECHANISM Assy

Code	Symptom	Contents of Error	Possible Cause	Checkpoints
C•	The unit stops before it enters REC/PAUSE mode.	Defects related to the recording laser power • Dirty or cracked disc • The optimum recording power cannot be obtained. • Trouble in RF detection.	• Defective laser diode • Trouble in RF detection • Defective RFT RFB circuit • Recording power is not sufficient. • Improper soldering, pattern short • Trouble with power supply • Unable to read ATIP or subcode	IC201 (PA9007A) IC101 (AK8563) IC363 (TC7S04F) IC364 (TC7S14F)
F•	The unit stops during playback or recording.	Defective pickup • Unable to focus because of dirt or crack on the inserted disc. • Unable to output the proper laser power	• Defective laser diode • Defective focus drive circuits • Defective pickup • Improper soldering • Pattern short • Trouble of power supply	IC451 (M56788FP) IC401 (CXD2585Q)
A•	The unit stops in a recording-related operation, displaying "CHECK DISC."	• Unable to focus Stop during recording • The unit stops, being obstructed by a dirt or a crack on the disc.	If any hardware trouble occurs before displaying A• or d•, the unit stops displaying a code other than these codes. Therefore, these service codes are generated only for troubles with the disc.	
d•	The unit stops in a recording related operation, displaying "CHECK DISC." The unit does not read the inserted disc, and stops.			

The indication for * shows the mechanism mode listed below.

No.	Mechanism Mode	No.	Mechanism Mode	No.	Mechanism Mode
0	PLAY	5	SETUP	A	REC
1	OPEN	6	TOC READ	B	TOC REC
2	STOP	7	—	C	OPC
3	—	8	SEARCH	D	TOC CHECK
4	—	9	REC/PAUSE	E	PMA, ACTUAL PAUSE REC

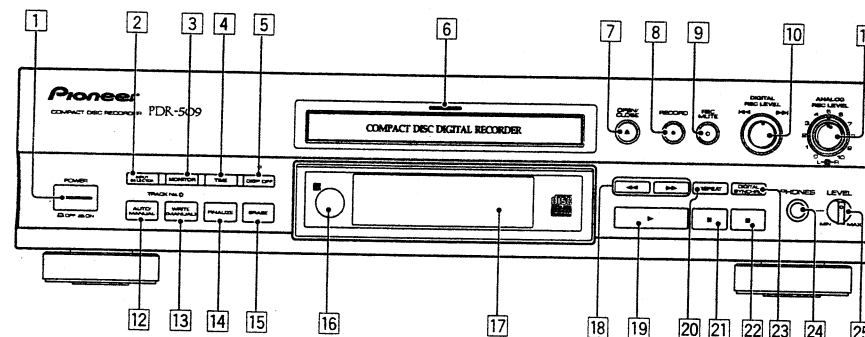


PDR-509

8. PANEL FACILITIES AND SPECIFICATIONS

8.1 PANEL FACILITIES

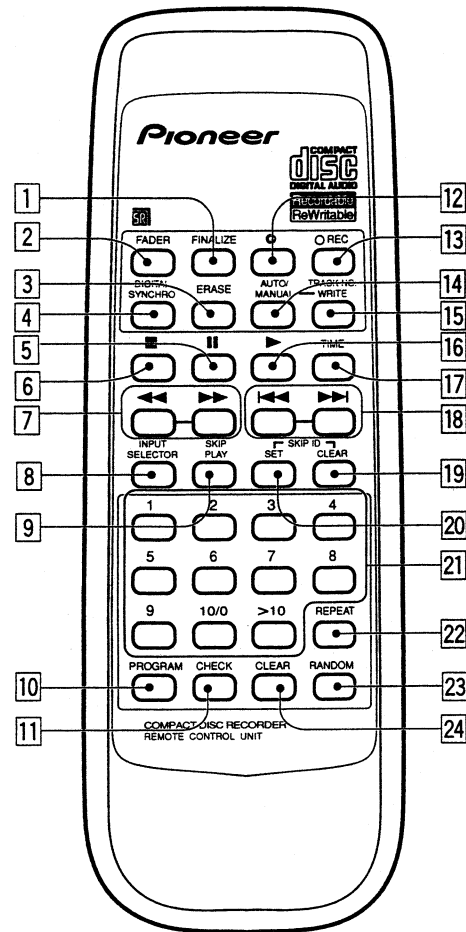
■ Front Panel



The illustration shows the U.S. model.

- 1 POWER switch**
Switches power to the unit on and off.
- 2 INPUT SELECTOR**
Switches between the analog, optical digital and coaxial digital inputs.
- 3 MONITOR**
Press to monitor the selected input and display digital source information.
- 4 TIME**
Switches the display mode (elapsed track time, remaining track time, total disc playing time, etc.)
- 5 DISP OFF**
Press to switch the character display off. The indicator above the button lights to remind you that the display is switched off.
- 6 Function indicator**
Indicates the current function of the unit:
Lit – recording a CD-R/CD-RW, or erasing a CD-RW disc.
Blinking – record-mute and PMA-record modes.
- 7 OPEN/CLOSE ▲**
Press to open or close the disc tray.
- 8 RECORD ●**
Press to enter record-pause mode.
- 9 REC MUTE ○**
Records a blank section on a disc (for space between tracks, etc.).
- 10 DIGITAL REC LEVEL / ◀▶▶▶**
Turn the jog dial to set the digital recording level and skip tracks.
Push the jog dial to: start playback (stop mode only); input track number (during programming); display the digital recording level (monitor, record, record-pause modes).
- 11 ANALOG REC LEVEL**
Sets the recording level for analog-input recording. Outside ring controls left input level; inside dial controls right input level.
- 12 TRACK NUMBER AUTO/MANUAL**
Switches between automatic and manual track numbering when recording a disc. Indicator lights to remind you when manual track numbering has been turned on.
- 13 TRACK NUMBER WRITE [MANUAL]**
Press during recording to start a new track number (when in manual track numbering mode).
- 14 FINALIZE**
Press to start the disc finalization process (to make recordable CDs playable on ordinary CD players).
- 15 ERASE (CD-RW discs only)**
Press to start erasing tracks, or to re-initialize a disc.
- 16 Remote sensor**
- 17 Character display**
- 18 ◀◀ and ▶▶**
Press and hold for fast-reverse and fast-forward playback.
- 19 ▶**
Press to play, or resume playing, a disc. Also use to start recording from record-pause mode.
- 20 REPEAT**
Use to set the repeat mode (current track, disc, or repeat off).
- 21 ||**
Press to pause playback or recording.
- 22 ■**
Press to stop playback or recording.
- 23 DIGITAL SYNCHRO**
Press to start recording on detection of a digital input signal.
- 24 PHONES jack**
Plug in a pair of stereo headphones for private listening or monitoring.
- 25 LEVEL**
Use to adjust the phones volume level.

Remote Control Unit



1 FINALIZE

Press to start the disc finalization process (to make recordable CDs playable on ordinary CD players).

2 FADER

Press to fade in or fade out during playback or recording.

3 ERASE (CD-RW discs only)

Press to start erasing tracks, or to re-initialize a disc.

4 DIGITAL SYNCHRO

Press to start recording on detection of a digital input signal.

5 II

Press to pause playback or recording.

6 ■

Press to stop playback or recording.

7 ◀ and ▶

Press and hold for fast-reverse and fast-forward playback.

8 INPUT SELECTOR

Switches between the analog, optical digital and coaxial digital inputs.

9 SKIP PLAY

Press to switch skip play on and off. When on, the player will skip tracks that skip IDs have been set for.

10 PROGRAM

Use to program the playback order of tracks on a disc.

11 CHECK

Press repeatedly to step through the programmed tracks in program-play mode.

12 ○ REC MUTE

Records a blank section on a disc (for space between tracks, etc.)

13 ○ REC

Press to enter record-pause mode.

14 TRACK NUMBER AUTO/MANUAL

Switches between automatic and manual track numbering when recording a disc. Front panel indicator lights to remind you when manual track numbering has been turned on.

15 TRACK NUMBER WRITE [MANUAL]

Press during recording to start a new track number (when in manual track numbering mode).

16 ▶

Press to play, or resume playing, a disc. Also use to start recording from record-pause mode.

17 TIME

Switches the display mode (elapsed track time, remaining track time, total disc playing time, etc.)

18 ◀ and ▶

Press to skip forward or backward tracks.

19 SKIP ID CLEAR

Clears the above setting.

20 SKIP ID SET

Instructs the player to skip a particular track on playback.

21 Number buttons

Use to select track numbers on a disc directly.

22 REPEAT

Use to set the repeat mode (current track, disc, or repeat off).

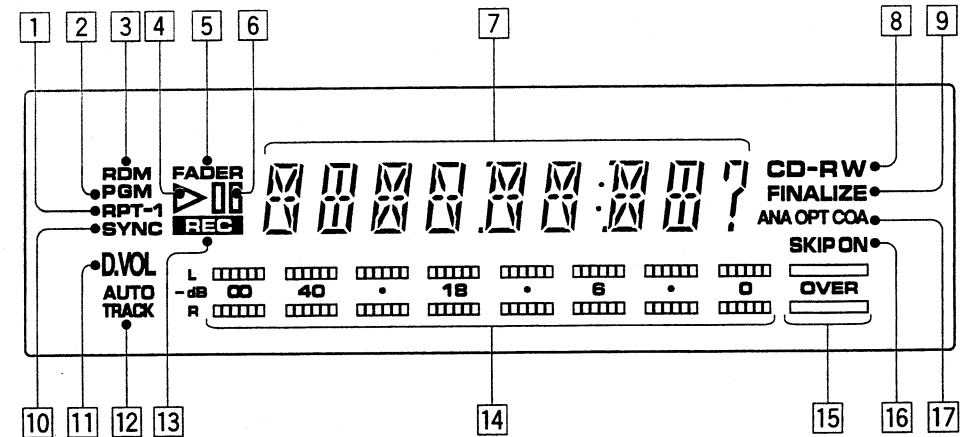
23 RANDOM

Press to start random playback.

24 CLEAR

Press to clear the last programmed track in program-play mode.

Display



1 RPT / RPT-1

Lights when disc repeat / track repeat mode is on.

2 PGM

Lights when program-play mode is active.

3 RDM

Lights when random-play mode is active.

4 ▶

Lights during playback.

5 FADER

Blinks during fade in or fade out.

6 II

Lights when the recorder is in either play-pause or record-pause mode.

7 Message/time display

8 CD / CD-R / CD-RW

Indicates the type of disc currently loaded.

9 FINALIZE

Lights if the CD-RW currently loaded has been finalized. Also blinks during Automatic Finalization Recording (p.22)

10 SYNC

Lights when the recorder is in automatic synchro recording mode.

11 D.VOL

Lights when the digital volume control function is active.

12 AUTO TRACK

Lights when automatic track numbering is on during recording.

13 REC

Lights to indicate recording or record-pause mode. Blinking display indicates record muting.

14 Recording level meter

Displays the input level during recording, or the recorded level during playback.

15 OVER indicator

Indicates that the input signal overloaded the disc during recording.

16 SKIP ON

Lights to indicate that a disc contains skip IDs. When setting or clearing skip IDs, the word SKIP blinks.

17 ANA

Lights when the analog input is selected.

OPT

Lights when the optical digital input is selected.

COA

Lights when the coaxial digital input is selected.

8.2 SPECIFICATIONS

■ KU/CA Type

1. General

Model Compact disc audio system
 Applicable discs CDs, CD-Rs and CD-RWs
 Power supply AC 120 V, 60 Hz
 Power consumption 18 W
 Operating temperature +5 °C to +35 °C (+41 °F to +95 °F)
 Weight (without package) 3.9 kg (8lb 10oz)
 Max. dimensions 420 (W) x 300 (D) x 105 (H) mm
 16 5/16 (W) x 11 13/16 (D) x 4 3/16 (H) in.

2. Audio unit

Frequency characteristics 2 Hz to 20 kHz
 Playback S/N 110 dB (EIAJ)
 Playback dynamic range 98 dB (EIAJ)
 Playback total harmonic distortion 0.002 % (EIAJ)
 Playback channel separation 98 dB
 Recording S/N 92 dB
 Recording dynamic range 92 dB
 Recording total harmonic distortion 0.005 %
 Output voltage 2 V
 Wow-flutter Less than measurement limit
 ((±0.001 % W.PEAK) (EIAJ))
 Number of channels 2 channels (stereo)
 Digital output:
 Coaxial output 0.5 Vp-p ±20 % (75 Ω)
 Optical output -15 to -21 dBm (wavelength: 660 nm)
 Frequency deflection: Level 2 (standard mode)

* Recording specification values are for the LINE input (analog)

3. Input jacks

Optical digital input jack
 Coaxial digital input jack
 Audio LINE input jack
 Control IN jack

4. Output jacks

Optical digital output jack
 Coaxial digital output jack
 Audio LINE output jack

5. Accessories

● Remote control unit 1
 ● Size AA/R6P dry cell batteries 2
 ● Audio cable 2
 ● AC power cord 1
 ● Operating Instructions 1

6. Pickup

● Laser wavelength (λ) 778 to 787 nm
 ● Object lens out (3 beam total) 23 mW
 ● Laser class IIIB

NOTE:

The specifications and design of this product are subject to change without notice, due to improvements.

■ MY and MV Types

1. General

Model Compact disc audio system
 Applicable discs CDs, CD-Rs and CD-RWs
 Power supply AC 220-230 V, 50/60 Hz
 Power consumption 18 W
 Operating temperature +5 °C to +35 °C
 Weight (without package) 3.9 kg
 Max. dimensions 420 (W) x 300 (D) x 105 (H) mm

2. Audio unit

Frequency characteristics 2 Hz to 20 kHz
 Playback S/N 112 dB (EIAJ)
 Playback dynamic range 98 dB (EIAJ)
 Playback total harmonic distortion 0.0017 % (EIAJ)
 Playback channel separation 98 dB
 Recording S/N 92 dB
 Recording dynamic range 92 dB
 Recording total harmonic distortion 0.004 %
 Output voltage 2 V
 Wow-flutter Less than measurement limit
 ((±0.001 % W.PEAK) (EIAJ))
 Number of channels 2 channels (stereo)
 Digital output:
 Coaxial output 0.5 Vp-p ±20 % (75 Ω)
 Optical output -15 to -21 dBm (wavelength: 660 nm)
 Frequency deflection: Level 2 (standard mode)

* Recording specification values are for the LINE input (analog)

3. Input jacks

Optical digital input jack
 Coaxial digital input jack
 Audio LINE input jack
 Control IN jack

4. Output jacks

Optical digital output jack
 Coaxial digital output jack
 Audio LINE output jack

5. Accessories

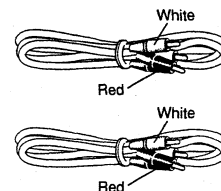
● Remote control unit 1
 ● Size AA/R6P dry cell batteries 2
 ● Audio cable 2
 ● AC power cord 1
 ● Operating Instructions 1

NOTE:

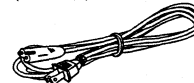
The specifications and design of this product are subject to change without notice, due to improvements.

■ Accessories

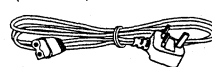
Two Sets of Audio Cords
 (PDE1249)(L = 1 m)



AC Power Cord (KU/CA Type)
 (ADG7021)



AC Power Cord (MV Type)
 (ADG7004)



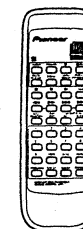
AC Power Cord (MY Type)
 (ADG1127)



Two "AA" size R6P Batteries
 (VEM-013)



Remote Control Unit
 CU-PD114 (PWW1163)



Service Manual

Pioneer

ORDER NO.
RRV2276

COMPACT DISC RECORDER

PDR-509

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Type	Model	Power Requirement	Remarks
	PDR-509		
MYXJ/2	○	AC220~230V	

● This service manual should be used together with the following manual(s):

Model No.	Order No.	Remarks
PDR-509/MY	RRV2167	
PDR-509	RRV2055	Service guide

- PDR-509 has three models which specifications are different. Each distinction will be confirmed with the indication of the rear base and the packing case. Refer to “ Confirm it ”.

CONTENTS

1. CONTRAST OF MISCELLANEOUS PARTS	2
2. SCHEMATIC DIAGRAM	6
3. PCB CONNECTION DIAGRAM	20
4. ADJUSTMENT	22
Confirm it	23

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PIONEER ELECTRONICS ASIACENTRE PTE. LTD. 253 Alexandra Road, #04-01, Singapore 159936
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Printed in Belgium

1. CONTRAST OF MISCELLANEOUS PARTS

NOTES : ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
● The Δ mark found on some component parts indicates the importance of the safety factor of the part.
Therefore, when replacing, be sure to use parts of identical designation.
● Reference Nos. indicate the pages and Nos. in the service manual for the base model.
● When ordering resistors, first convert resistance values into code form as shown in the following examples.
Ex. 1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J = 5%, and K = 10%).
560 Ω \rightarrow $56 \times 10^1 \rightarrow 561$ RD1/4PU $\begin{bmatrix} 5 & 6 & 1 \end{bmatrix} J$
47k Ω \rightarrow $47 \times 10^3 \rightarrow 473$ RD1/4PU $\begin{bmatrix} 4 & 7 & 3 \end{bmatrix} J$
0.5 Ω \rightarrow R50 RN2H $\begin{bmatrix} R & 5 & 0 \end{bmatrix} K$
1 Ω \rightarrow 1R0 RS1P $\begin{bmatrix} 1 & R & 0 \end{bmatrix} K$
Ex. 2 When there are 3 effective digits (such as in high precision metal film resistors).
5.62k Ω \rightarrow $562 \times 10^1 \rightarrow 5621$ RN1/4PC $\begin{bmatrix} 5 & 6 & 2 & 1 \end{bmatrix} F$

■ CONTRAST TABLE

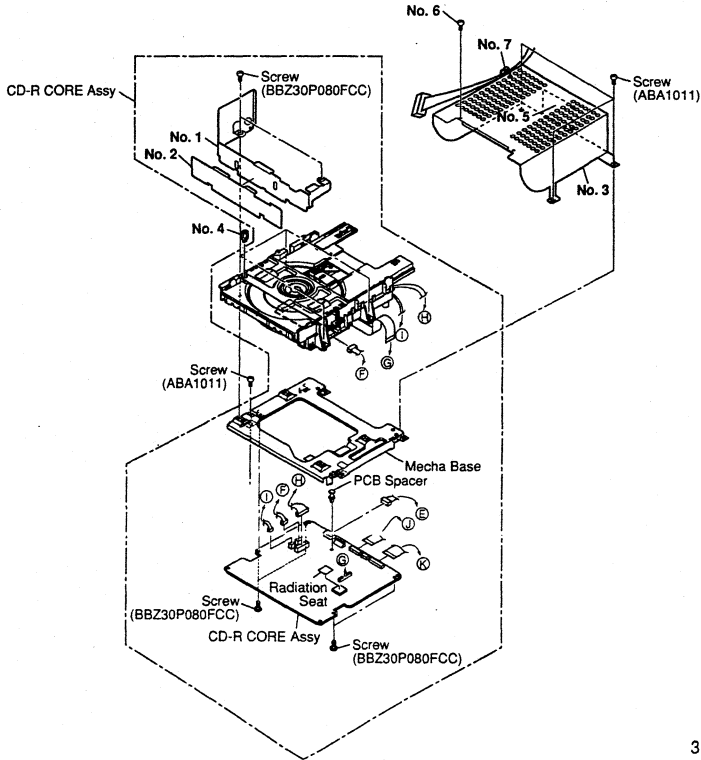
PDR-509/MYXJ/2 and PDR-509/MY are constructed the same except for the following:

Ref. No.	Mark	Symbol and Description	Part No.		Remarks
			MY type	MYXJ/2 type	
P6 - 1		PCB ASSEMBLIES CD-R CORE Assy	PYY1273	PYY1279	
P4 - 1	Δ	PACKING AC Power Cord	ADG1127	ADG1154	
P4 - 3		Audio Cable	PDE1249	RDE1036	
P4 - 9		Packing Case	PHG2383	PHG2394	
P4 -10		Protector (F)	RHA1238	PHA1339	
P4 -11		Protector (R)	RHA1239	PHA1340	
P4 -13		Operating Instructions (Dutch/Swedish/Spanish/Danish)	PRD1057	PRD1053	
P4 -14		Operating Instructions (English/French/German/Italian)	PRE1287	PRE1284	
P4 -16		Accessory Spacer	PHC1093	PHC1095	
P6 - 6	NSP	EXTERIOR SECTION CD-R CORE Assy	PXA1625	PXA1630	
P6 - 9		15P Flexible Cable/30V	PDD1197	PDD1200	
P6 -10		21P Flexible Cable/30V	PDD1198	PDD1201	
P6 -13		PCB Mold	AMR2115	AMR2534	
P6 -14		Bonnet	PYY1147	PYY1271	
P6 -16		Rear Base	PNA2526	PNA2542	
P6 -20		Power Button	RAC2207	PAC1941	
P6 -32		Radiation Sheet	PEB1305	PEB1306	
	NSP	Binder	PEC-107	Not used	
		Binder	Not used	ZCA-T18S	
	NSP	Shield Plate	Not used	PNB1621	No. 1
	NSP	Cover	Not used	PNM134R	No. 2
		Shield Case	Not used	PNB1622	No. 3
	NSP	PCB Spacer	Not used	PNY-404	No. 4
	NSP	Insulating Seal	Not used	PNM1350	No. 5
		Screw	Not used	IPZ30P120FMC	No. 6
	NSP	Binder	Not used	ZCA-T18S	No. 7

Ref. No.	Mark	Symbol and Description	Part No.		Remarks
			MY type	MYXJ/2 type	
P7 - 4		FRONT PANEL SECTION Front Panel	PNW2929	PNW2944	
P7 - 9		Manual Button L	PAC1974	PAC1988	
P7 -10		Manual Button R	PAC1975	PAC1989	
P7 -11		Mode Button	PAC1873	PAC1990	
P7 -12		Play Button	RAC2204	PAC2002	
P7 -19		JOG Knob	RAC2210	PAC1939	
P7 -24		19P Flexible Cable/60V	PDD1196	PDD1199	
P8 - 5		CD-R CORE ASSY (1/2) Screw	DBA1006	VBA1065	
P8 -10		Loading Base	VNL1844	VNL1854	
P8 -11		Tray	VNL1731	VNK4570	
P8 -22		Binder	PEC-107	Not used	
P8 -22	NSP	Binder	Not used	ZCA-T18S	
P8 -28	NSP	Earth Lead Unit	PDF1200	Not used	
P8 -29		Tray Holder	PNM1341	PNM1346	
P9 - 4		CD-R CORE ASSY (2/2) Float Rubber A	AEB7063	Not used	
P9 - 4		Float Rubber C	Not used	VEB1301	
P9 -21		CD-R Pickup	PEA1351	PEA1352	

● The numbers in the remarks column correspond to the numbers on the "EXPLODED VIEWS".
● For PCB ASSEMBLIES, Refer to "PCB PARTS LIST", "2. SCHEMATIC DIAGRAM" and "3. PCB CONNECTION DIAGRAM".

■ EXPLODED VIEWS
● EXTERIOR SECTION



■ PCB PARTS LIST

Mark	No.	Description	Part No.
------	-----	-------------	----------

AF CD-R CORE ASSY

SEMICONDUCTORS

△	IC502	AD1893JST
	IC101	AK8563
△	IC321	BA05FP
	IC142, IC143, IC304	BA10358F
	IC561	BA7082F
	IC303 (BR93LC46F)	PYY1196
△	IC401	CXD2585Q
	IC141, IC161, IC255, IC508	HD74HC4053FP
	IC371	HD74HC573FP
	IC501	LC89585
	IC503	LH64256CK-70
	IC931	LP2980IMS-5.0
	IC451	M56788FP
	IC252-IC254	NJM2100M
	IC181	NJM2137M
	IC251	NJU7016M
△	IC201	PA9007A
	IC507	PCX1026
	IC431	PD9020A
	IC351	PDJ014A
	IC301	PE5147A
	IC932	PST994C
	IC505	TC74HC157AF
	IC362	TC7S00F
	IC363, IC562	TC7S04F
	IC305, IC509	TC7S08F
△	IC361, IC364	TC7S14F
	IC510	TC7S32F
	IC506	TC7SU04F
	IC302	TK11041M-1
	Q452	2SK209
	Q102	DTA114TK
	Q204, Q401, Q402	DTA124EK
	Q551	DTA143EK
	Q101, Q142, Q201, Q203	DTC114TK
	Q103, Q141, Q301-Q304	DTC124EK
△	D101, D182, D201, D302	1SS355
	D321, D322, D361, D362, D452	1SS355
	D102, D181, D202, D251	DA204K
	D551	DAN202K
	D301	DAP202K
	D141, D252, D323	MA704
	D204	UDZ3.0B
COILS AND FILTERS		
△	L434, L505 CHIP COIL	DTL1058
	L101, L302, L305-L309	OTL1040
	L311-L315, L318-L320	OTL1040
	L322-L324, L351, L371	OTL1040
	L511-L513, L515-L518, L522	OTL1040
	L524, L526 CHIP SOLID INDUCTOR	OTL1040
	L568 CHIP SOLID INDUCTOR	QTL1015
	L1128, L1134 CHIP BEADS	VTL1099
	L1151, L1153 CHIP BEADS	VTL1099
	F401, F403, F432, F433	VTF1097

Mark	No.	Description	Part No.
------	-----	-------------	----------

F501-F504, F506-F508, F510	VTF1097
F514, F520, F521, F527, F533	VTF1097
F551, F552, F561, F571	VTF1097
CHIP SOLID INDUCTOR	

CAPACITORS

C122, C185	CCSQCH100D50
C159, C434, C551, C564, C1225	CCSQCH101J50
C507, C508	CCSQCH150J50
C563	CCSQCH160J50
C406	CCSQCH221J50
C107, C404	CCSQCH331J50
C263, C311	CCSQCH470J50
C213, C256, C408, C526	CCSQCH471J50
C518	CCSQCH820J50
C183, C184	CCSQCH330C50
C480-C482	CCSQSL471J50
C321, C465, C931	CEAL100M50
C121, C126, C327, C401, C409	CEAL101M6R3
C417, C901, C902	CEAL101M6R3
C307	CEAL1R0M50
C216	CEAL2R2M50
C933	CEAL3R3M50
C181, C452, C477, C903	CEAL470M16
C130, C201, C209, C211, C253	CEAL470M6R3
C323, C368, C431, C502, C512	CEAL470M6R3
C515, C520, C553, C559, C566	CEAL470M6R3
C308	CEAL4R7M50
C207, C233, C252, C257, C403	CKSQYB102K50
C407, C501, C517	CKSQYB102K50
C109, C110, C112, C206	CKSQYB103K50
C226, C230, C231, C251, C324	CKSQYB103K50
C412, C454-C459, C470-C473	CKSQYB103K50
C505, C510, C558, C567, C934	CKSQYB104K25
C142, C151, C182, C224, C225	CKSQYB104K25
C361, C364, C453, C476, C504	CKSQYB104K25
C141, C932	CKSQYB104K25
C105, C116, C127, C232, C258	CKSQYB105K10
C514, C557	CKSQYB105K10
C208, C414	CKSQYB152K50
C155, C156, C203	CKSQYB223K50
C113-C115, C117, C118	CKSQYB224K16
C304, C305, C460, C462	CKSQYB224K16
C212, C451	CKSQYB272K50
C104	CKSQYB331K50
C103	CKSQYB333K50
C131, C215, C227-C229	CKSQYB334K16
C363, C474, C475, C1351	CKSQYB471K50
C413	CKSQYB473K25
C102	CKSQYB473K50
C119, C204, C468, C469	CKSQYB681K50
C463, C464	CKSQYB682K50
C205	CKSQYB683K25
C133, C144, C254, C303	CKSQYF103Z50
C322, C362, C416, C433, C519	CKSQYF103Z50
C521, C555, C561, C562	CKSQYF103Z50

Mark	No.	Description	Part No.
------	-----	-------------	----------

C568, C569, C905	CKSQYF103Z50
C106, C108, C111, C120, C123	CKSQYF104Z25
C125, C129, C132, C143, C152	CKSQYF104Z25
C154, C158, C161, C202, C210	CKSQYF104Z25
C214, C217, C255, C260-C262	CKSQYF104Z25
C301, C302, C306, C309, C310	CKSQYF104Z25
C312, C313, C326, C365-C367	CKSQYF104Z25
C369, C371, C402, C405	CKSQYF104Z25
C410, C411, C415, C432	CKSQYF104Z25
C466, C467, C503, C506, C509	CKSQYF104Z25
C511, C513, C516, C523, C524	CKSQYF104Z25
C552, C554, C560, C565, C572	CKSQYF104Z25
C522, C525	CKSQYF104Z50
C124, C328, C570, C904	CKSQYF473Z25
C325 (1F/5.5V)	VCH1039

RESISTORS

R113, R441, R442, R514 (330Ω)	ACN7054
R308, R417 (470Ω)	ACN7056
R313, R570 (47kΩ)	ACN7077
R401, R416, R433 (100Ω)	DCN1092
R507 (220Ω)	DCN1093
R371, R372, R501 (10kΩ)	DCN1094
R181 (4.7kΩ)	DCN1100
R143 (2.2kΩ)	PCN1039
R468, R470, R476, R478	RN1/10SE1002D
R477, R479	RN1/10SE1202D
R464, R466	RN1/10SE2202D
R469, R471	RN1/10SE5102D
R465, R467	RN1/10SK2003D
VR101, VR161-VR164 (4.7kΩ)	VCP1154
VR141 (22kΩ)	VCP1158

Other Resistors	RS1/10SC00J
-----------------	-------------

OTHERS

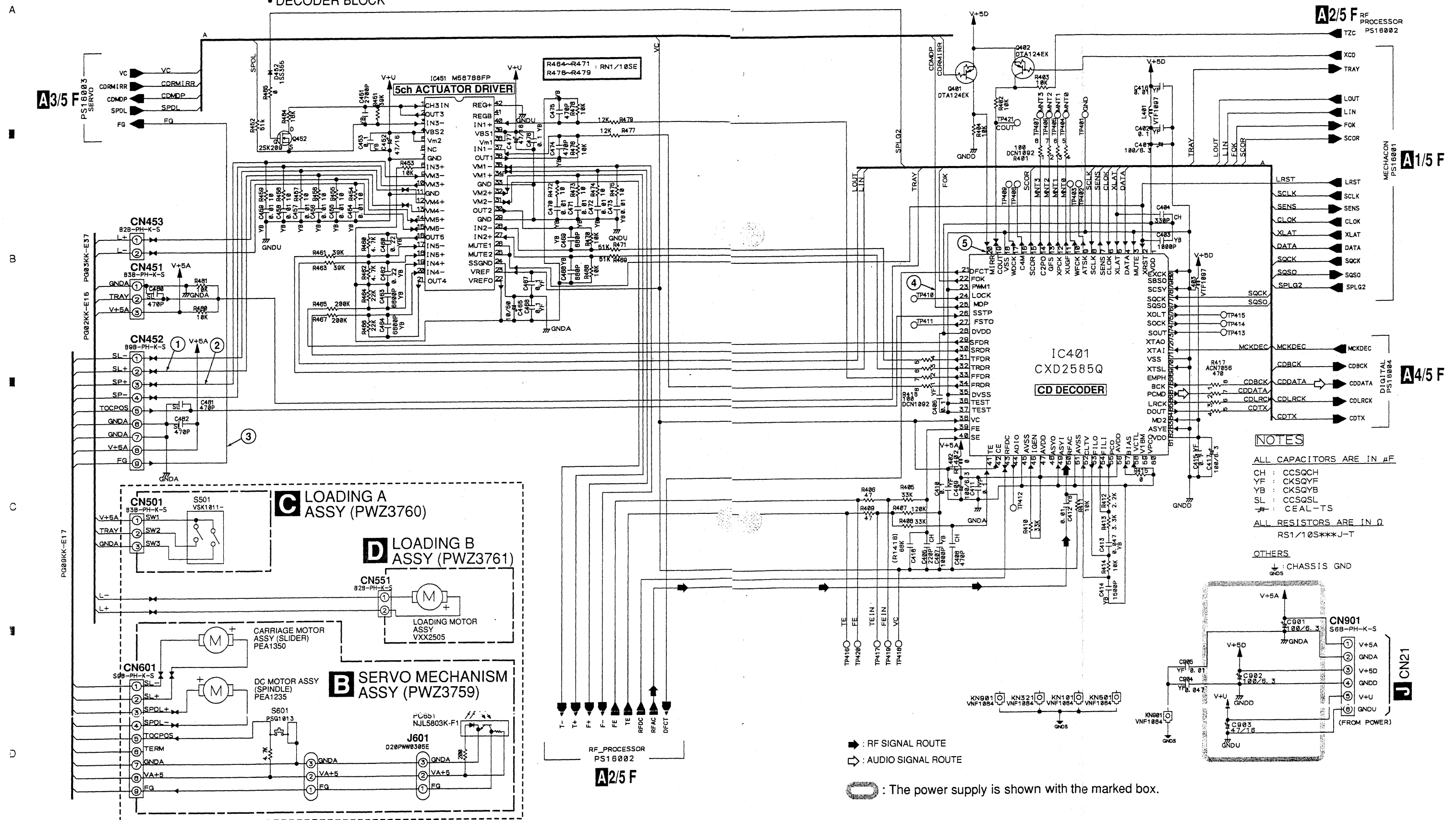
CN453	KR CONNECTOR 2P	B2B-PH-K-S
CN451	KR CONNECTOR 3P	B3B-PH-K-S
CN901	KR CONNECTOR 6P	S6B-PH-K-S
CN502	8P FFC CONNECTOR	VKN1268
CN301	15P FFC CONNECTOR	VKN1275
CN501	21P FFC CONNECTOR	VKN1281
CN101	32P FFC CONNECTOR	VKN1463
KN101, KN321, KN501, KN901	EARTH METAL FITTING	VNF1084
X501	CRYSTAL (16.9344MHz)	PSS1008
X301	CERAMIC (32MHz)	PSS1023

2.1 CD-R CORE ASSY (1/5)

- MECHA. CONTROL μ COM BLOCK



A 1/5F 7
8



VOLTAGES and WAVEFORMS

A1/5 F CD-R CORE ASSY

Media	Pickup Position	DGAI (IC351-pin56)	D8CM (IC351-pin57)
CD	12cm Inner	0V	0V
	12cm Outer	5V	0V
	8cm Inner	0V	0V
	8cm Outer	5V	0V
CD-R CD-RW	12cm Inner	5V	0V
	12cm Outer	5V	0V
	8cm Inner	0V	5V
	8cm Outer	5V	5V

	at FS = 44 kHz (at FS Converter through)	Others
FSR/XTHR (CN301-pin 2)	0V	5V
	at DIGITAL LOCK	at DIGITAL UNLOCK
XVCO (CN301-pin3)	0V	5V
UNLOCK (CN301-pin7)	0V	5V
XPFAIL (CN301-pin4)	5V	-
XRST (CN301-pin11)	5V	-

A3/5 F CD-R CORE ASSY

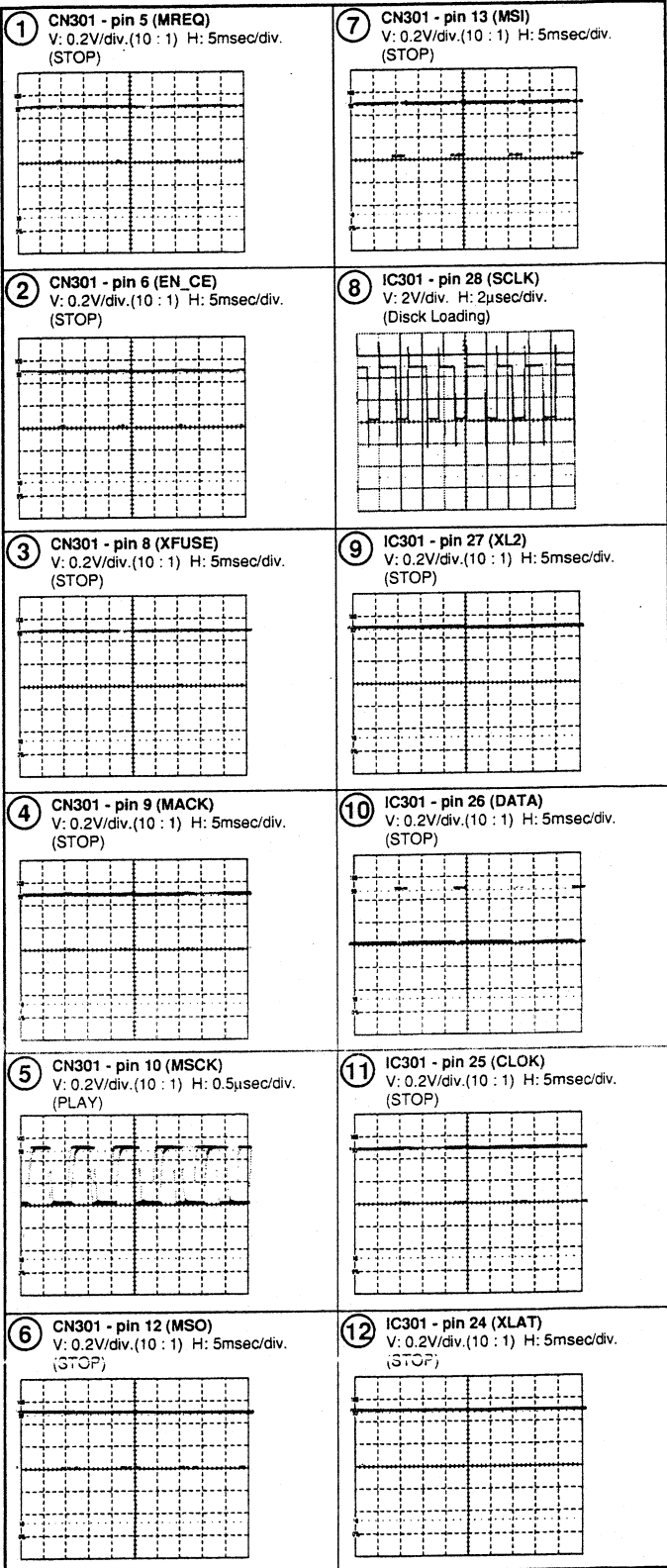
Operating Mode	CLV (IC201-pin13)	ECLV (IC201-pin14)
STOP	0V	0V
CAV	0V	5V
CLV	5V	0V
ECLV	5V	5V

A4/5 F CD-R CORE ASSY

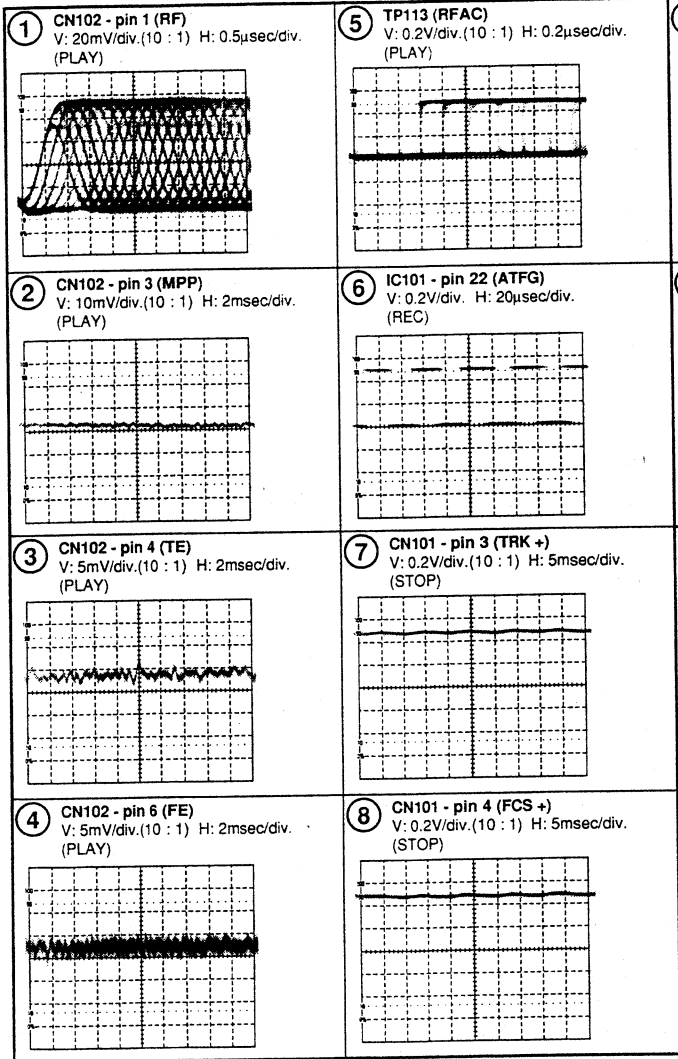
	A/D Converter used	
	at Analog REC Pause or REC	Others
ADSTBY (CN501-pin4)	0V	5V
	at MUTE ON (Audio Signal Not Output)	at MUTE OFF (Audio Signal Output)
AMUTE (CN501-pin9)	5V	0V
LRSTD (CN501-pin10)	5V	-

Note :
The encircled numbers denote measuring point in the schematic diagram.

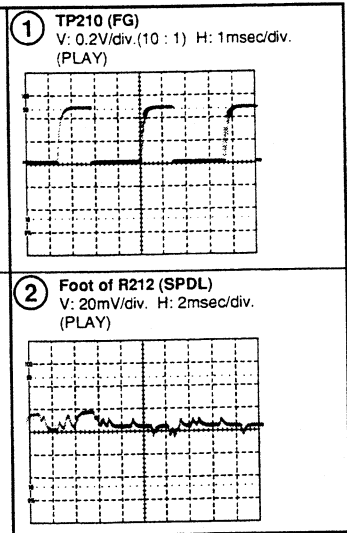
A1/5 F CD-R CORE ASSY

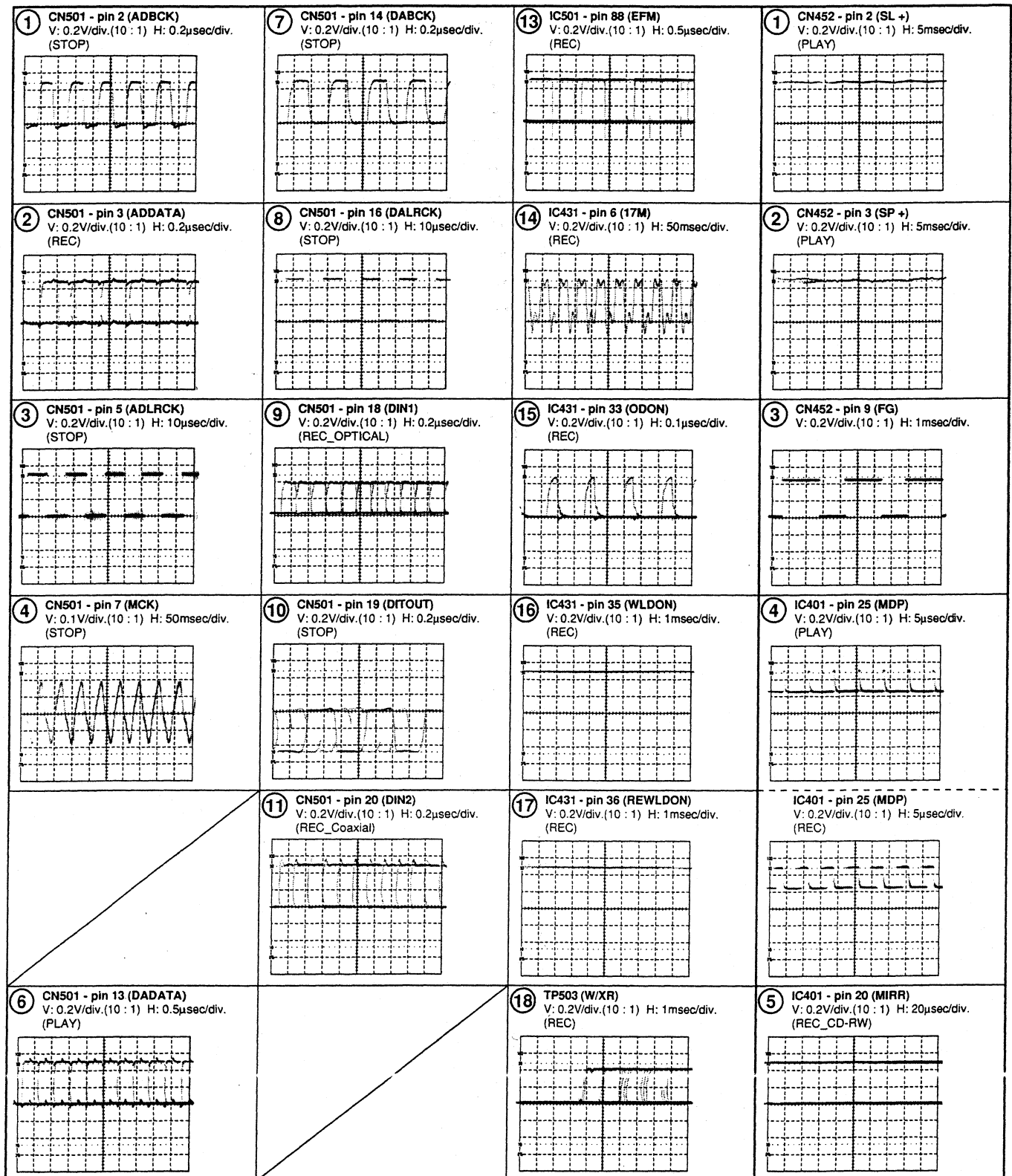


A2/5 F CD-R CORE ASSY



A3/5 F CD-R CORE ASSY



A4/5 F CD-R CORE ASSY**A5/5 F CD-R CORE ASSY**

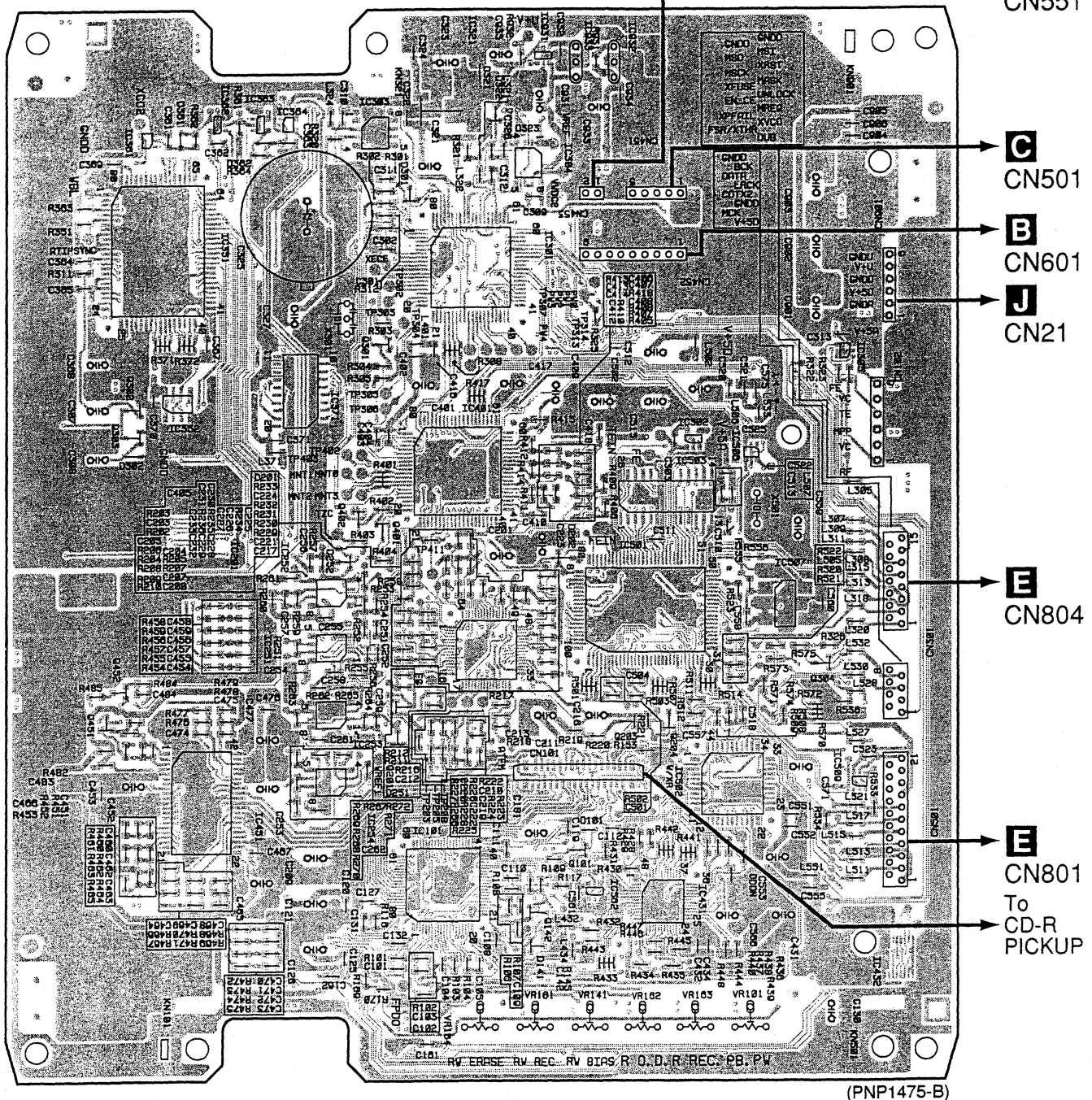
3. PCB CONNECTION DIAGRAM

3.1 CD-R CORE ASSY

• This diagram has four layers.

In the two middle layers, mainly Vcc and GND are Connected.

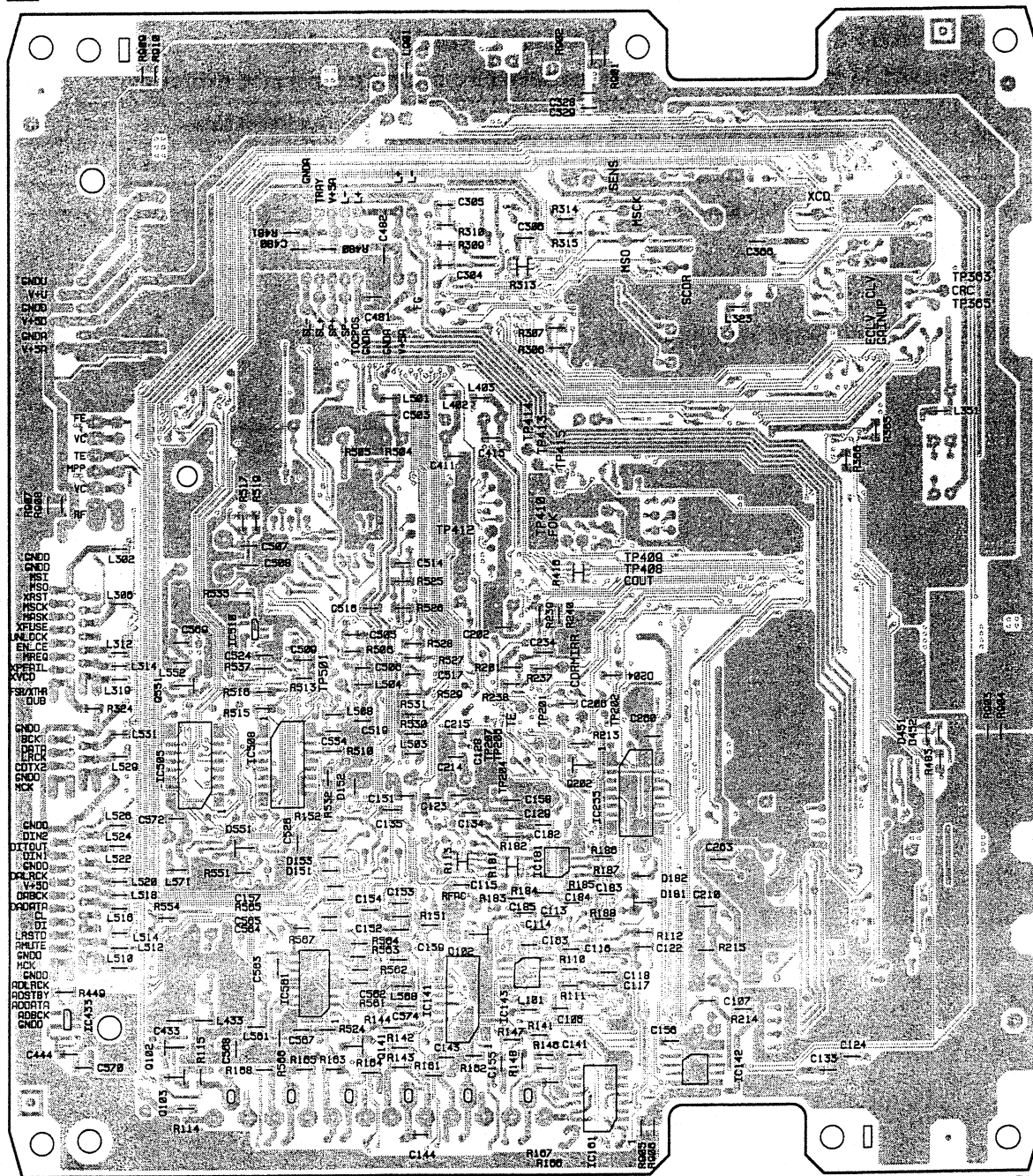
A F CD-R CORE ASSY



• This diagram has four layers.

In the two middle layers, mainly Vcc and GND are Connected.

AF CD-R CORE ASSY



(PNP1475-B)

Q551 IC505 IC508 Q102 Q103 IC561 Q141 IC141 IC143 IC181 IC161 IC142 Q202 IC255

SIDE B

AF 21

4. ADJUSTMENT

As for PDR-509/MYXJ/2 and PDR-509/MY, a part of adjustment value is different.
The adjustment method does not have the change.

■ CONTRAST TABLE OF ADJUSTMENT VALUE

PDR-509/MYXJ/2 and PDR-509/MY are constructed the same except for the following:

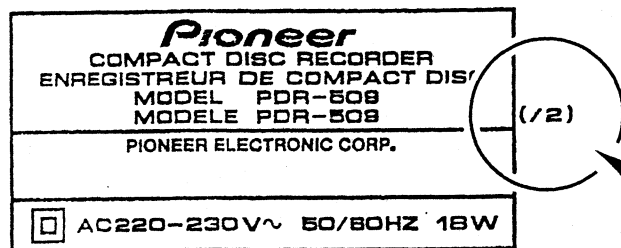
Page	Adjustment Item	Adjustment Value	
		MY type	MYXJ/2 type
P53	Playback Power Adjustment	0.60 mW \pm 0.05 mW	0.90 mW \pm 0.05 mW
P54	CD-R Record Power Adjustment	VR603 : 4.60 mW \pm 0.1 mW	VR603 : 4.80 mW \pm 0.1 mW
P54	CD-RW Record Power Adjustment	VR141 : 0.40 mW \pm 0.05 mW VR161 : 2.00 mW \pm 0.1 mW VR164 : 5.70 mW \pm 0.1 mW	VR141 : 0.60 mW \pm 0.05 mW VR161 : 1.30 mW \pm 0.1 mW VR164 : 5.60 mW \pm 0.1 mW

● "Page" in the table shows the adjustment item publishing page in the service manual for the base model.

Confirm it

- PDR-509 has three models which specifications are different. Each distinction will be confirmed with the indication of the rear base and the packing case. Refer to the following service manuals.

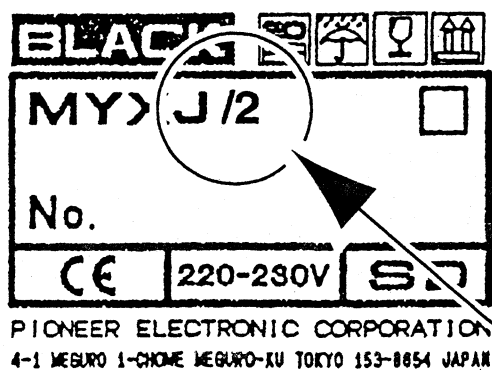
■ Rear View (Rear Base)



" (/2) " : RRV2276 [this manual]
(PDR-509/MYXJ/2)

" none " : RRV2167
(PDR-509/MY)
or
RRV2256
(PDR-509/MYXJ)

■ Packing Case



" MYXJ/2 " : RRV2276 [this manual]
(PDR-509/MYXJ/2)

" MY " : RRV2167
(PDR-509/MY)

" MYXJ " : RRV2256
(PDR-509/MYXJ)